

N-Channel 600-V (D-S) MOSFET

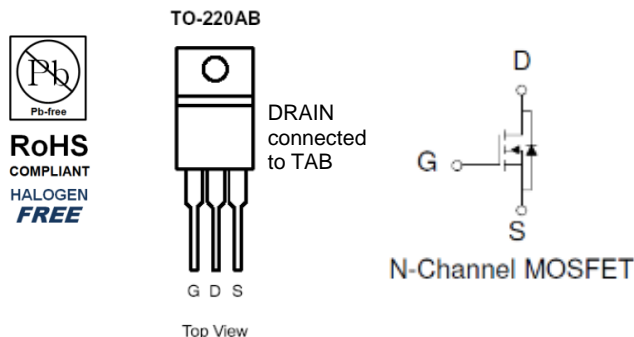
Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
600	186 @ $V_{GS} = 10V$	60 ^a
	194 @ $V_{GS} = 6.5V$	



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_C = 25^\circ\text{C}$	I_D	60	A
Pulsed Drain Current ^b		I_{DM}	240	
Continuous Source Current (Diode Conduction) ^a	$T_C = 25^\circ\text{C}$	I_S	40	A
Power Dissipation ^a	$T_C = 25^\circ\text{C}$	P_D	300	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^c	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	

Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

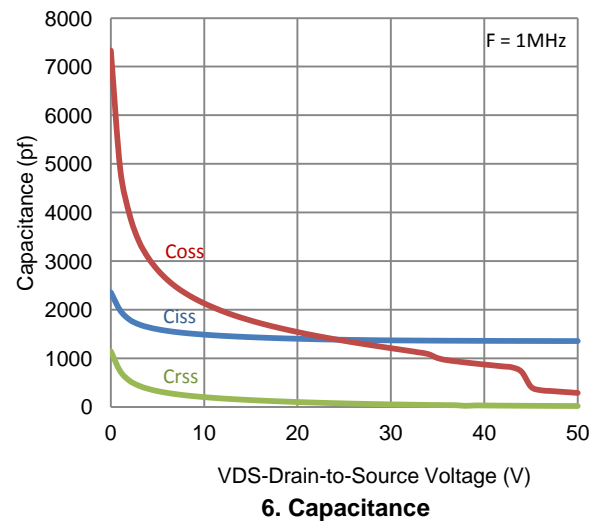
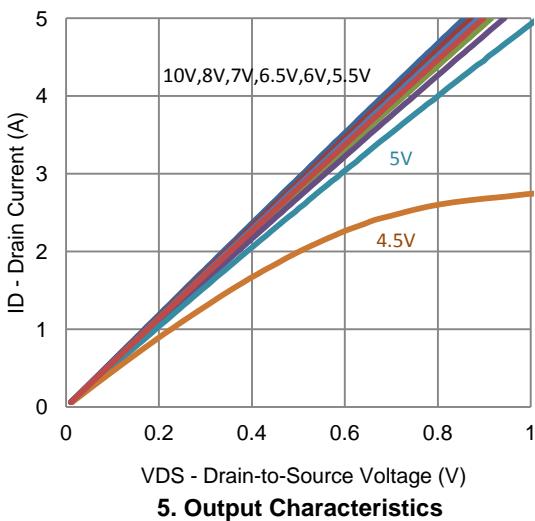
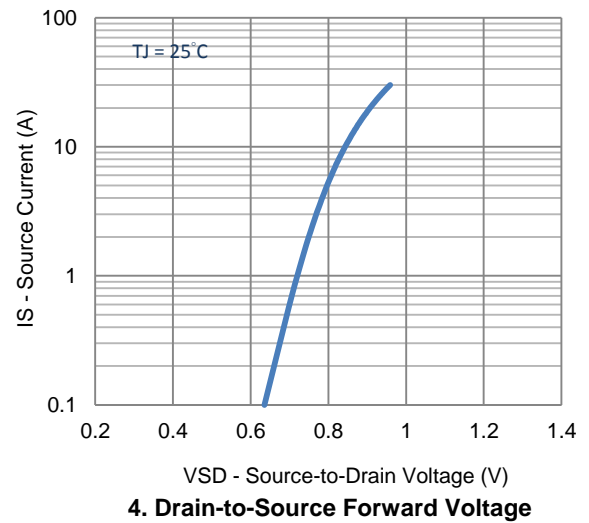
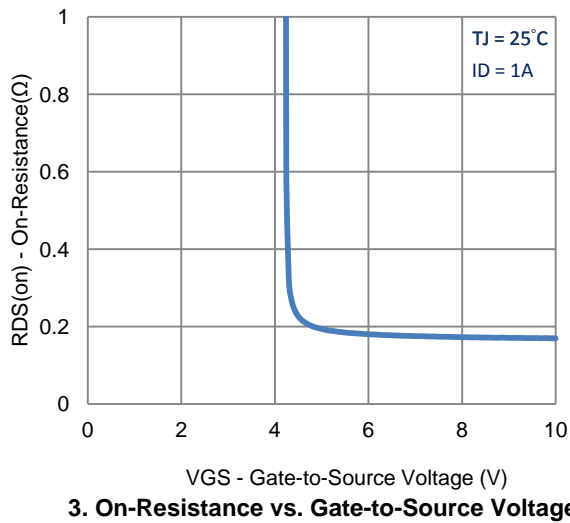
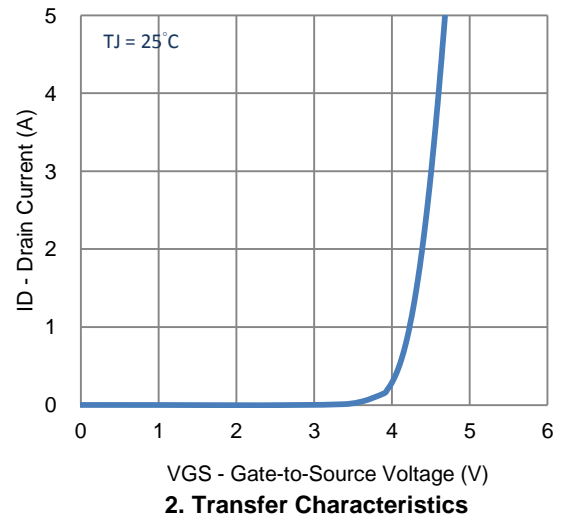
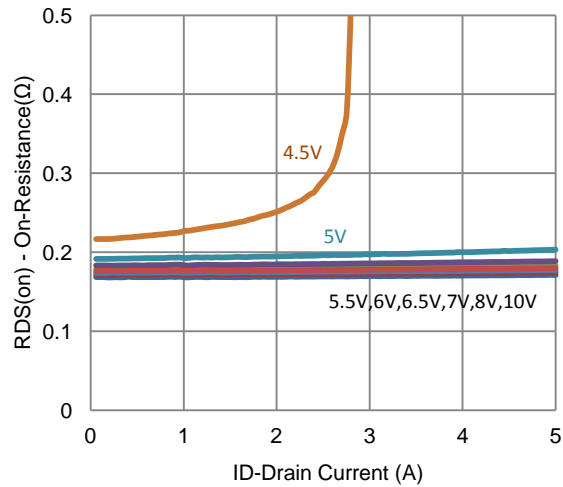
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 480 V$, $V_{GS} = 0 V$			1	μA
		$V_{DS} = 480 V$, $V_{GS} = 0 V$, $T_J = 55^\circ C$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 V$, $V_{GS} = 10 V$	75			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10 V$, $I_D = 10 A$			186	m Ω
		$V_{GS} = 6.5 V$, $I_D = 8 A$			194	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 50 V$, $I_D = 10 A$		18		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 20 A$, $V_{GS} = 0 V$		0.9		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 100 V$, $V_{GS} = 6.5 V$, $I_D = 1 A$		22		nC
Gate-Source Charge	Q_{gs}			6.3		
Gate-Drain Charge	Q_{gd}			11		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 100 V$, $R_L = 100 \Omega$, $I_D = 1 A$, $V_{GEN} = 10 V$, $R_{GEN} = 6 \Omega$		13		ns
Rise Time	t_r			14		
Turn-Off Delay Time	$t_{d(off)}$			116		
Fall Time	t_f			89		
Input Capacitance	C_{iss}	$V_{DS} = 50 V$, $V_{GS} = 0 V$, $f = 1 Mhz$		1353		pF
Output Capacitance	C_{oss}			272		
Reverse Transfer Capacitance	C_{rss}			15		

Notes

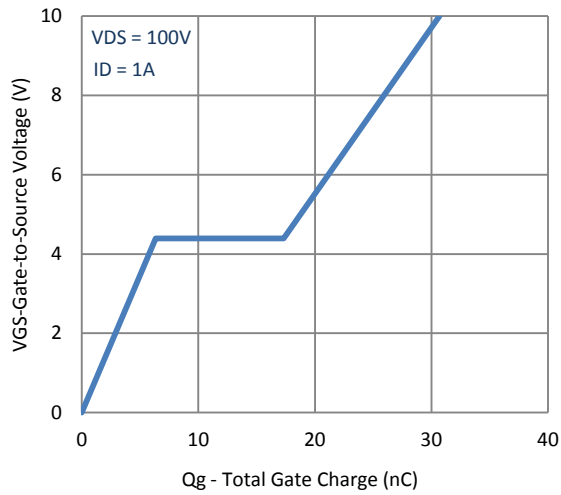
- a. Pulse test: $PW \leq 300 \mu s$ duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

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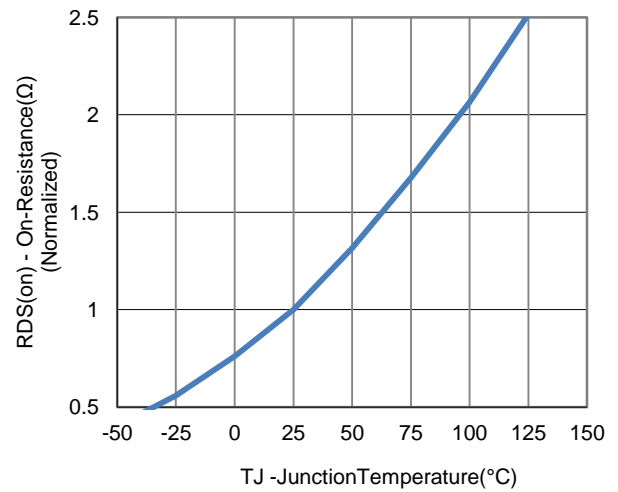
Typical Electrical Characteristics



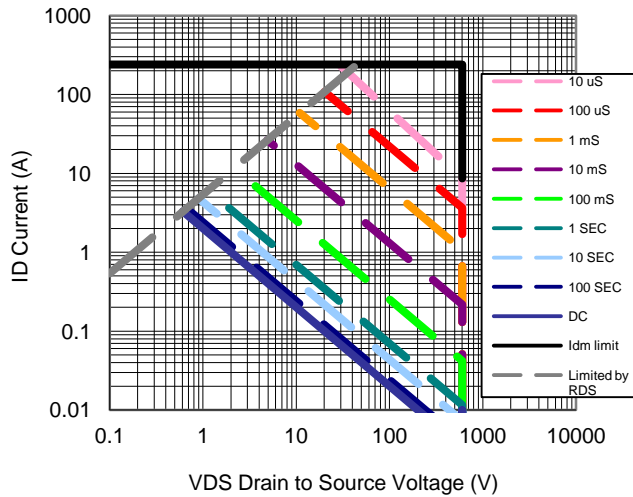
Typical Electrical Characteristics



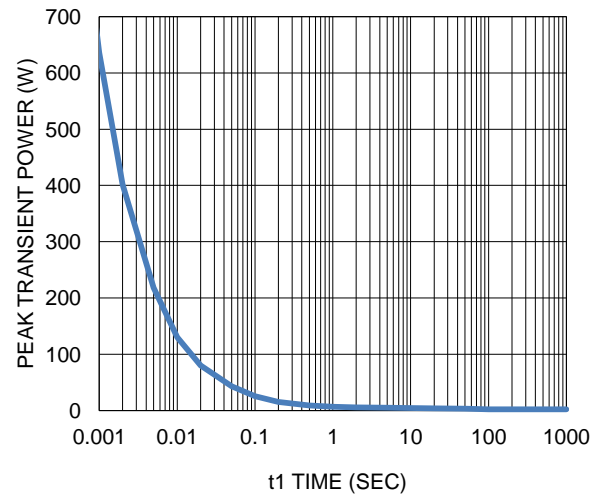
7. Gate Charge



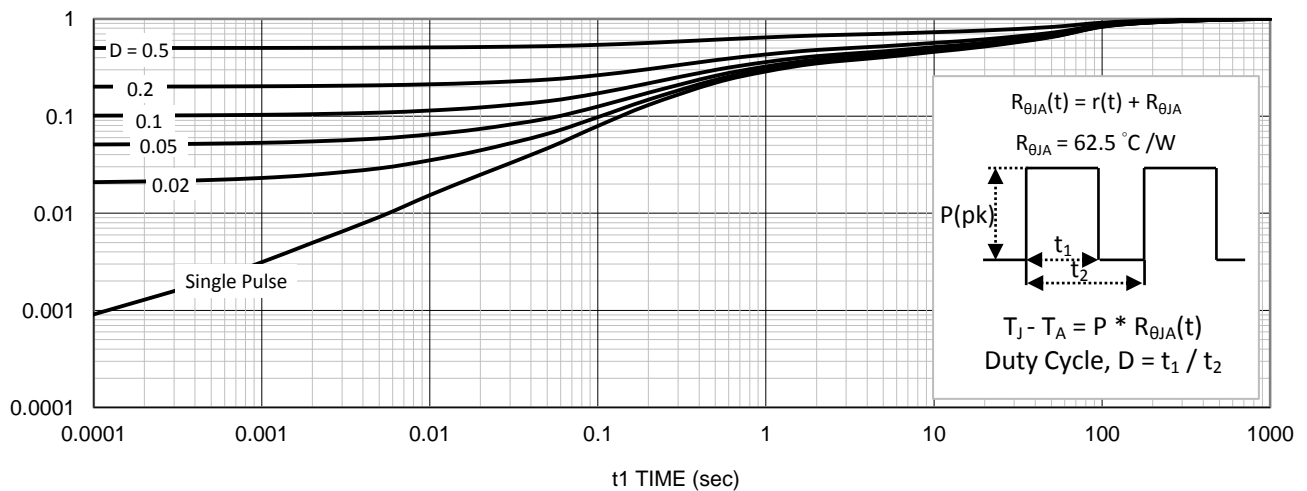
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

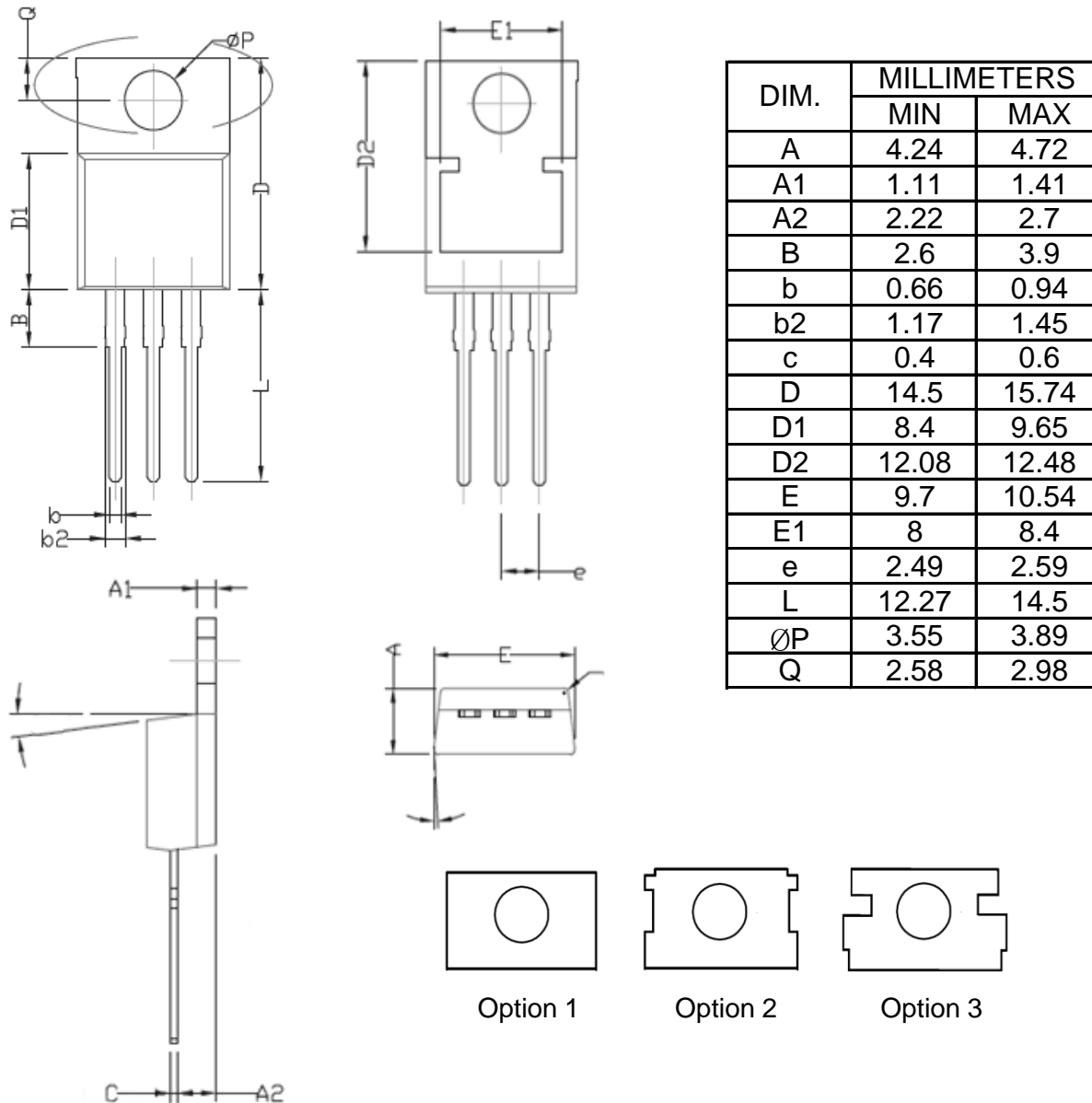


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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