N-Channel 20-V (D-S) MOSFET

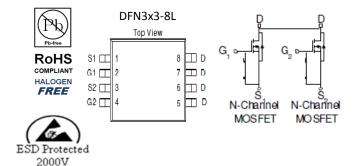
Key Features:

- Low r_{DS(on)} trench technology
- Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- · Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□(A)		
20	21 @ V _{GS} = 4.5V	8.2		
20	24 @ V _{GS} = 2.5V	7.5		



ABSOLUTE MAXIMUM RATINGS (T _A = 25 ℃ UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Limit	Units				
Drain-Source Voltage	V_{DS}	20	V				
Gate-Source Voltage		V_{GS}	±8				
Continuous Drain Current ^a	T _A =25 ℃	I _D	8.2				
Continuous Drain Current	T _A =70 ℃		6	Α			
Pulsed Drain Current ^b		I _{DM}	40				
Continuous Source Current (Diode Conduction) a		I _S	2.1	Α			
Dower Discipation ^a	T _A =25 ℃	P_{D}	1.5	· w			
Power Dissipation ^a	T _A =70 ℃	' D	0.8				
Operating Junction and Storage Temperature Range		T_J,T_stg	-55 to 150	$^{\circ}$			

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	83	°C/W			
IMAXIMUM JUNCTION-TO-AMBIENT	Steady State	ιιθJA	120				

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

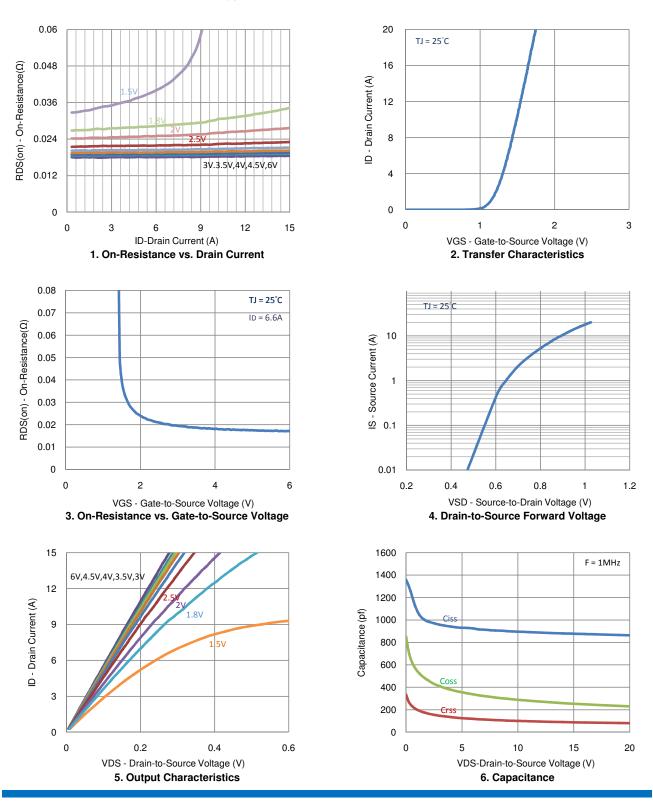
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$ 0.4				V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	l	V _{DS} = 16 V, V _{GS} = 0 V			1	uA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			25		
On-State Drain Current	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	15			Α	
Drain-Source On-Resistance	r	$V_{GS} = 4.5 \text{ V}, I_D = 6.6 \text{ A}$		16	21	mΩ	
Dialii-Source Oil-nesistance	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 6 \text{ A}$		20	24		
Forward Transconductance	g _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 6.6 \text{ A}$		25		S	
Diode Forward Voltage	V_{SD}	I _S = 1.1 A, V _{GS} = 0 V		0.68		V	
		Dynamic					
Total Gate Charge	Q_g	V 10 V V 45 V		11		nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 6.6 \text{ A}$		2.7			
Gate-Drain Charge	Q_{gd}	1D = 0.0 A		2.1		1	
Turn-On Delay Time	$t_{d(on)}$	V 10 V D 10 O		57			
Rise Time	t _r	$V_{DS} = 10 \text{ V}, R_{L} = 1.6 \Omega,$ $I_{D} = 6.6 \text{ A}.$		87			
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		604		ns	
Fall Time	t _f	V GEN = 4.5 V, 1 GEN = 0 12		198			
Input Capacitance	C _{iss}			877			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		88		pF	
Reverse Transfer Capacitance	C_{rss}			254			

Notes

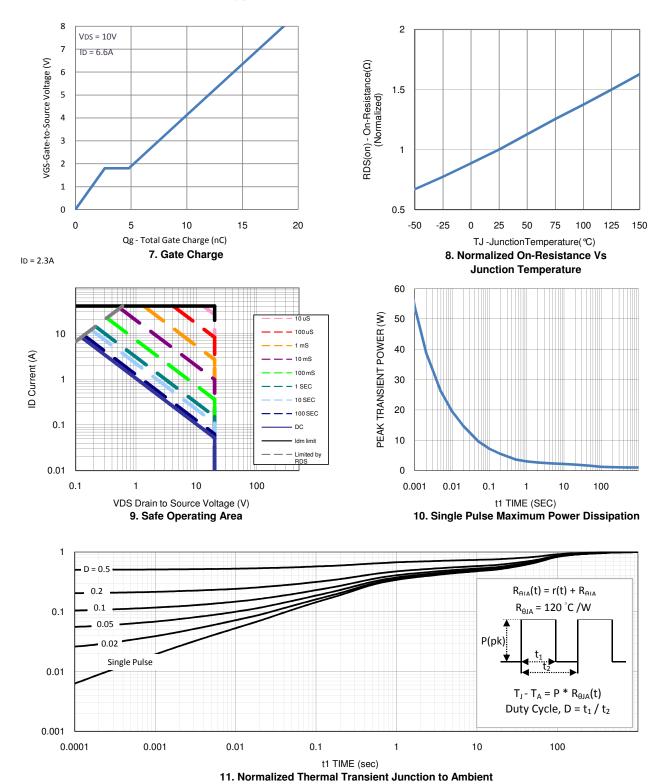
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

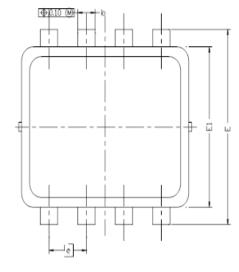
Typical Electrical Characteristics

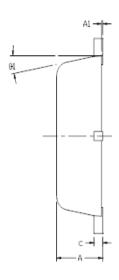


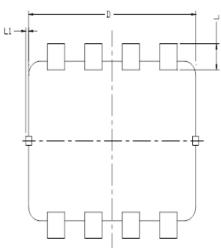
Typical Electrical Characteristics



Package Information







DIM.	MILLIMETERS			INCHES			
	MIN	NDM	MAX	MIN	NDM	MAX	
Α	0.700	0.80	0.900	0.0276	0.0315	0.0354	
A1	0.00		0,05	0,000		0,002	
b	0.24	0,30	0,35	0.009	0.012	0.014	
_	0.08	0.152	0.25	0.003	0,006	0.010	
D	2.90 BSC			0.114 BSC			
E	2	2.80 BSC			0.110 BSC		
E1	2.30 BSC			0.091 BSC			
е	0.65 BSC			0.026 BSC			
L	0.20	0.375	0.450	0.008	0.0148	0.0177	
L1	0		0.100	0		0.004	
91	0	10	12	0	10	12	