

P-Channel 25-V (D-S) MOSFET

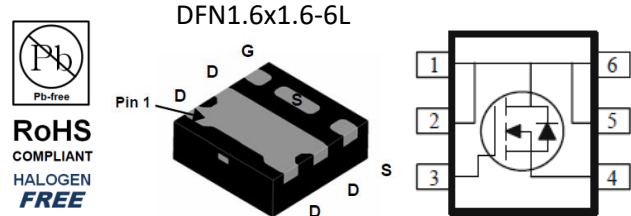
Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- Load Switches
- DC/DC Conversion
- Motor Drives

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
-25	58 @ $V_{GS} = -4.5V$	-10.7
	79 @ $V_{GS} = -2.5V$	-9.2



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	-25	V
Gate-Source Voltage		V_{GS}	± 8	
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	-10.7	A
	$T_C = 70^\circ\text{C}$		-8.6	
	$T_A = 25^\circ\text{C}$		-4.8 ^a	
	$T_A = 70^\circ\text{C}$		-3.9 ^a	
Pulsed Drain Current ^b		I_{DM}	-20	A
Continuous Source Current (Diode Conduction) ^a		I_S	-1.6	
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	9.9	W
	$T_C = 70^\circ\text{C}$		6.4	
	$T_A = 25^\circ\text{C}$		2.0 ^a	
	$T_A = 70^\circ\text{C}$		1.3 ^a	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
	Steady State		110	
Maximum Junction-to-Case	Steady State	$R_{\theta JC}$	13	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature
- Package limited

Electrical Characteristics

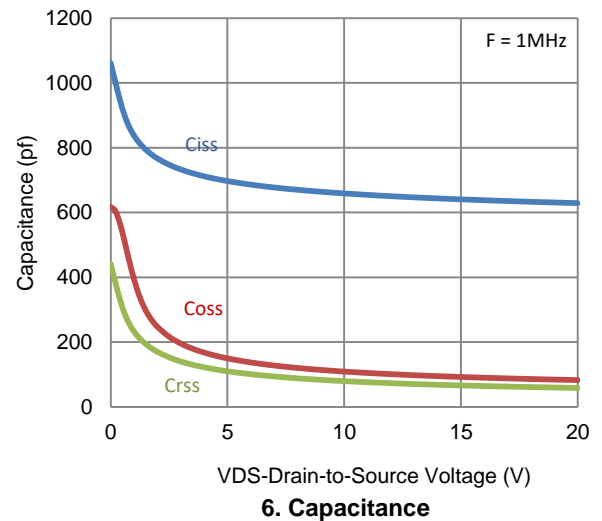
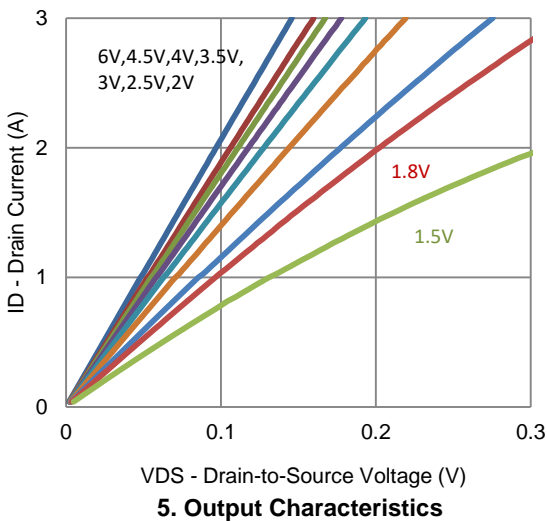
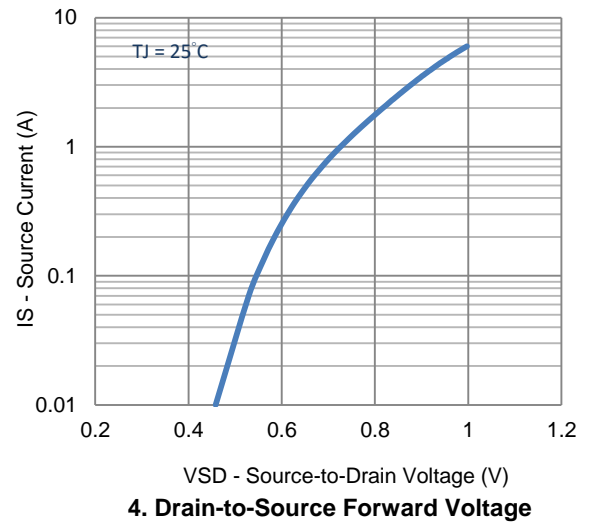
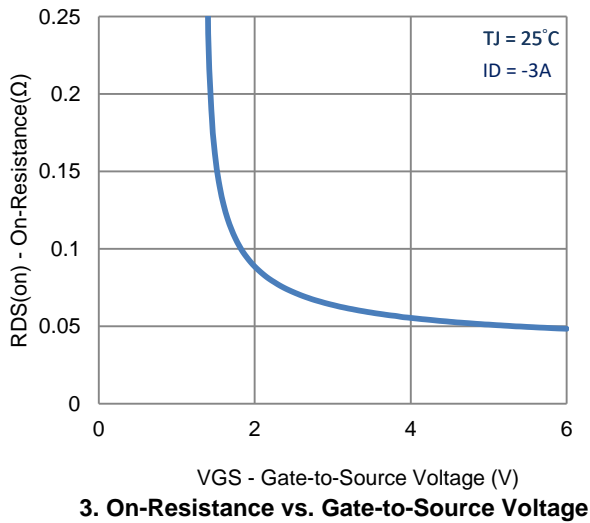
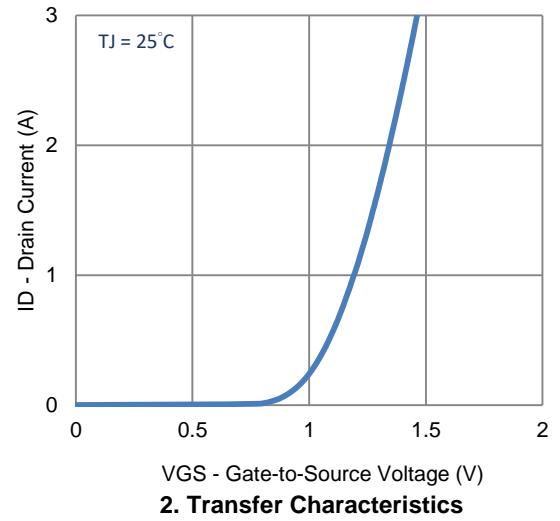
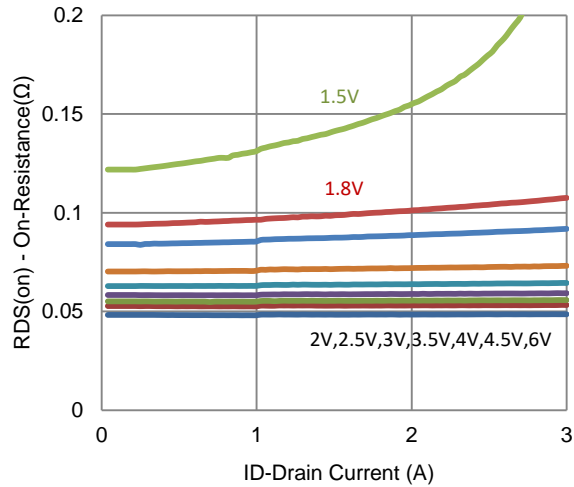
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20 V, V_{GS} = 0 V$			-1	μA
		$V_{DS} = -20 V, V_{GS} = 0 V, T_J = 55^\circ C$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -4.5 V$	-6			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5 V, I_D = -3 A$			58	m Ω
		$V_{GS} = -2.5 V, I_D = -2 A$			79	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 V, I_D = -3 A$		9		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -0.8 A, V_{GS} = 0 V$		-0.7		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = -12.5 V, V_{GS} = 4.5 V,$ $I_D = -3 A$		11		nC
Gate-Source Charge	Q_{gs}			1.2		
Gate-Drain Charge	Q_{gd}			3.0		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -12.5 V, R_L = 4.2 \Omega,$ $I_D = -3 A,$ $V_{GEN} = 4.5 V, R_{GEN} = 6 \Omega$		12		ns
Rise Time	t_r			17		
Turn-Off Delay Time	$t_{d(off)}$			54		
Fall Time	t_f			29		
Input Capacitance	C_{iss}	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 \text{ Mhz}$		640		pF
Output Capacitance	C_{oss}			92		
Reverse Transfer Capacitance	C_{rss}			66		

Notes

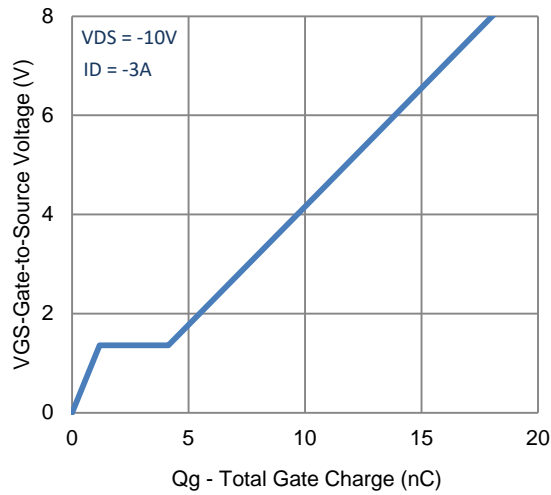
- a. Pulse test: $PW \leq 300 \mu s$ duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

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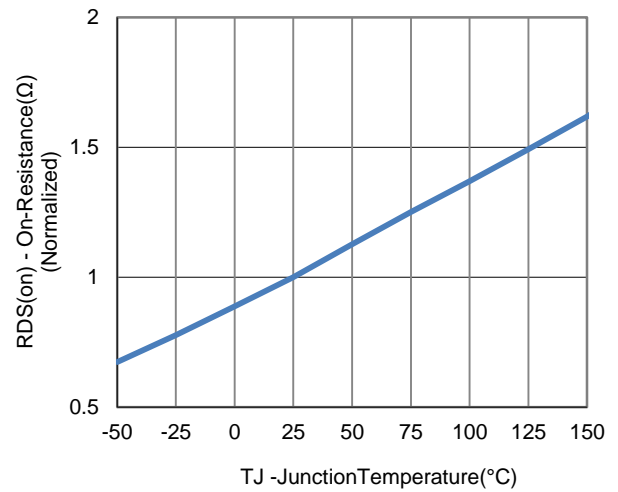
Typical Electrical Characteristics



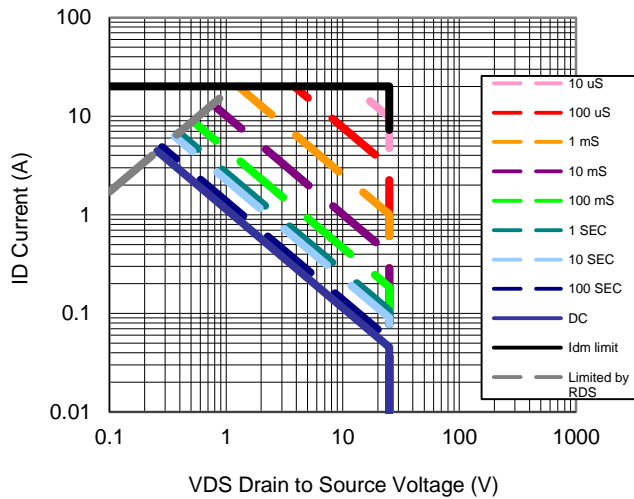
Typical Electrical Characteristics



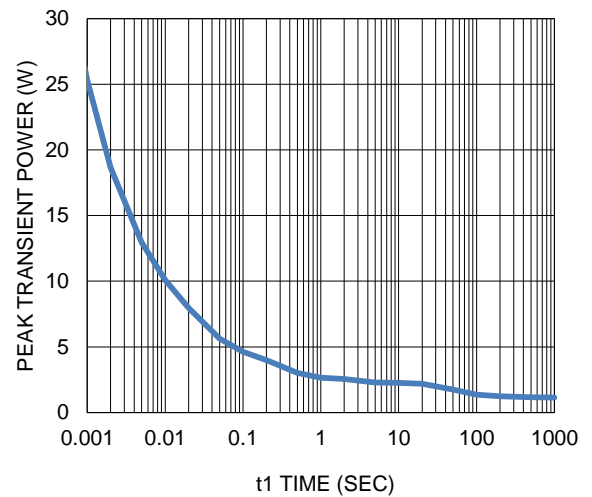
7. Gate Charge



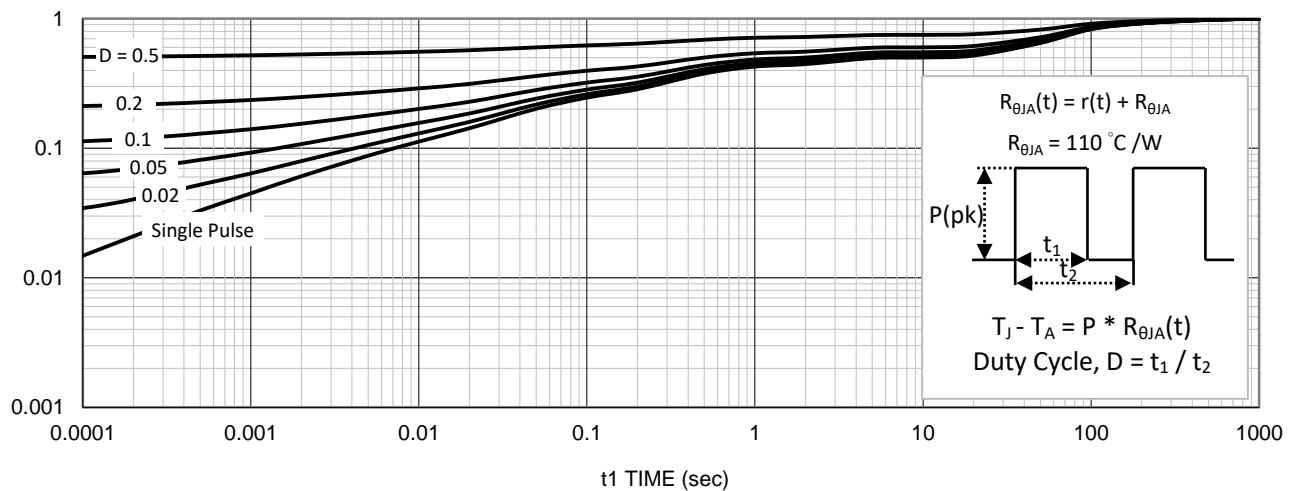
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

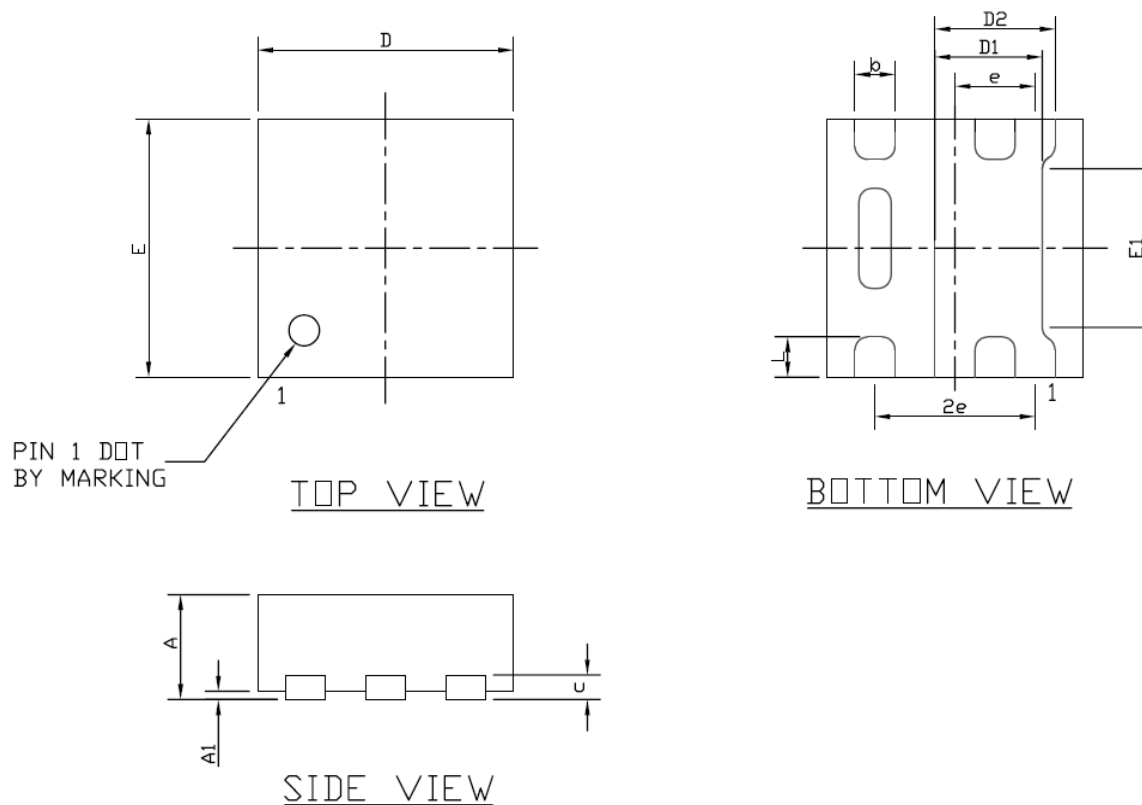


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	---	0.05	0.000	---	0.002
b	0.22	0.25	0.28	0.009	0.010	0.011
c	0.152 Ref.			0.006 Ref.		
D	1.55	1.60	1.65	0.061	0.063	0.065
D1	0.67 TYP			0.026 TYP		
D2	0.75 TYP			0.030 TYP		
E	1.55	1.60	1.65	0.061	0.063	0.065
E1	0.98 TYP			0.039 TYP		
e	0.50 BSC			0.020 BSC		
L	0.20	0.25	0.30	0.008	0.010	0.012