N-Channel 100-V (D-S) MOSFET

Key Features:

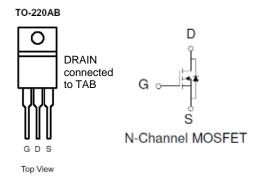
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _□ (A)	
100	12 @ V _{GS} = 10V	60 ^a	
	$17 @ V_{GS} = 6.5V$	60	





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			100	V		
Gate-Source Voltage			±20	V		
Continuous Drain Current a	T _C =25°C	I _D	60	А		
Pulsed Drain Current ^b	I _{DM}			^		
Continuous Source Current (Diode Conduction) ^a T _C =25°C		I _S	60	Α		
Power Dissipation ^a	T _C =25°C	P_{D}	300	W		
Operating Junction and Storage Temperature Range			-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV		

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

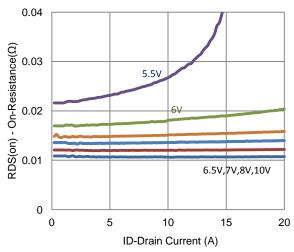
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
	I _{DSS}	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	75			Α
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			12	mΩ
	r _{DS(on)}	$V_{GS} = 6.5 \text{ V}, I_D = 18 \text{ A}$			17	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 50 \text{ V}, I_{D} = 20 \text{ A}$		35		S
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 30 \text{ A}, V_{GS} = 0 \text{ V}$		0.92		V
		Dynamic ^b				
Total Gate Charge	Q_g	$V_{DS} = 50 \text{ V}, V_{GS} = 6.5 \text{ V},$ $I_{D} = 2 \text{ A}$		19		nC
Gate-Source Charge	Q_gs			6.2		
Gate-Drain Charge	Q_gd			9.2		
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 50 \text{ V}, R_{L} = 25 \Omega,$ $I_{D} = 2 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		12		ns
Rise Time	t _r			13		
Turn-Off Delay Time	t _{d(off)}			25		
Fall Time	t _f			29		
Input Capacitance	C_{iss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		1160		
Output Capacitance	C _{oss}			206		pF
Reverse Transfer Capacitance	C_{rss}			14		

Notes

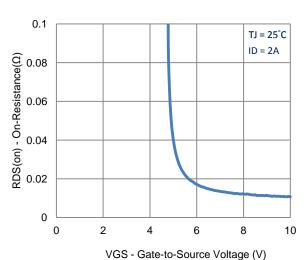
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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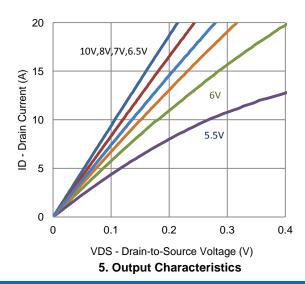
Typical Electrical Characteristics



1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage

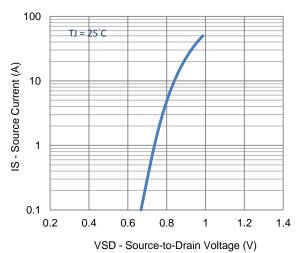


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TJ = 25°C

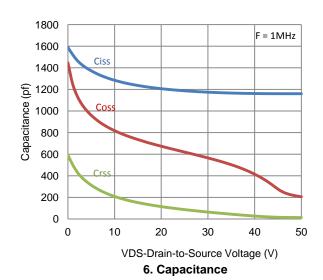
(Y) tuend 10
United 10
0 2 4 6 8

VGS - Gate-to-Source Voltage (V)

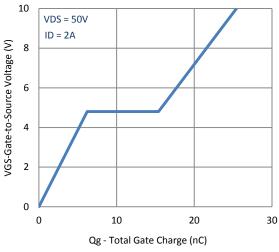
2. Transfer Characteristics



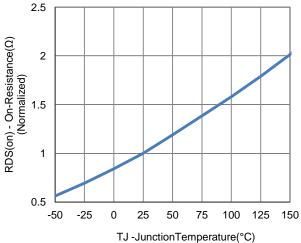
4. Drain-to-Source Forward Voltage



Typical Electrical Characteristics

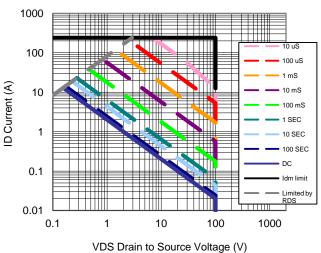


7. Gate Charge

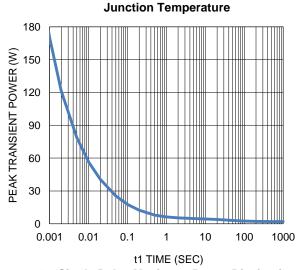


TJ -JunctionTemperature(°C)

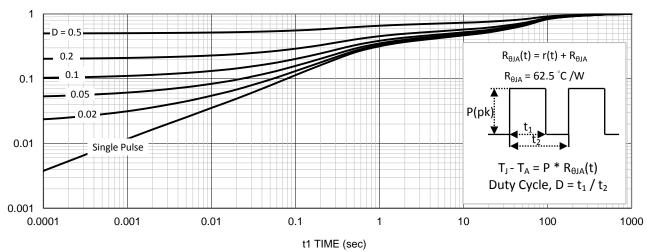
8. Normalized On-Resistance Vs



9. Safe Operating Area

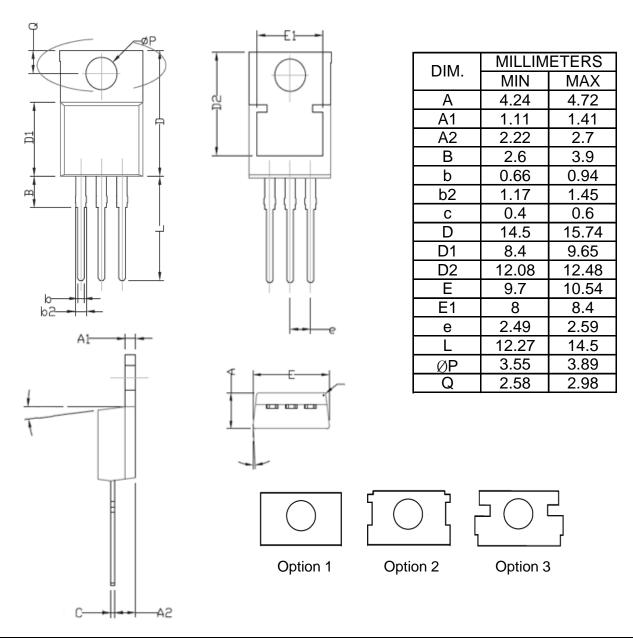


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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