Dual N-Channel 30-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

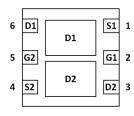
Typical	Applications:
----------------	---------------

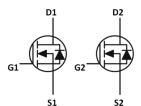
- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
30	63 @ V _{GS} = 4.5V	4.4		
	110 @ V _{GS} = 2.5V	3.4		



FREE





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter			Symbol	Limit	Units	
Drain-Source Voltage				30	W	
Gate-Source Voltage	Gate-Source Voltage				V	
		T _A =25°C		4.4		
		T _A =70°C	l _D	3.5	Α	
Pulsed Drain Current ^b				±10		
Continuous Source Current (Diode Conduction) a				2.2	Α	
Power Dissipation ^a		T _A =25°C	P _D	2.1	W	
		T _A =70°C	ı D	1.3	V V	
Operating Junction and Storage Temperature Range			T_J, T_{sta}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	60	°C/W			
Maximum Junction-to-Ambient	Steady State	IΛθJA	110	C/VV			

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

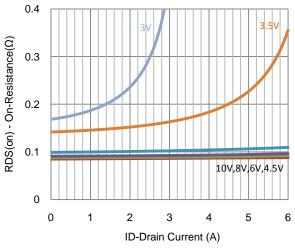
Parameter	Symbol	Test Conditions		Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	0.7		3	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current	1	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA		
Zelo Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10			
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α		
Drain-Source On-Resistance	r	$V_{GS} = 4.5 \text{ V}, I_D = 3.5 \text{ A}$			63	mΩ		
Dialii-Source Oil-Nesistance	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 2.7 \text{ A}$			110			
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 3.5 \text{ A}$		11		S		
Diode Forward Voltage	V_{SD}	$I_{S} = 1.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.9		V		
		Dynamic						
Total Gate Charge	Q_g			2.4				
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.5 \text{ A}$		1.1		nC		
Gate-Drain Charge	Q_{gd}			8.0				
Turn-On Delay Time	t _{d(on)}			7				
Rise Time	t _r	t_r $V_{DD} = 15 \text{ V}, R_L = 4.3 \Omega, I_D = 3.5 \text{ A},$		8		no		
Turn-Off Delay Time	$t_{d(off)}$	V_{GEN} = 4.5 V, R_{GEN} = 6 Ω		14		ns		
Fall Time	t _f			5				
Input Capacitance	C _{iss}			271				
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		48		pF		
Reverse Transfer Capacitance	C _{rss}		·	38				

Notes

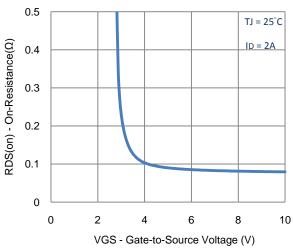
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

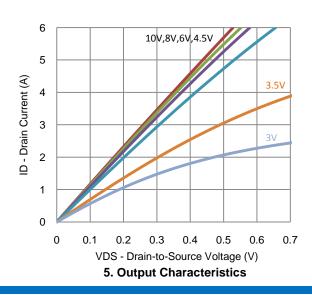
Typical Electrical Characteristics

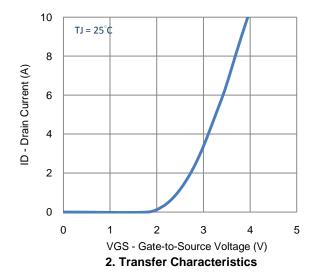


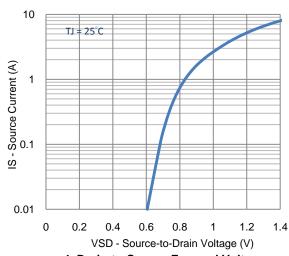
1. On-Resistance vs. Drain Current



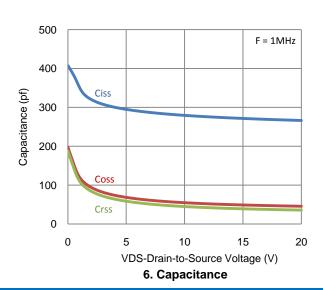
3. On-Resistance vs. Gate-to-Source Voltage



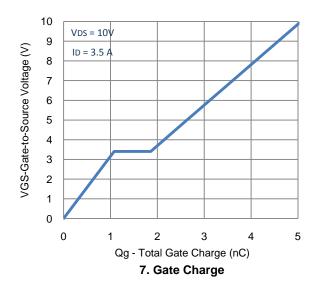


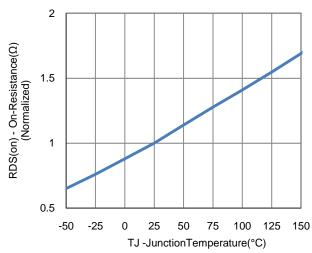


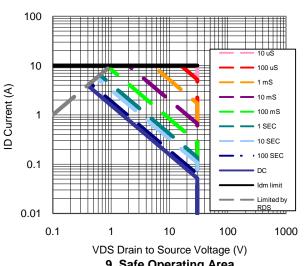
4. Drain-to-Source Forward Voltage



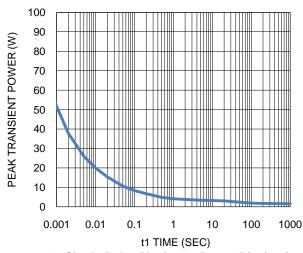
Typical Electrical Characteristics





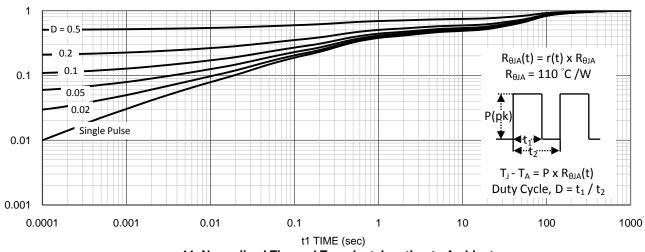




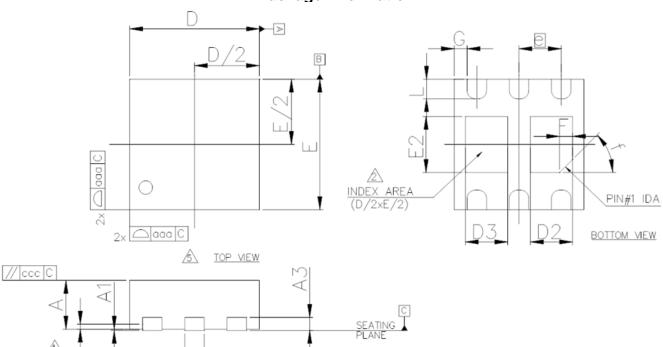


9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation



Package Information



SIL) I-	\/I	- W
211	حيث		- **
		_	

⊕bbbMCAB

SYMBOL	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES				
711111111111111111111111111111111111111	MIN.	NOM.	MAX.	П	MIN.	NOM.	MAX.	
A	0.70	0.75	0.80	П	0.028	0.030	0.032	
A1	0.00	0.02	0.05	П	0.000	0.001	0.002	
A3		0.20 ref		П		0.008 ref		
Ь	0.25	0.30	0.35	П	0.010	0.012	0.014	
D	2.00 BSC			П	0.079 BSC			
D2	0.60	0.65	0.70	П	0.024	0.026	0.028	
D3	0.60	0.65	0.70	П	0.024	0.026	0.028	
E		2.00 BSC		П	0.079 BSC			
E2	0.81	0.86	0.91	П	0.032	0.034	0.036	
e		0.65 BSC				0.026 BSC		
L	0.25	0.30	0.35	П	0.010	0.012	0.014	
F	0.20 REF			0.008 REF				
f	45?			45?				
G	0.15	0.20	0.25		0.006	0.008	0.010	
aaa	0.15			П	0.006			
bbb	0.10			П	0.004			

Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess
 Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The
 Foot.