

P-Channel 30-V (D-S) MOSFET

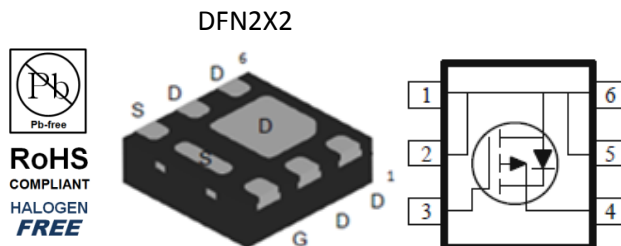
Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- Load Switches
- DC/DC Conversion
- Motor Drives

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
-30	30 @ $V_{GS} = -4.5V$	-8.2
	42 @ $V_{GS} = -2.5V$	-7.0



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 12	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	-8.2	A
	$T_A = 70^\circ\text{C}$		-6.5	
Pulsed Drain Current ^b		I_{DM}	-30	A
Continuous Source Current (Diode Conduction) ^a		I_S	-3	
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D	3	W
	$T_A = 70^\circ\text{C}$		1.9	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10 \text{ sec}$	$R_{\theta JA}$	40	$^\circ\text{C/W}$
	Steady State		90	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

Electrical Characteristics

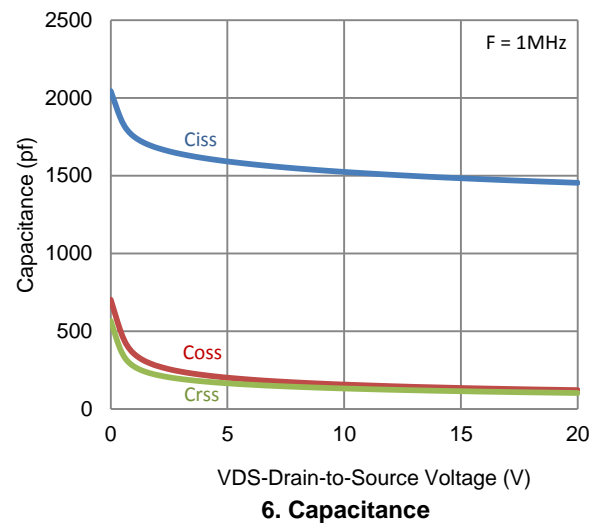
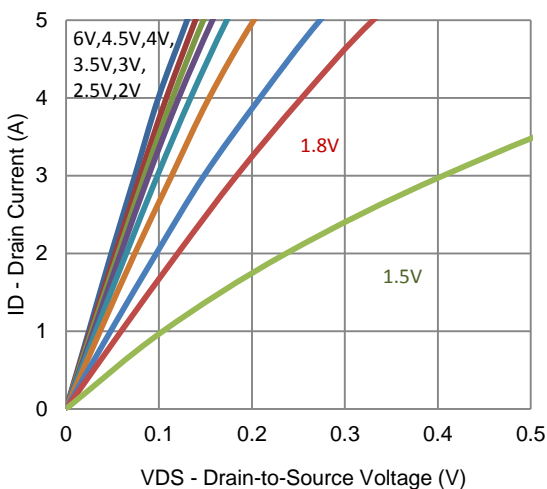
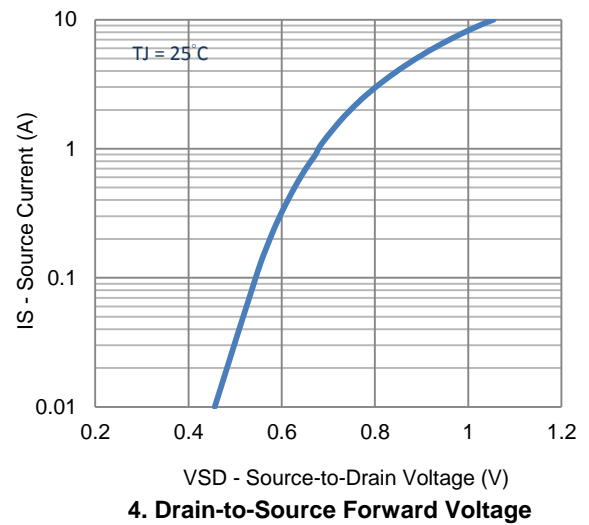
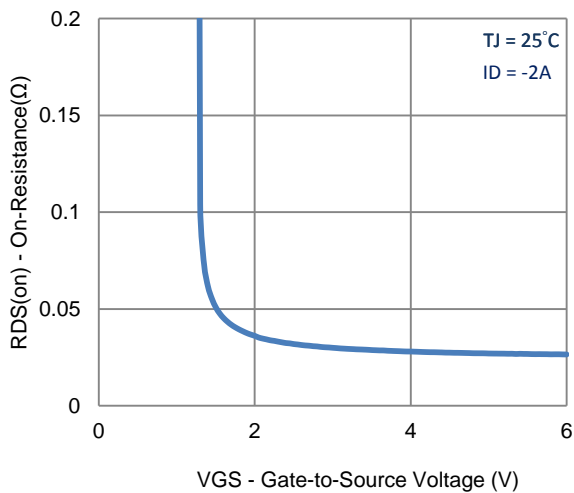
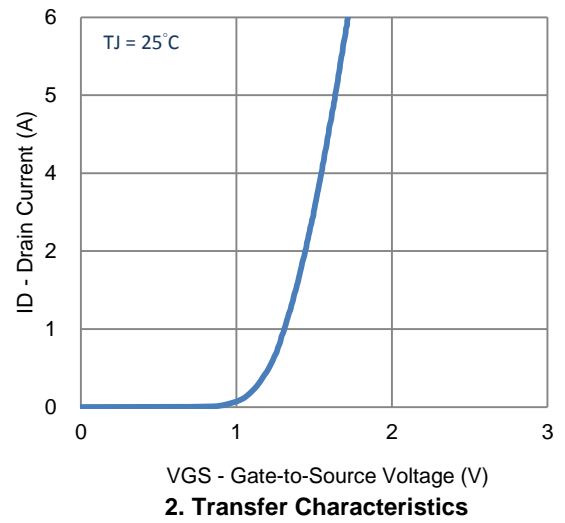
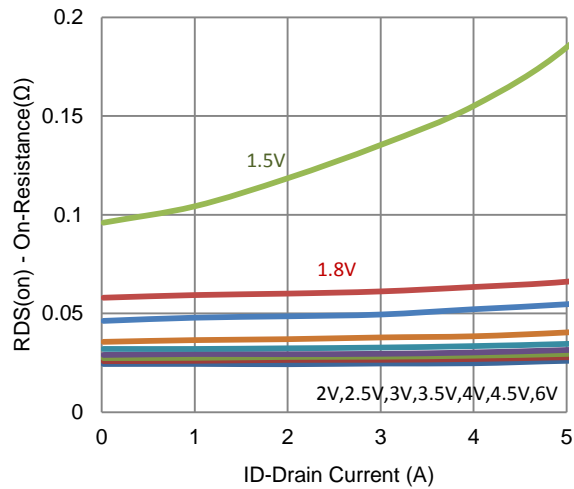
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24 V, V_{GS} = 0 V$			-1	uA
		$V_{DS} = -24 V, V_{GS} = 0 V, T_J = 55^\circ C$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5 V, V_{GS} = -10 V$	-12			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5 V, I_D = -2 A$			30	mΩ
		$V_{GS} = -2.5 V, I_D = -1.6 A$			42	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15 V, I_D = -2 A$		6		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.5 A, V_{GS} = 0 V$		-0.73		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = -15 V, V_{GS} = -4.5 V,$ $I_D = -2 A$		29		nC
Gate-Source Charge	Q_{gs}			3.9		
Gate-Drain Charge	Q_{gd}			7.1		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -15 V, R_L = 7.5 \Omega,$ $I_D = -2 A,$ $V_{GEN} = -4.5 V, R_{GEN} = 6 \Omega$		15		ns
Rise Time	t_r			20		
Turn-Off Delay Time	$t_{d(off)}$			81		
Fall Time	t_f			37		
Input Capacitance	C_{iss}	$V_{DS} = -15 V, V_{GS} = 0 V, f = 1 \text{ Mhz}$		1483		pF
Output Capacitance	C_{oss}			134		
Reverse Transfer Capacitance	C_{rss}			114		

Notes

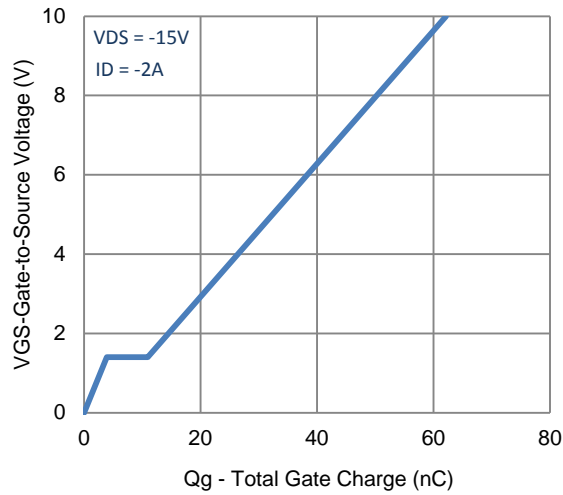
- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

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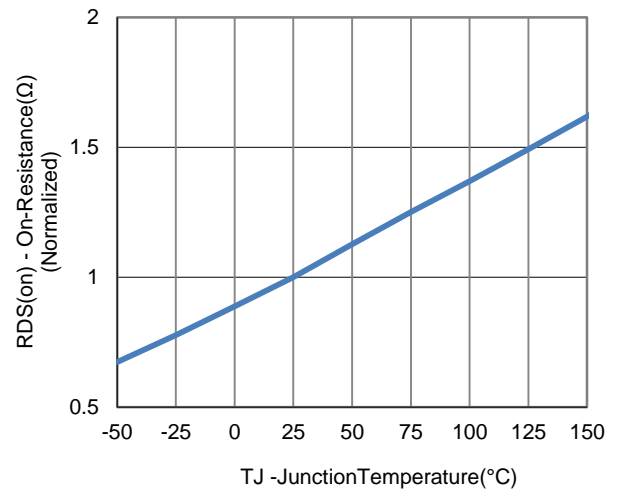
Typical Electrical Characteristics



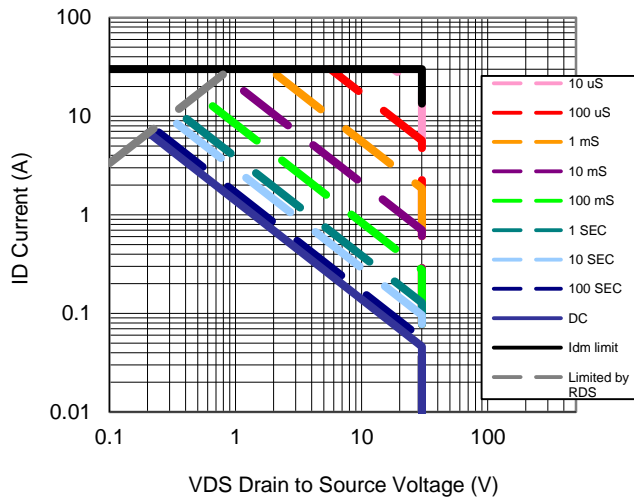
Typical Electrical Characteristics



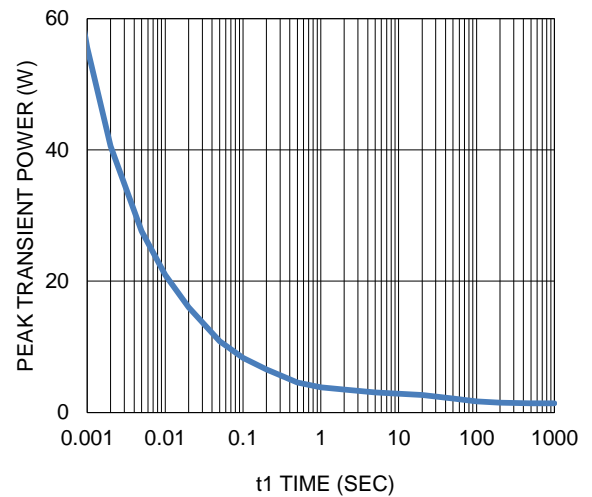
7. Gate Charge



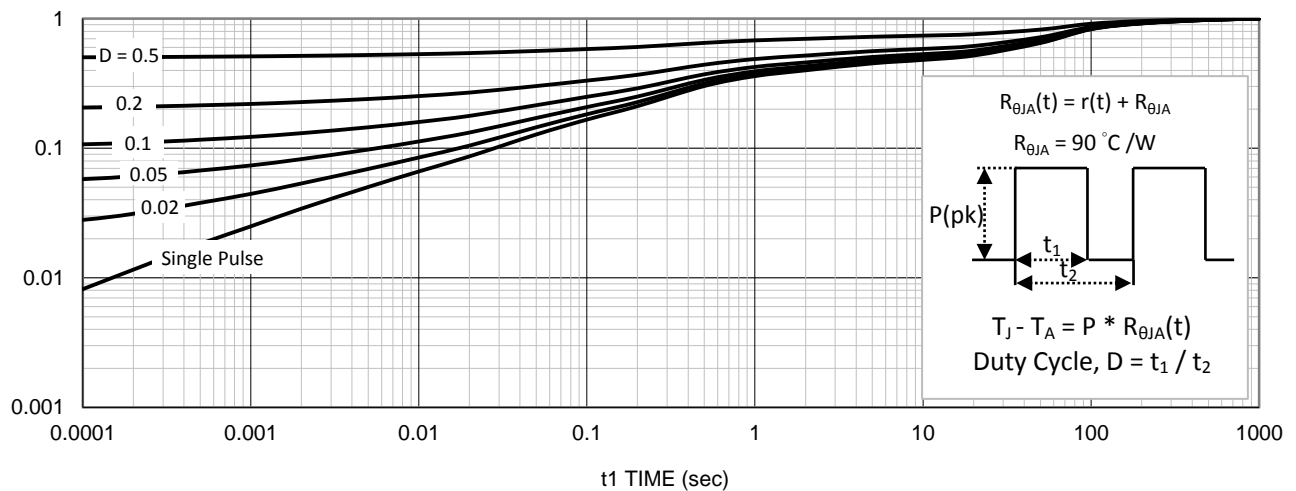
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

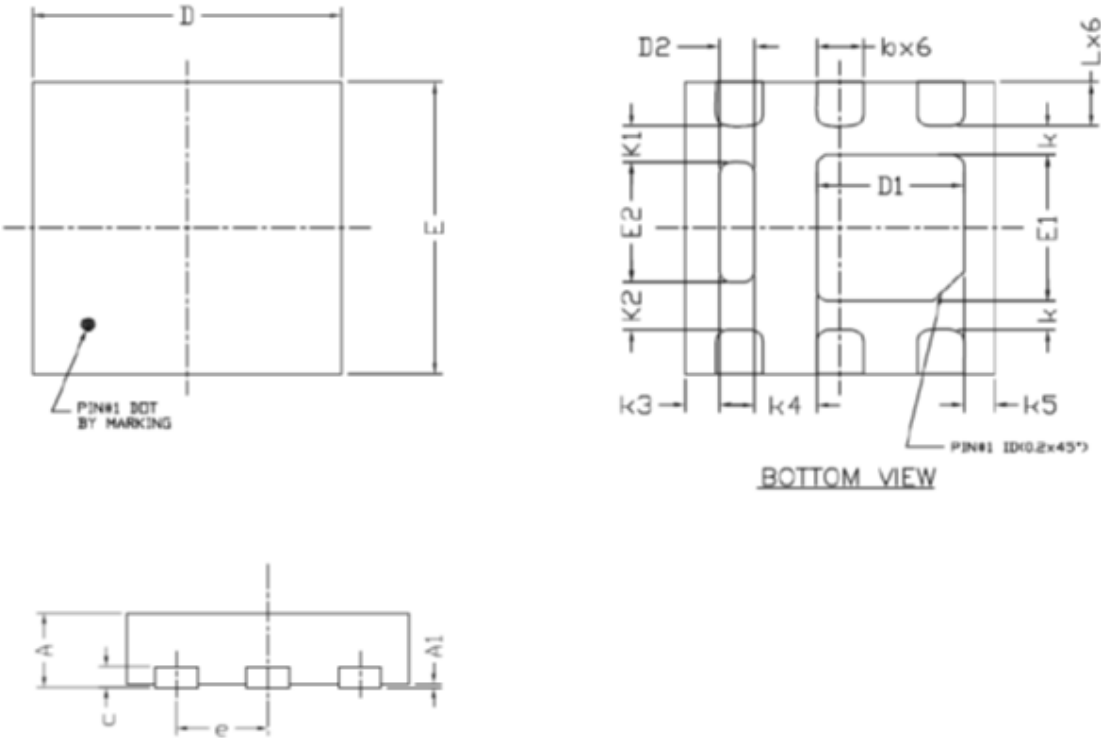


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	—	0.05	0.000	—	0.002
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.152 REF			0.006 REF		
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.85	0.95	1.05	0.033	0.037	0.041
D2	0.13	0.23	0.33	0.005	0.009	0.013
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.72	0.82	0.92	0.028	0.032	0.036
e	0.65 BSC			0.026 BSC		
K	0.20 BSC			0.008 BSC		
K1	0.25 BSC			0.010 BSC		
K2	0.33 BSC			0.013 BSC		
K3	0.22 BSC			0.009 BSC		
K4	0.40 BSC			0.016 BSC		
K5	0.20 BSC			0.008 BSC		
L	0.25	0.30	0.35	0.010	0.012	0.014