N-Channel 500-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- Off-line Power Supplies
- Electronic Ballasts
- High Power LED Lighting

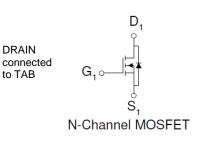
PRODUCT SUMMARY			
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
500	750 @ V _{GS} = 10V	9 ^a	



TO-220AB

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G D S Top View



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)							
Parameter		Symbol	Limit	Units			
Drain-Source Voltage		V _{DS}	500	V			
Gate-Source Voltage		V _{GS}	±20				
Continuous Drain Current ^a	T _C =25°C	I _D	9	А			
Pulsed Drain Current ^b		I _{DM}	40	A			
Continuous Source Current (Diode Conduction) ^a			9	А			
ower Dissipation T _c =25°C		PD	150	W			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C			

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	R _{θJA}	62.5	°C/W
Maximum Junction-to-Case	$R_{ extsf{ heta}JC}$	1	C/ VV

Notes

- a. Calculated continuous current based on maximum allowable junction temperature.
- b. Pulse width limited by maximum junction temperature

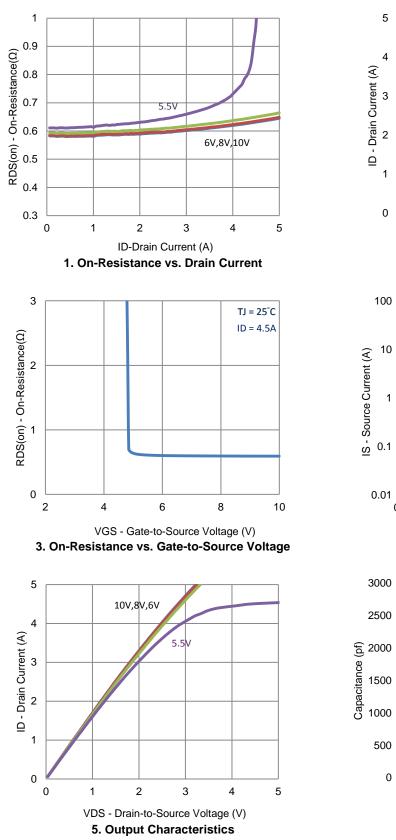
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 400 V, V_{GS} = 0 V$			1	uA		
		$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	15			А		
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$			750	mΩ		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$		38		S		
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 4.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.9		V		
Dynamic ^b								
Total Gate Charge	Q_{g}	$V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 10 \text{ V},$ $I_D = 6 \text{ A}$		20		nC		
Gate-Source Charge	Q_{gs}			9				
Gate-Drain Charge	Q_{gd}			9				
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 250 \text{ V}, \text{ R}_{L} = 50 \Omega,$ $I_{D} = 6 \text{ A},$ $V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		22		ns		
Rise Time	t _r			10				
Turn-Off Delay Time	t _{d(off)}			53				
Fall Time	t _f			10				
Input Capacitance	C _{iss}			1845				
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 Mhz		113		pF		
Reverse Transfer Capacitance	C _{rss}			26				

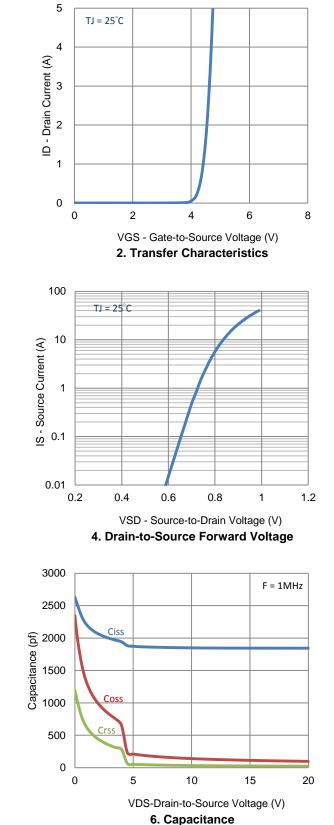
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

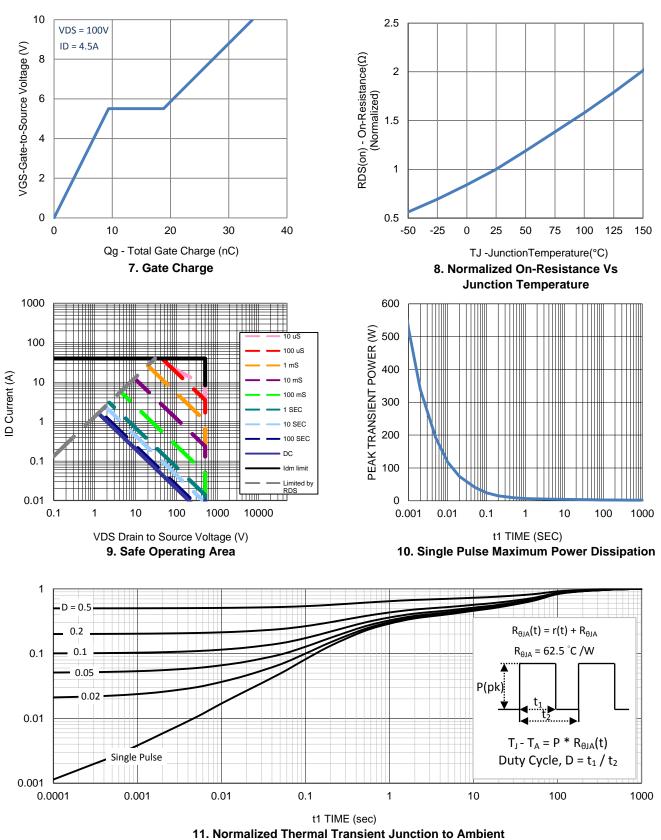
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Typical Electrical Characteristics



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Package Information

