# P-Channel 80-V (D-S) MOSFET

### **Key Features:**

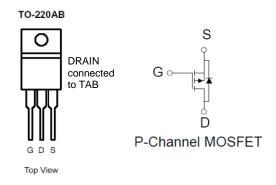
- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

# **Typical Applications:**

- White LED boost converters
- · Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>D</sub> (A)	
-80	15 @ V <sub>GS</sub> = -10V	-95 <sup>a</sup>	
-60	$17 @ V_{GS} = -4.5V$	-95	





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		$V_{DS}$	-80	\/	
Gate-Source Voltage		$V_{GS}$	±20	V	
Continuous Drain Current a	T <sub>C</sub> =25°C	I <sub>D</sub>	-95		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	-380	А	
Continuous Source Current (Diode Conduction) <sup>a</sup> T <sub>C</sub> =25°C		I <sub>S</sub>	-95	Α	
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_{D}$	300	W	
Operating Junction and Storage Temperature Range			-55 to 175	°C	

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV

#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

### **Electrical Characteristics**

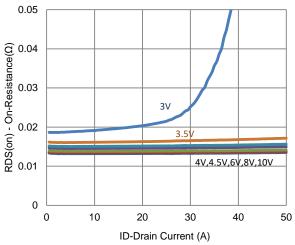
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250 \text{ uA}$	-1			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zara Cata Valtaga Drain Current	l	$V_{DS} = -64 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-120			Α	
Drain-Source On-Resistance <sup>a</sup>	r	$V_{GS} = -10 \text{ V}, I_D = -50 \text{ A}$			15	mΩ	
	r <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -45 \text{ A}$			17		
Forward Transconductance a	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, I_{D} = -50 \text{ A}$		83		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -50 \text{ A}, V_{GS} = 0 \text{ V}$		-0.88		V	
		Dynamic <sup>b</sup>					
Total Gate Charge	$Q_g$	$V_{DS} = -40 \text{ V}, V_{GS} = -4.5 \text{ V},$ $I_{D} = -20 \text{ A}$		236		nC	
Gate-Source Charge	$Q_{gs}$			80			
Gate-Drain Charge	$Q_gd$			69			
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = -40 \text{ V}, R_{L} = 2 \Omega,$ $I_{D} = -20 \text{ A},$ $V_{GEN} = -10 \text{ V}, R_{GEN} = 6 \Omega$		34			
Rise Time	t <sub>r</sub>			37		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>			514			
Fall Time	t <sub>f</sub>			146			
Input Capacitance	C <sub>iss</sub>	$V_{DS} = -50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		15879		pF	
Output Capacitance	C <sub>oss</sub>			366			
Reverse Transfer Capacitance	C <sub>rss</sub>			364			

### Notes

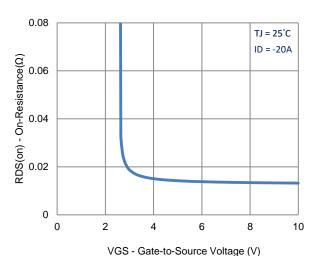
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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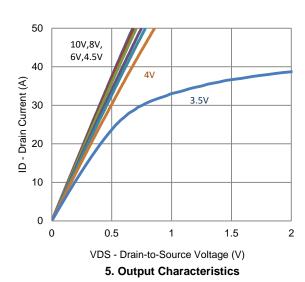
## **Typical Electrical Characteristics**

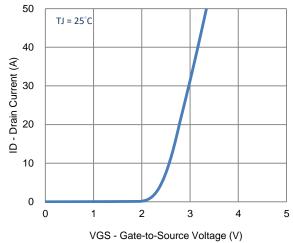


1. On-Resistance vs. Drain Current

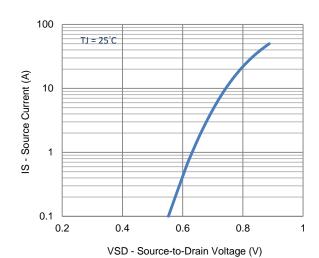


3. On-Resistance vs. Gate-to-Source Voltage

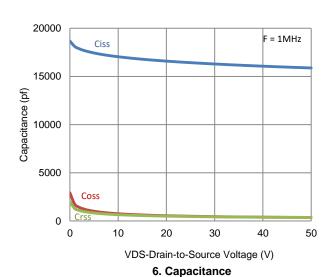




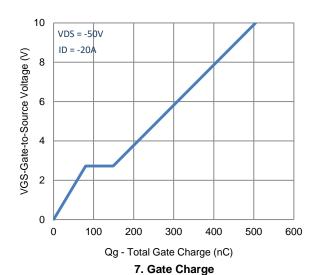
2. Transfer Characteristics

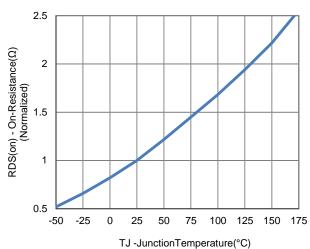


4. Drain-to-Source Forward Voltage

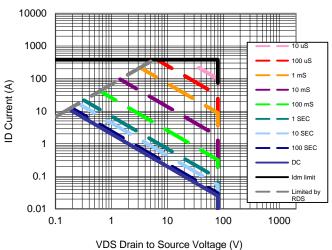


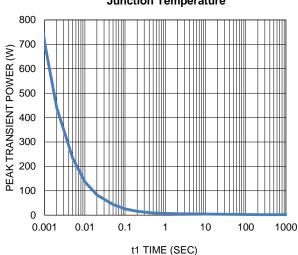
## **Typical Electrical Characteristics**





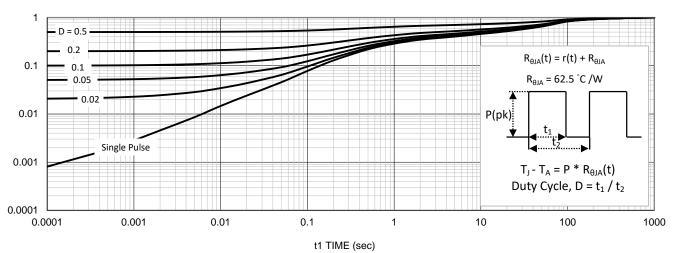






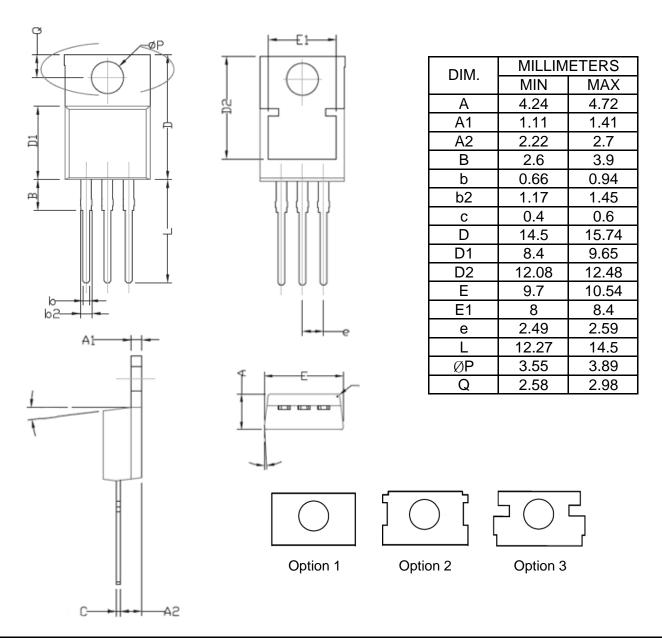
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

# **Package Information**



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