

## N-Channel 30-V (D-S) MOSFET

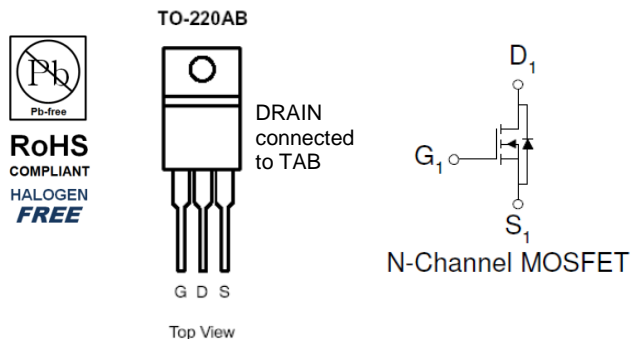
### Key Features:

- Low  $r_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed

### Typical Applications:

- Automotive Systems
- DC/DC Conversion Circuits
- Battery Powered Power Tools

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ (m $\Omega$ )	$I_D$ (A)
30	1.9 @ $V_{GS} = 10V$	120 <sup>a</sup>
	2.9 @ $V_{GS} = 4.5V$	



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$T_C = 25^\circ\text{C}$	$I_D$	120	A
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	360	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$T_C = 25^\circ\text{C}$	$I_S$	120	A
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	300	W
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>c</sup>	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	

### Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board

## Electrical Characteristics

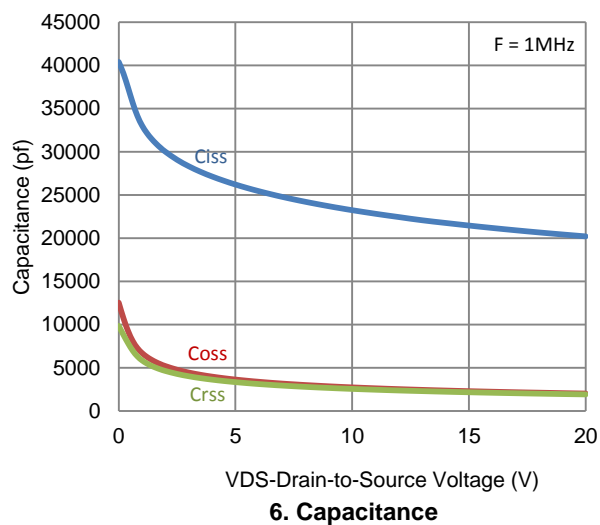
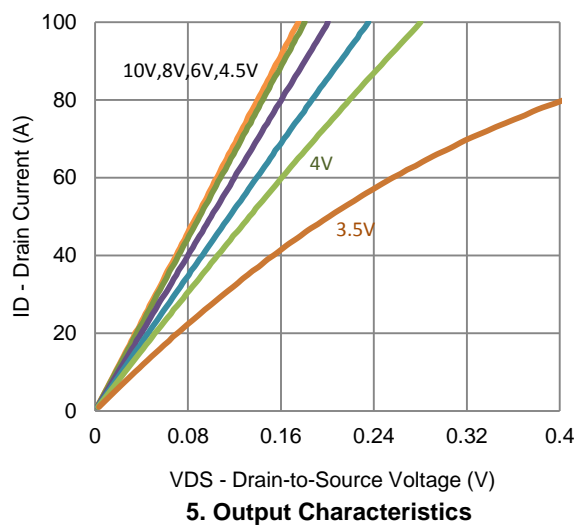
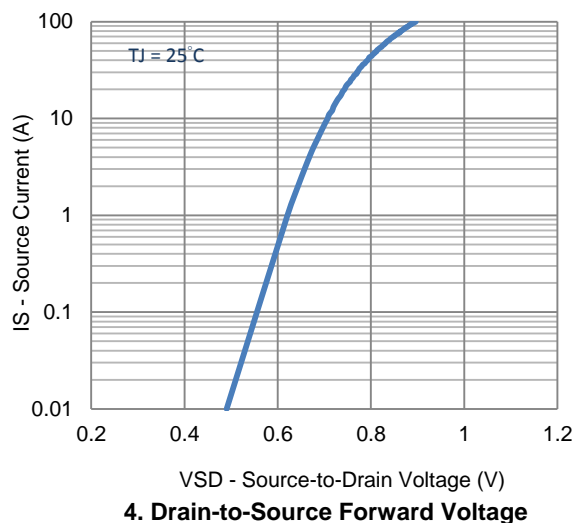
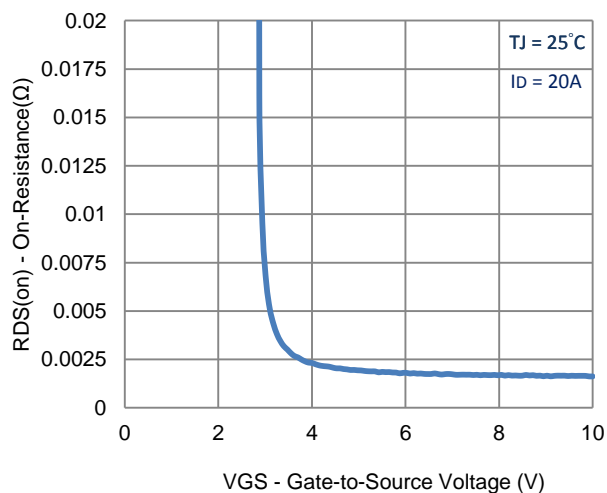
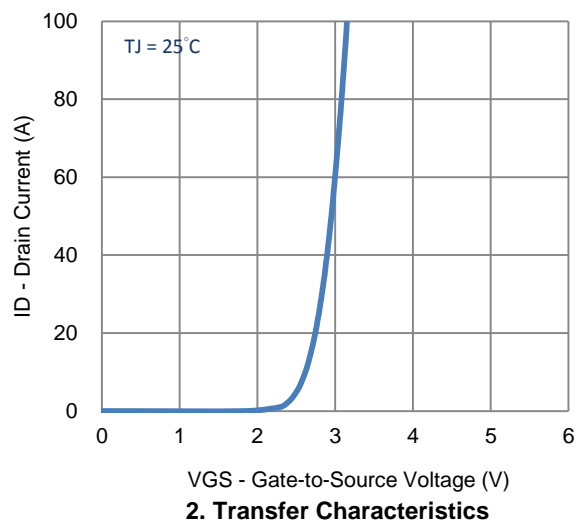
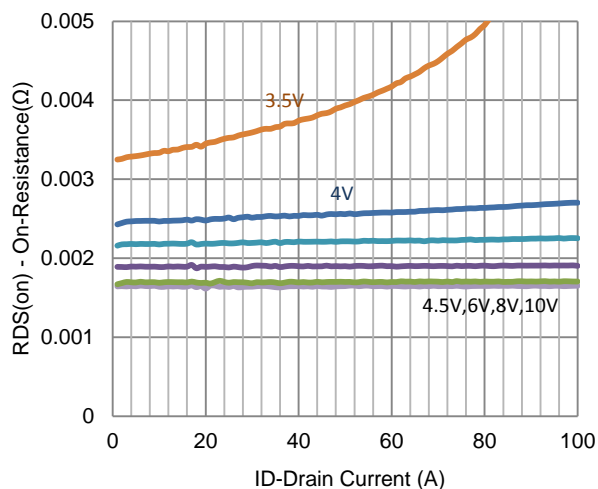
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24 V, V_{GS} = 0 V$			1	uA
		$V_{DS} = 24 V, V_{GS} = 0 V, T_J = 55^\circ C$			25	
On-State Drain Current	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	120			A
Drain-Source On-Resistance	$r_{DS(on)}$	$V_{GS} = 10 V, I_D = 45 A$			1.9	mΩ
		$V_{GS} = 4.5 V, I_D = 44 A$			2.9	
Forward Transconductance	$g_{fs}$	$V_{DS} = 15 V, I_D = 20 A$		35		S
Diode Forward Voltage	$V_{SD}$	$I_S = 60 A, V_{GS} = 0 V$		0.83		V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 20 A$		138		nC
Gate-Source Charge	$Q_{gs}$			40		
Gate-Drain Charge	$Q_{gd}$			68		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 15 V, R_L = 0.8 \Omega, I_D = 20 A, V_{GEN} = 10 V, R_{GEN} = 6 \Omega$		38		ns
Rise Time	$t_r$			83		
Turn-Off Delay Time	$t_{d(off)}$			313		
Fall Time	$t_f$			153		
Input Capacitance	$C_{iss}$	$V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz$		21451		pF
Output Capacitance	$C_{oss}$			2286		
Reverse Transfer Capacitance	$C_{rss}$			2156		

## Notes

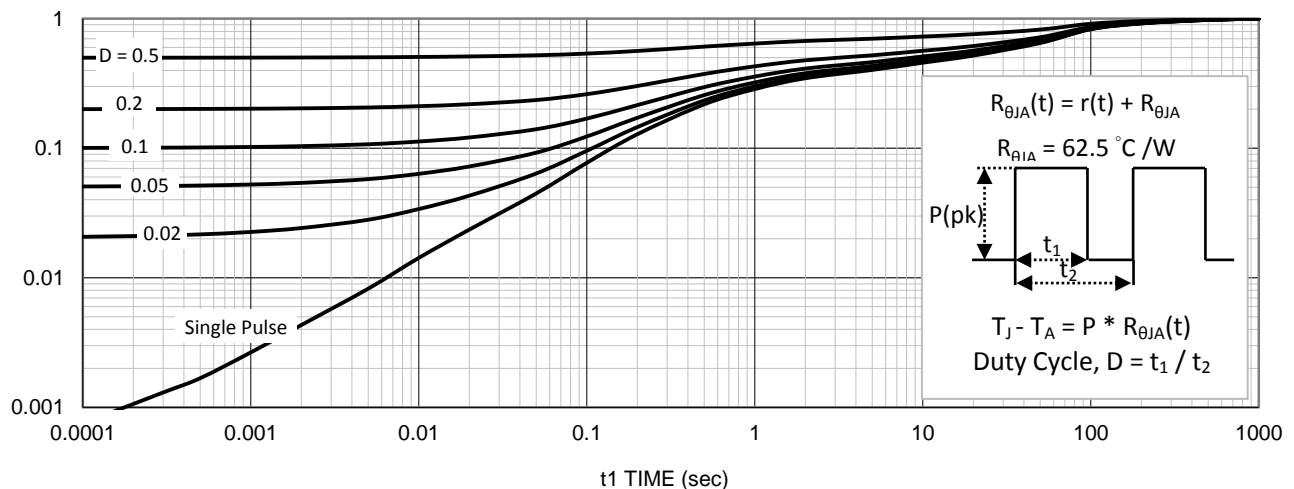
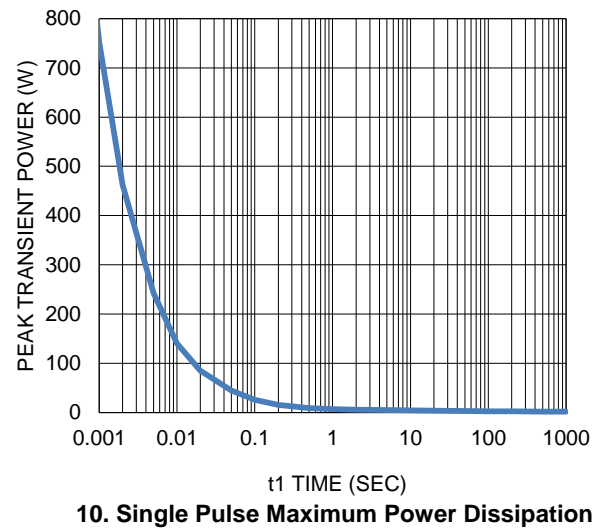
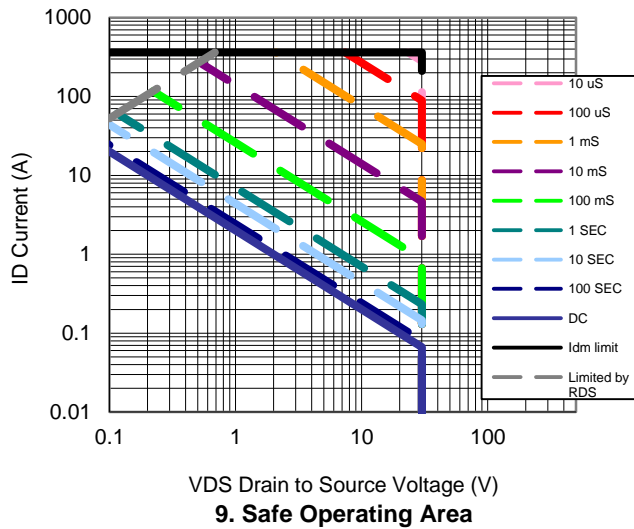
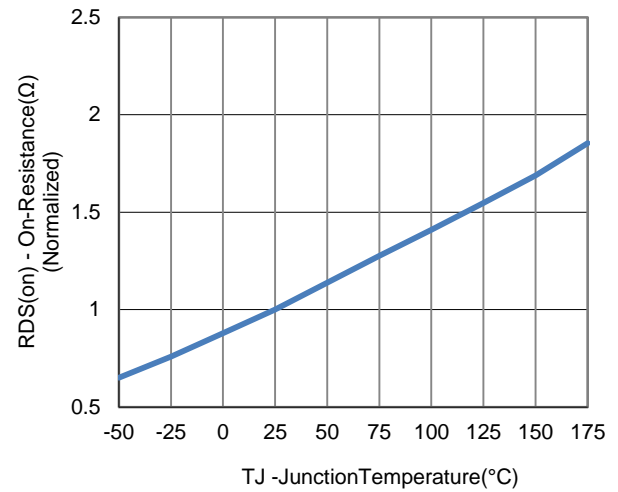
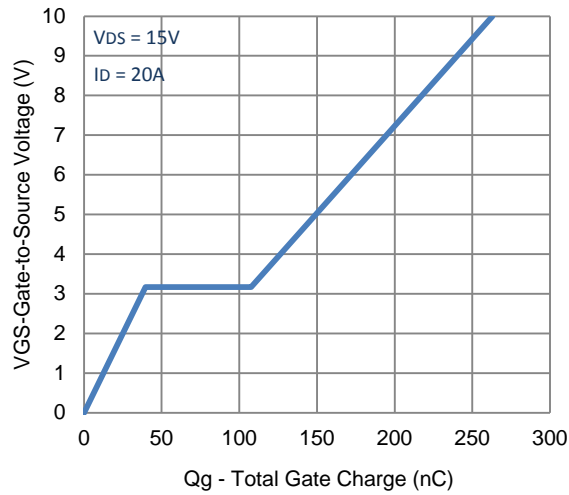
- Pulse test:  $PW \leq 300 \mu s$  duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.

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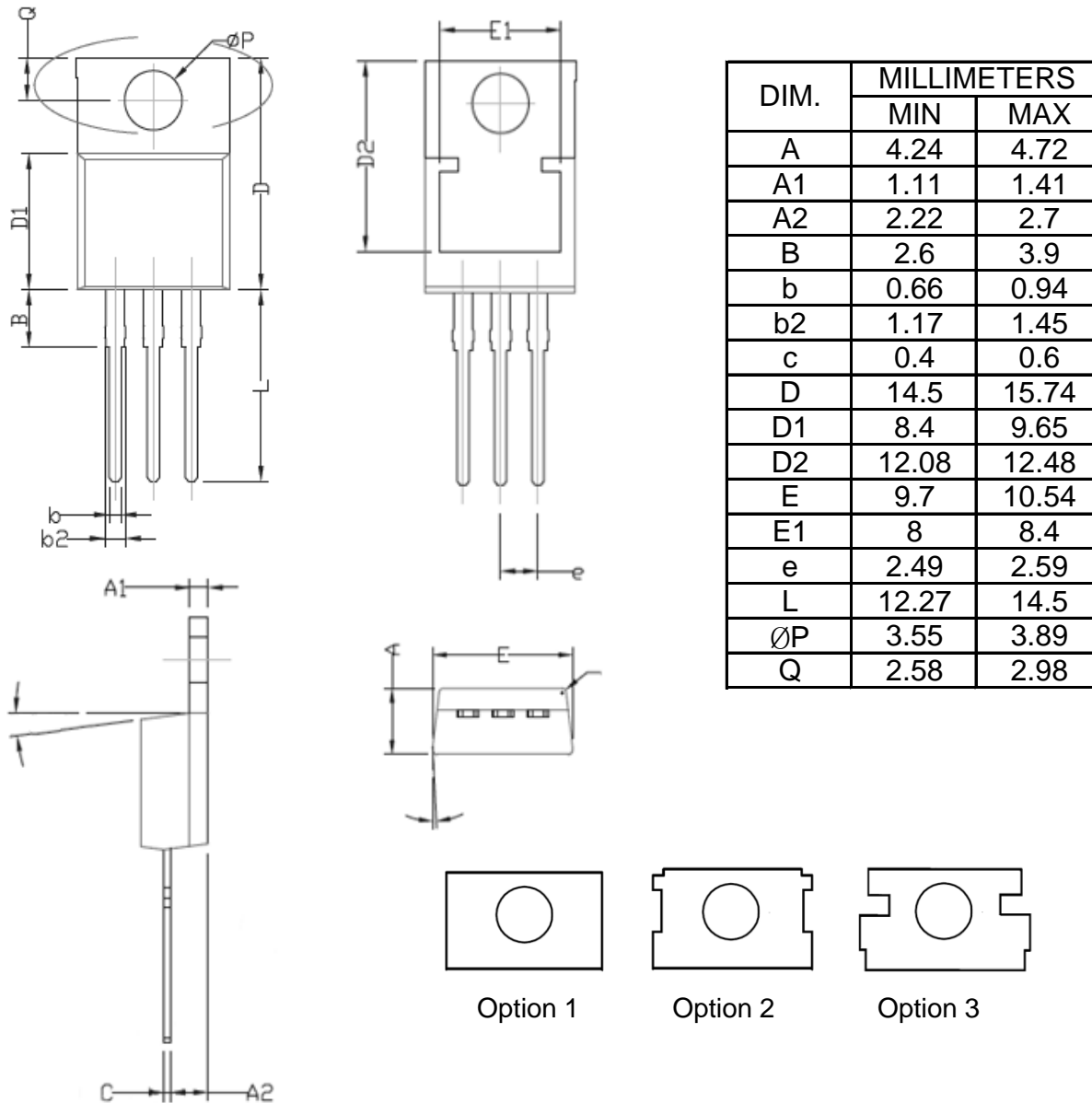
## Typical Electrical Characteristics



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## Package Information



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