N-Channel 20-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

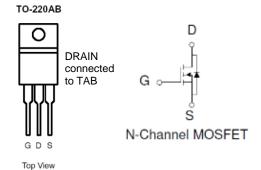
Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□ (A)	
	1.9 @ V _{GS} = 4.5V		
20	2.2 @ V _{GS} = 2.5V	90 ^a	
	2.8 @ V _{GS} = 1.8V		



FREE



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			20	V		
Gate-Source Voltage		V_{GS}	±8] V		
Continuous Drain Current a	T _C =25°C	I _D	90	Α		
Pulsed Drain Current ^b		I _{DM}	200			
Continuous Source Current (Diode Conduction) a	T _C =25°C	I _S	90	Α		
Power Dissipation ^a	T _C =25°C	P_{D}	300	W		
Operating Junction and Storage Temperature Range			-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV		

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

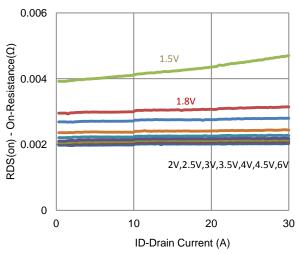
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	0.4			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Brain Gurrent	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	112.5			Α	
Drain-Source On-Resistance ^a		$V_{GS} = 4.5 \text{ V}, I_D = 30 \text{ A}$			1.9	mΩ	
	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 24 \text{ A}$			2.2		
		$V_{GS} = 1.8 \text{ V}, I_D = 16 \text{ A}$			2.8		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 30 \text{ A}$		247		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 45 \text{ A}, V_{GS} = 0 \text{ V}$		0.79		V	
Dynamic ^b							
Total Gate Charge	Q_g	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 2 \text{ A}$		148		nC	
Gate-Source Charge	Q_gs			13			
Gate-Drain Charge	Q_gd	10 - 2 /\		26			
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 10 \text{ V}, R_{L} = 5 \Omega,$		27			
Rise Time	t _r	$I_{DS} = 10 \text{ V}, \text{ NL} = 3 \Omega,$ $I_{D} = 2 \text{ A},$		68		ns	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		586			
Fall Time	t _f			181			
Input Capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 Mhz		9726			
Output Capacitance	C _{oss}			1136		pF	
Reverse Transfer Capacitance	C_{rss}			1056			

Notes

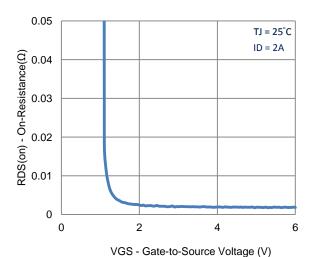
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing.

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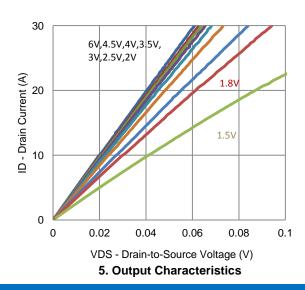
Typical Electrical Characteristics

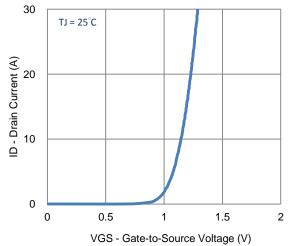


1. On-Resistance vs. Drain Current

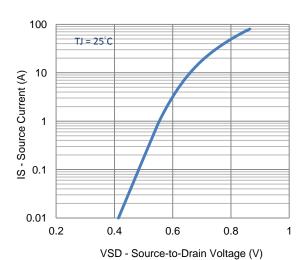


3. On-Resistance vs. Gate-to-Source Voltage

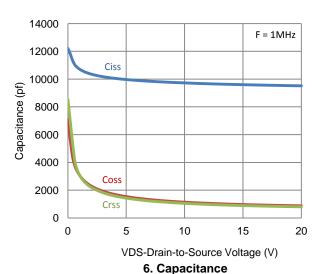




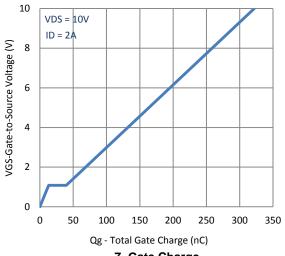
2. Transfer Characteristics



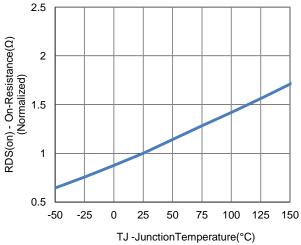
4. Drain-to-Source Forward Voltage



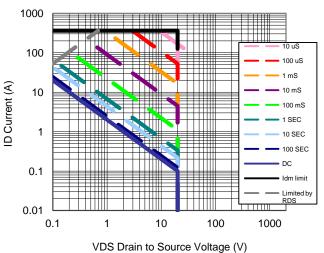
Typical Electrical Characteristics



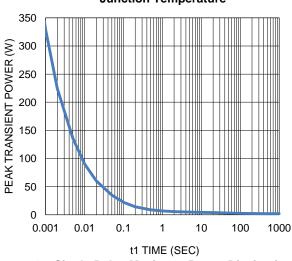
7. Gate Charge



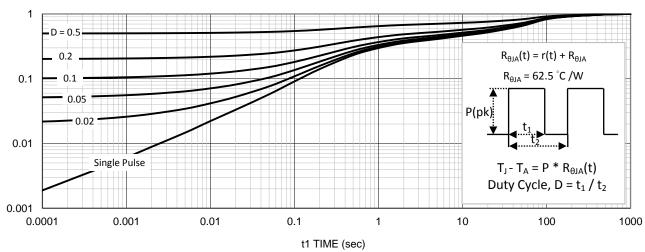
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

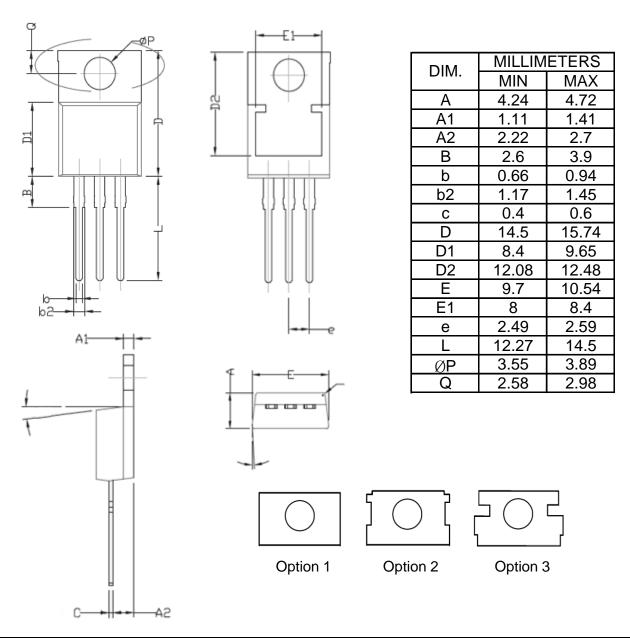


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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