# N-Channel 800-V (D-S) MOSFET

### **Key Features:**

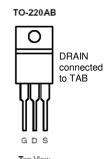
- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

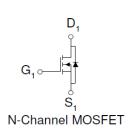
## **Typical Applications:**

- Power Supplies
- Motor Drives
- · Consumer Electronics

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
800	$1.5 @ V_{GS} = 10V$	8 <sup>a</sup>	







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 ℃ UNLESS OTHERWISE NOTED)							
Parameter			Limit	Units			
Drain-Source Voltage			800	V			
Gate-Source Voltage		$V_{GS}$	±20	v			
Continuous Drain Current a	T <sub>C</sub> =25°C	$I_D$	8				
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	50	Α			
Continuous Source Current (Diode Conduction) <sup>a</sup> T <sub>C</sub> =25 °C		I <sub>S</sub>	8	Α			
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_{D}$	300	W			
Operating Junction and Storage Temperature Range		$T_J,T_stg$	-55 to 175	℃			

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>c</sup>	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{ heta JC}$	0.5	C/VV

#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

#### **Electrical Characteristics**

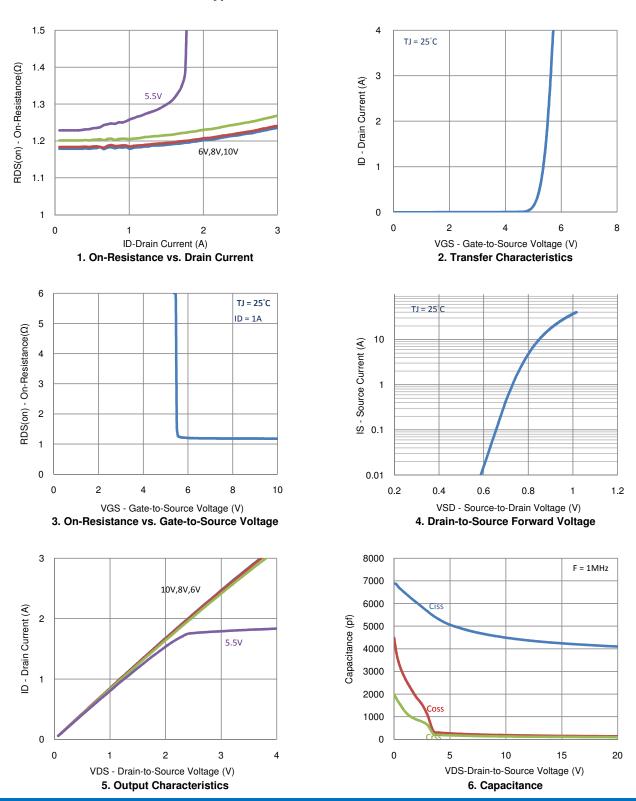
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	2			V	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 640 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 640 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			1 25	uA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 6 \text{ V}$	10		20	Α	
Drain-Source On-Resistance a	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 1 \text{ A}$			1.5	Ω	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}, I_{D} = 1 \text{ A}$		6		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{S} = 4 A, V_{GS} = 0 V$		0.79		V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$	$V_{DS} = 100 \text{ V}, V_{GS} = 6 \text{ V},$ $I_{D} = 1 \text{ A}$		23			
Gate-Source Charge	$Q_gs$			11		nC	
Gate-Drain Charge	$Q_{gd}$			9.2			
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 100 \text{ V}, \text{ R}_{L} = 100 \Omega,$ $I_{D} = 1 \text{ A},$ $V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		25			
Rise Time	t <sub>r</sub>			7		ns	
Turn-Off Delay Time	$t_{d(off)}$			56			
Fall Time	t <sub>f</sub>			19			
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 Mhz		4241			
Output Capacitance	C <sub>oss</sub>			155		pF	
Reverse Transfer Capacitance	$C_{rss}$			111			

#### Notes

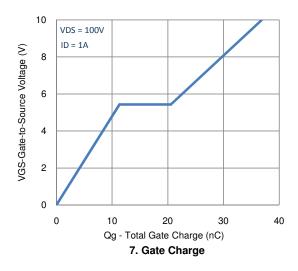
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

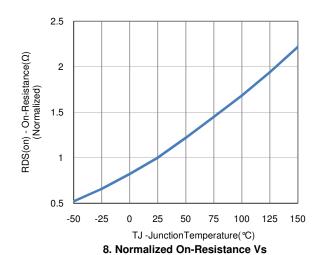
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## **Typical Electrical Characteristics**

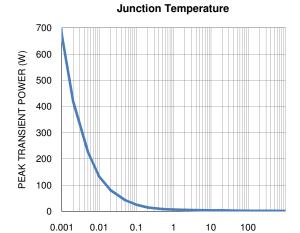


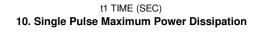
## **Typical Electrical Characteristics**

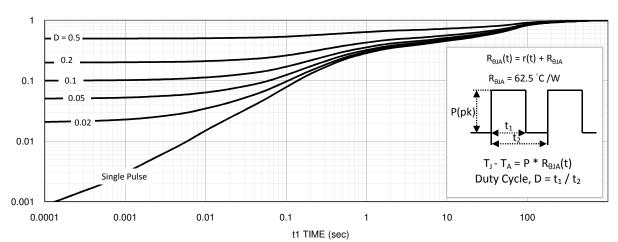




9. Safe Operating Area

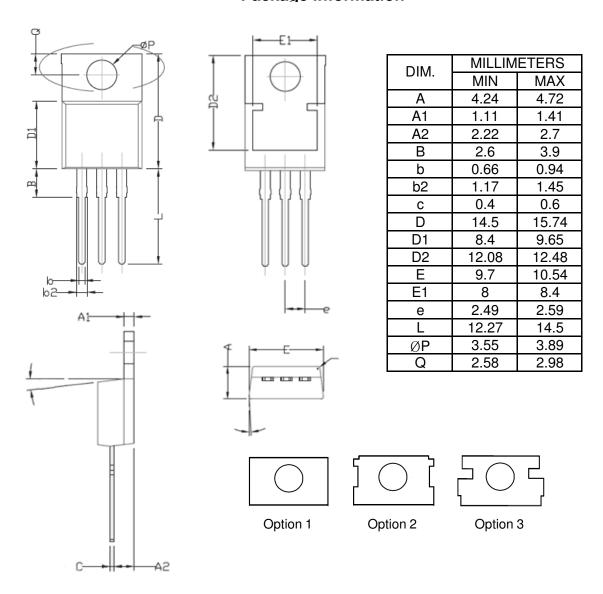






11. Normalized Thermal Transient Junction to Ambient

### **Package Information**



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