N-Channel 150-V (D-S) MOSFET

Key Features:

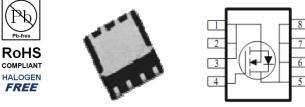
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
150	88 @ V _{GS} = 10V	6.2		
150	96 @ V _{GS} = 5.5V	5.9		

DFN5X6-8L



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Limit	Units				
Drain-Source Voltage			150	V			
Gate-Source Voltage	V _{GS}	±20	v				
Continuous Drain Current ^a	T _A =25°C		6.2	А			
Continuous Drain Current	T _A =70°C	I _D	5				
Pulsed Drain Current ^b		I _{DM}	30				
Continuous Source Current (Diode Conduction) ^a		I _S	6.7	А			
Power Dissinction ^a	T _A =25°C	P _D	5	W			
Power Dissipation ^a	T _A =70°C	'D	3.2	vv			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C			

THERMAL RESISTANCE RATINGS						
Parameter			Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{eja}	25	°C/W		
	Steady State	ιν _θ ja	65			

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

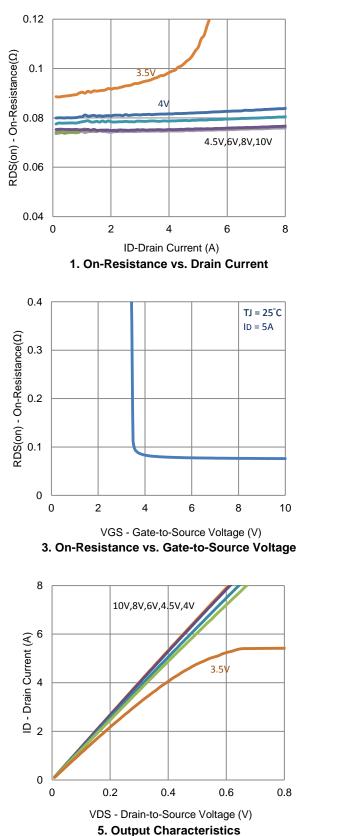
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 120 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	uA	
Zero Gale Voltage Drain Current	DSS	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	25		25		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	15			А	
Drain Course On Desistance ^a	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$			88	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 5.5 \text{ V}, \text{ I}_{D} = 4 \text{ A}$			96	11152	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		32		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 3.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.75		V	
		Dynamic ^b					
Total Gate Charge	Qg	V _{DS} = 75 V, V _{GS} = 5.5 V,		23		nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 75$ V, $V_{GS} = 5.5$ V, $I_{D} = 5$ A		7.4			
Gate-Drain Charge	Q _{gd}	1 <u>0</u> = 0 A		9.0			
Turn-On Delay Time	t _{d(on)}	V _{DS} = 75 V, R _I = 15 Ω,		14			
Rise Time	t _r	$V_{DS} = 73 V, K_{L} = 13 \Omega_{2},$ $I_{D} = 5 A,$		15		n 0	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		70		ns	
Fall Time	t _f	$v_{\text{GEN}} = 10$ v_{1} $n_{\text{GEN}} = 0.22$		31			
Input Capacitance	C _{iss}			2599			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		167		pF	
Reverse Transfer Capacitance	C _{rss}			90			

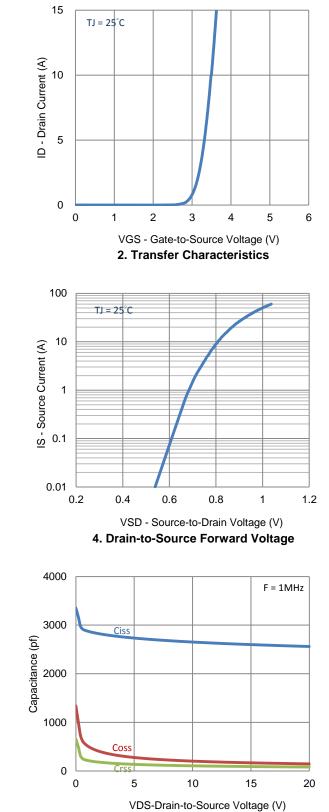
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

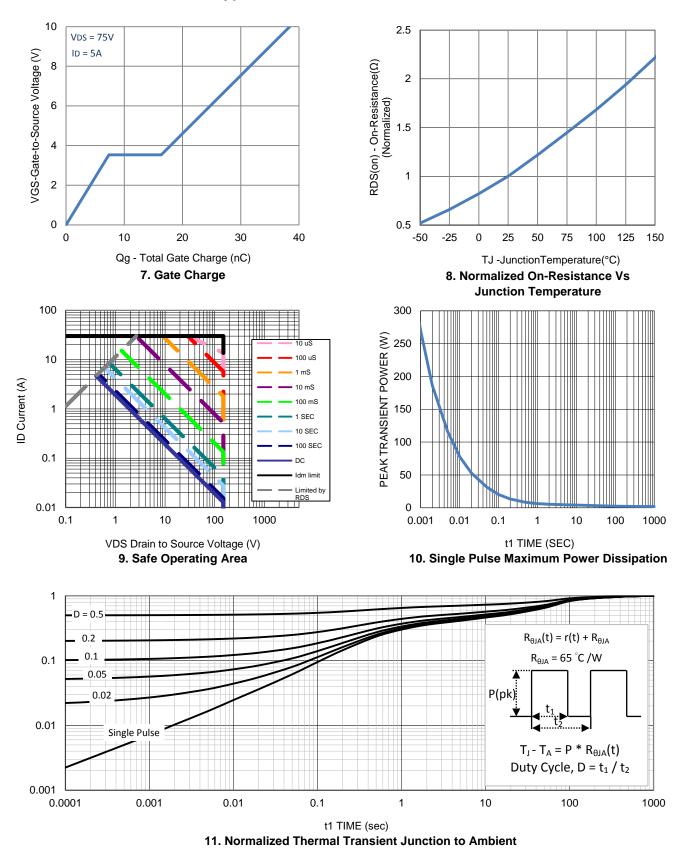
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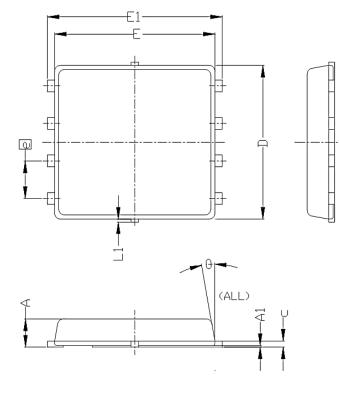


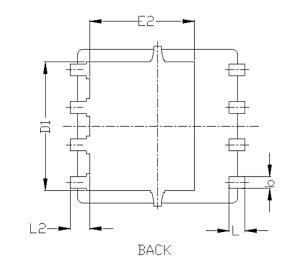
6. Capacitance



Typical Electrical Characteristics

Package Information





SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
51 MIDOLS	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.85	0.95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
с	0.15	0.20	0.25	0.006	0.008	0.010	
D	5.20 BSC			0.205 BSC			
D1	4.35 BSC			0.171 BSC			
E	5.55 BSC			0.219 BSC			
E1	6.05 BSC			0.238 BSC			
E2	3.62 BSC			0. 143 BSC			
e	1.27 BSC			0.050 BSC			
L	0.45	0.55	0.65	0.018	0.022	0.026	
Ll	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	