Analog Power AM6968N

### **Dual N-Channel Logical Level MOSFET**

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

onverters and power management in portable and		$0.022 @ V_{GS} = 4.5 V$	6.8
attery-powered products such as computers, rinters, PCMCIA cards, cellular and cordless	20	$0.030$ @ $V_{GS} = 2.5V$	5.8
elephones.		$0.047 @ V_{GS} = 1.8V$	4.7
Low r provides higher efficiency and			

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe TSSOP-8 saves board space
- Fast switching speed
- High performance trench technology

	TSSOP-8 Top View			D <sub>1</sub>	$D_2$
D1	1 2 3 4	8	D2 S2 S2 G2	G <sub>1</sub> S <sub>1</sub> N-Channel MOSFET	G₂ → S₂ N-Channel MOSFET

 $r_{DS(on)}\left(OHM\right)$ 

 $I_D(A)$ 

PRODUCT SUMMARY

 $V_{DS}(V)$ 

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Maximum	Units	
Drain-Source Voltage		$ m V_{DS}$	20	V	
Gate-Source Voltage		$V_{GS}$	±12	V	
Continuous Drain Current <sup>a</sup>	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	I.	6.8		
Continuous Drain Current	ъ	5.4	A		
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	±30			
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	1.5	A	
Power Dissipation <sup>a</sup>	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	D <sub>D</sub>	1.5	W	
T <sub>A</sub> = $70^{\circ}$ C		ТЪ	1.0	V V	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур	Max	
M · I · · · a	t <= 10 sec	D	72	83	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	$R_{thJA}$	100	120	

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### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

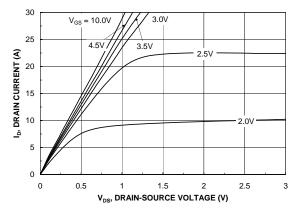
SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)						
Parame te r	Symbol	Test Conditions				Unit
1 arameter	Symbol	Test conditions	Min	Тур	Max	Cint
Static						-
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}$ , $I_D = 250$ uA	0.7			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zero Guite Voltage Brain Carrent	*D88	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	uA
On-State Drain Current <sup>A</sup>	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	30			A
		$V_{GS} = 4.5 \text{ V}, I_D = 6.8 \text{ A}$			0.022	Ω
Drain-Source On-Resistance <sup>A</sup>	r <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 5.8 \text{ A}$			0.030	
		$V_{GS} = 1.8 \text{ V}, I_D = 4.7 \text{ A}$			0.047	
Forward Tranconductance <sup>A</sup>	$g_{fs}$	$V_{DS} = 10 \text{ V}, I_{D} = 6.8 \text{ A}$		25		S
Diode Forward Voltage <sup>A</sup>	$V_{SD}$	$I_S = 6.8 \text{ A}, V_{GS} = 0 \text{ V}$		0.89		V
Dynamic <sup>b</sup>						
Total Gate Charge	Qg			13.4		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10V, V_{GS} = 4.5V, I_{D} = 6.8A$		0.9		nC
Gate-Drain Charge	Qgd			2.0		1
Turn-On Delay Time	td(on)			18		
Rise Time	tr	$V_{DD}=10V, V_{GS}=4.5V, I_{D}=1A$ ,		25		C
Turn-Off Delay Time	td(off)	$R_{\text{GEN}}=10\Omega$		50		nS
Fall-Time	tf			25		

#### Notes

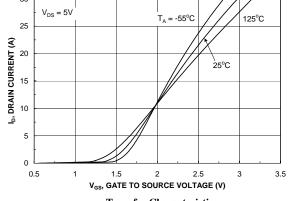
- a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

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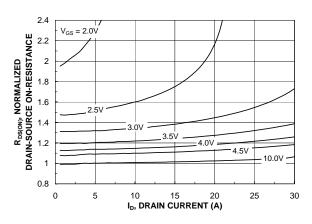
# Typical Electrical Characteristics (N-Channel)



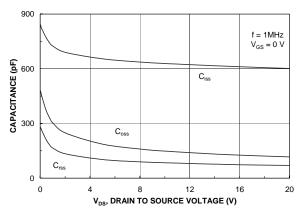
**Output Characteristics** 



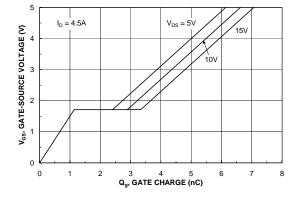
**Transfer Characteristics** 



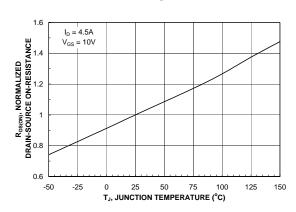
On-Resistance vs. Drain Current



Capacitance



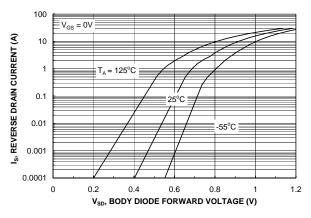
**Gate Charge** 

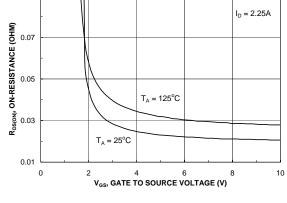


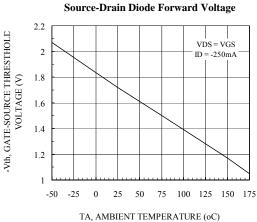
On-Resistance vs. Junction Temperature

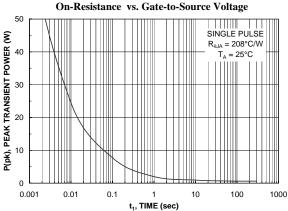
## Typical Electrical Characteristics (N-Channel)

0.09



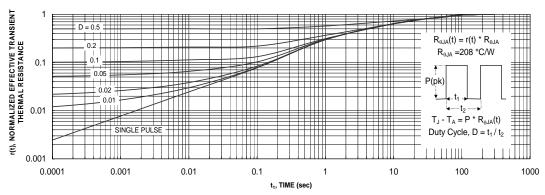






Vth Gate to Source Voltage Vs Temperature

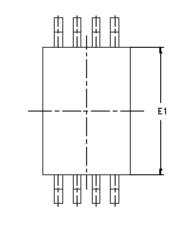
Single Pulse Power, Junction-to-Ambient

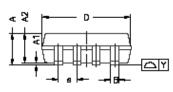


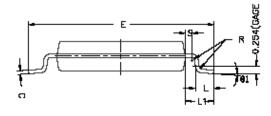
**Normalized Thermal Transient Junction to Ambient** 

# Package Information

TSSOP-8: 8LEAD







<b>D</b>	MILLIMETERS					
DIM.	MIN.	NDM.	MAX.			
A	1.05	1.10	1.20			
A(1)	0.05	0.10	0.15			
A(2)	0.99	1.02	1.05			
В	D.19	0.25	0.30			
C		0.127				
D	2.90	3.0D	3.10			
Ε	6.20	6.40	6.60			
E1	4.30	4.40	4.50			
В	0.659SC					
L	0.45	0.60	0.75			
L1	0.90	1.00	1.10			
Y			0.10			
<b>8</b> 1	O.	4	Ē.			
R	0.09		- 1			
S	0.20					