Dual N-Channel 30-V (D-S) MOSFET

Key Features:

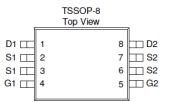
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

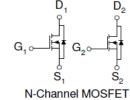
PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)		
30	24 @ V _{GS} = 10V	5.7		
30	$33 @ V_{GS} = 4.5V$	4.9		

Typical Applications:

- Power Routing
- · Li Ion Battery Packs
- · Level Shifting and Driver Circuits







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter			Symbol	Limit	Units	
Drain-Source Voltage			V_{DS}	30	V	
Gate-Source Voltage			V_{GS}	±20	V	
Continuous Dunin Commental	Т	_A =25°C	ı	5.7		
Continuous Drain Current ^a	T _A =70		I _D	4.5	Α	
Pulsed Drain Current ^b			I _{DM}	23		
Continuous Source Current (Diode Conduction) ^a			Is	1.6	Α	
Device Discipation a	Т	_A =25°C	P_{D}	1.15	W	
Power Dissipation ^a	Т	_A =70°C	гD	0.7		
Operating Junction and Storage Temperature Range			T_J , T_{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	110	°C/W		
IMAXIMUM SUNCTION-AMBIENT	Steady State	VθJA	150			

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

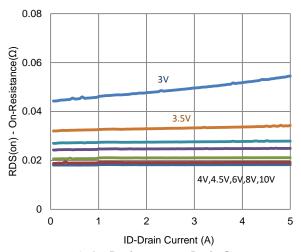
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zelo Gale Voltage Dialii Cullent	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	10		10	T UA	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	8			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 4 \text{ A}$			24	mO	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_{D} = 3 \text{ A}$			33	mΩ	
Forward Transconductance a	g_{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 4 \text{ A}$		16		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 0.8 \text{ A}, V_{GS} = 0 \text{ V}$		0.76		V	
	Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		4.9			
Gate-Source Charge	Q_gs	$I_{DS} = 13 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 4 \text{ A}$		1.2		nC	
Gate-Drain Charge	Q_gd	1D = 477		2.0			
Turn-On Delay Time	$t_{d(on)}$	V 45 V D = 2.0 O		2			
Rise Time	t _r	$V_{DS} = 15 \text{ V}, R_L = 3.8 \Omega,$ $I_D = 4 \text{ A},$		6		no	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		20		ns	
Fall Time	t _f	VGEN = 10 V, NGEN 0 12		5			
Input Capacitance	C_{iss}			437			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		64		pF	
Reverse Transfer Capacitance	C_{rss}			50			

Notes

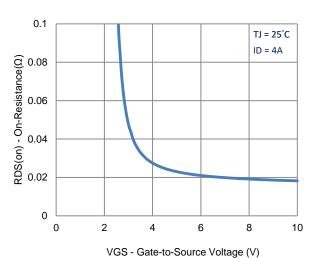
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing.

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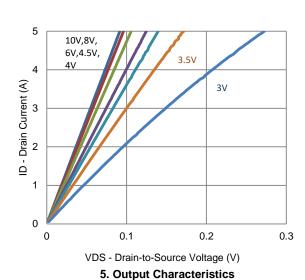
Typical Electrical Characteristics

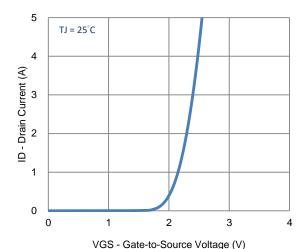


1. On-Resistance vs. Drain Current

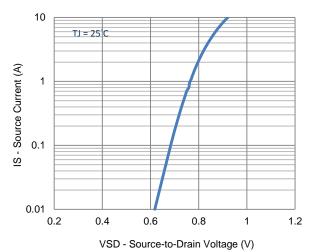


3. On-Resistance vs. Gate-to-Source Voltage

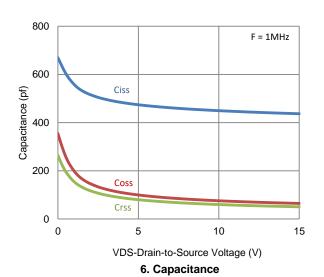




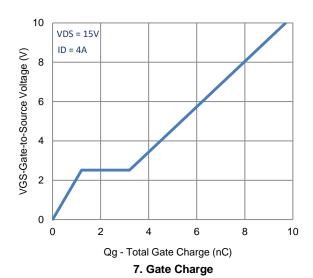
2. Transfer Characteristics

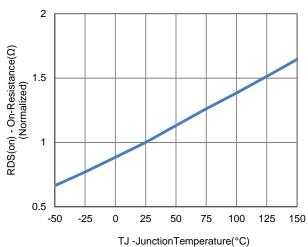


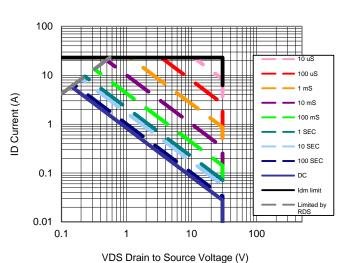
4. Drain-to-Source Forward Voltage



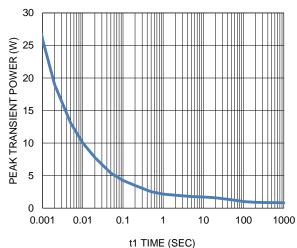
Typical Electrical Characteristics





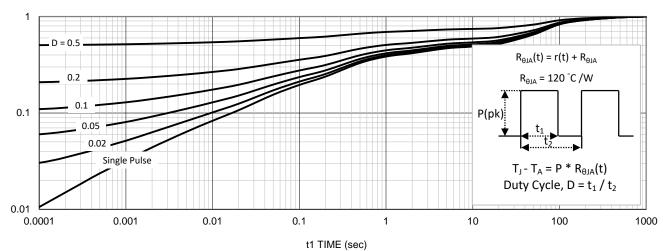


8. Normalized On-Resistance Vs Junction Temperature



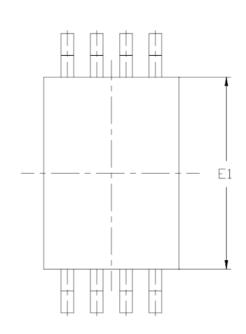
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation

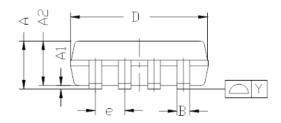


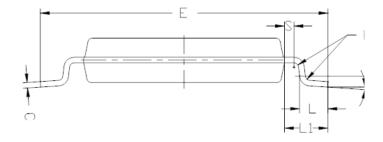
11. Normalized Thermal Transient Junction to Ambient

Package Information



DIM	MILLIMETERS				
DIM.	MIN.	N□M.	MAX.		
Α	1.05	1.10	1.20		
A(1)	0.05	0.10	0.15		
A(2)	0.99	1.02	1.05		
В	0.19	0.25	0,30		
С		0.127			
D	2.90	3.00	3.10		
E	6.20	6.40	6.60		
E1	4.30	4.40	4.50		
е	0.65BSC				
L	0.45	0.60	0.75		
L1	0.90	1.00	1.10		
Υ			0.10		
θ1	0°	4°	8°		
R	0.09				
S	0.20				





Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 1. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.

R 752.0