N-Channel 20-V (D-S) MOSFET

Key Features:

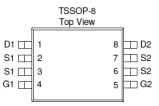
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

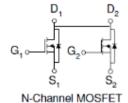
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY					
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)			
20	$22 @ V_{GS} = 4.5V$	6.8			
	$30 @ V_{GS} = 2.5V$	5.8			
	46 @ V _{GS} = 1.8V	4.7			







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter			Symbol	Limit	Units
Drain-Source Voltage				20	V
Gate-Source Voltage			V_{GS}	±8	V
Continuous Drain Current ^a		_A =25°C	. 1	6.8	А
Continuous Drain Current	ī	_A =70°C	I _D	5.5	
Pulsed Drain Current ^b			I _{DM}	30	
Continuous Source Current (Diode Conduction) ^a		Is	2.1	Α	
Davier Dissipation 8		_A =25°C	P _D	1.5	W
Power Dissipation ^a	Т	_A =70°C	гD	1	
Operating Junction and Storage Temperature Range				-55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	83	°C/W		
Maximum Junction-to-Ambient	Steady State	IXOJA	120	C/VV		

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

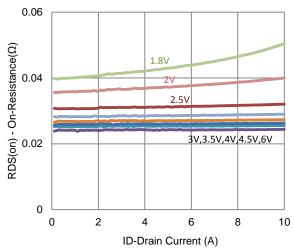
Parameter	Symbol	nbol Test Conditions		Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25		
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	10			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 5.4 \text{ A}$			22		
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 4.4 \text{ A}$			30) mΩ	
		$V_{GS} = 1.8 \text{ V}, I_D = 3.6 \text{ A}$			48		
Forward Transconductance	g_{fs}	$V_{DS} = 10 \text{ V}, I_D = 5.4 \text{ A}$		26		S	
Diode Forward Voltage	V_{SD}	$I_S = 1.1 A, V_{GS} = 0 V$		0.73		V	
Dynamic							
Total Gate Charge	Q_g	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$		8		nC	
Gate-Source Charge	Q_gs	$I_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 5.4 \text{ A}$		1.1			
Gate-Drain Charge	Q_gd	ID = 0.4 A		1.9			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 10 \text{ V}, R_{L} = 1.9 \Omega,$		12			
Rise Time	t _r	$I_{DS} = 10 \text{ V}, \text{ NL} = 1.9 \Omega,$ $I_{D} = 5.4 \text{ A},$		19		ns	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		41			
Fall Time	t _f	V GEN - 7.0 V, NGEN - 0 12		11			
Input Capacitance	C _{iss}			633			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		65		pF	
Reverse Transfer Capacitance	C_{rss}]		45			

Notes

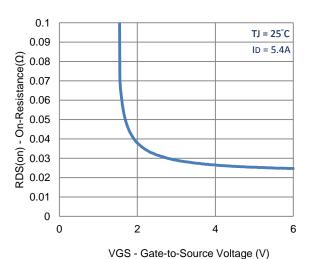
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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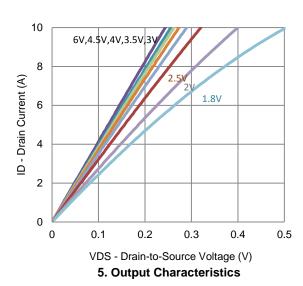
Typical Electrical Characteristics

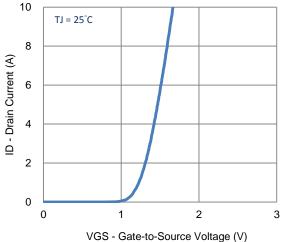


1. On-Resistance vs. Drain Current

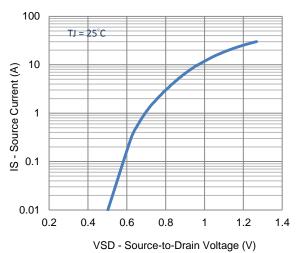


3. On-Resistance vs. Gate-to-Source Voltage

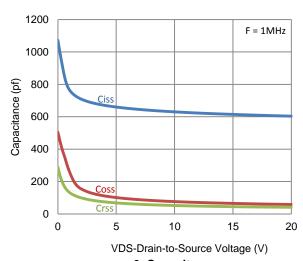




2. Transfer Characteristics



4. Drain-to-Source Forward Voltage

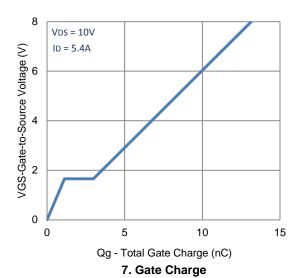


Typical Electrical Characteristics

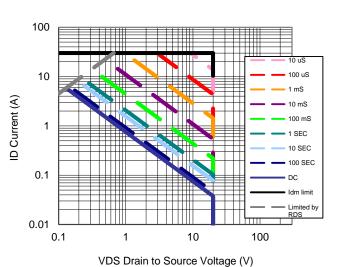
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1.5

 $RDS(on) - On-Resistance(\Omega)$ (Normalized)

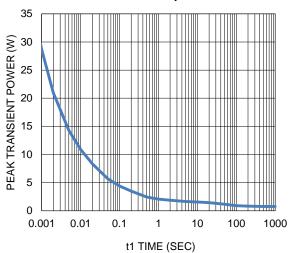


0.5 -50 -25 0 25 50 75 100 125 TJ -JunctionTemperature(°C)



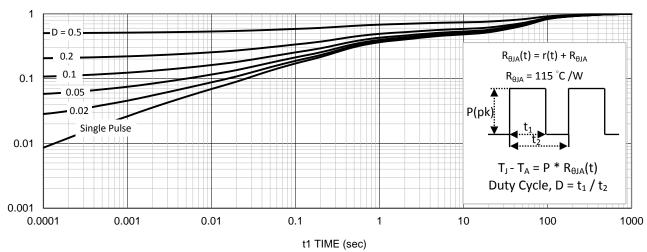
8. Normalized On-Resistance Vs
Junction Temperature

150



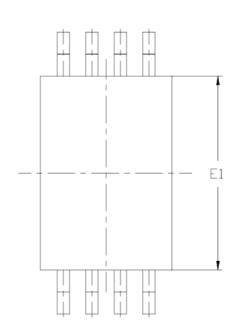
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation

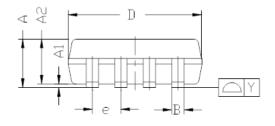


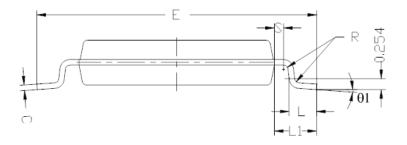
11. Normalized Thermal Transient Junction to Ambient

Package Information



DIM	MILLIMETERS					
DIM.	MIN.	N□M.	MAX.			
А	1.05	1.10	1.20			
A(1)	0.05	0.10	0.15			
A(2)	0.99	1.02	1.05			
В	0.19	0.25	0,30			
С		0.127				
D	2.90	3.00	3.10			
Ε	6.20	6.40	6.60			
E1	4,30	4.40	4.50			
6	0.65BSC					
L	0.45	0.60	0.75			
L1	0.90	1.00	1.10			
Υ			0.10			
θ1	0°	4°	8°			
R	0.09					
S	0.20					





Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.