Dual N-Channel 12-V (D-S) MOSFET

Key Features:

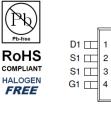
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

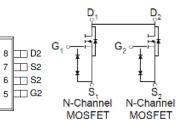
Typical Applications:

- Power Routing
- Li Ion Battery Packs
- Level Shifting and Driver Circuits

| PRODUCT SUMMARY | | | | |
|-----------------|-----------------------------|-------|--|--|
| VDS (V) | $r_{DS(on)}(m\Omega)$ | I⊳(A) | | |
| 16 | 11 @ V _{GS} = 4.5V | 9.6 | | |
| 10 | 13 @ V _{GS} = 2.5V | 8.8 | | |

TSSOP-8 Top View







| ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED) | | | | | |
|--|----------------------|-----------------------------------|------------|-------|--|
| Parameter | | Symbol | Limit | Units | |
| Drain-Source Voltage | | V _{DS} | 16 | V | |
| Gate-Source Voltage | V _{GS} | ±8 | V | | |
| Continuous Drain Current ^a | T _A =25°C | 1 | 9.6 | А | |
| | T _A =70°C | I _D | 7.8 | | |
| Pulsed Drain Current ^b | I _{DM} | 40 | | | |
| Continuous Source Current (Diode Conduction) ^a | | I _S | 2.2 | А | |
| Power Dissipation ^a | T _A =25°C | PD | 1.5 | W | |
| Power Dissipation ^a | T _A =70°C | ' ^D 1 | | ٧V | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | -55 to 150 | °C | |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|--------------|--------------------|---------|-------|--|
| Parameter | | Symbol | Maximum | Units | |
| Maximum Junction-to-Ambient ^a | t <= 10 sec | R _{eja} | 83 | °C/W | |
| | Steady State | ιν _θ ja | 120 | C/VV | |

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

| Parameter | Symbol | Test Conditions | Min | Тур | Мах | Unit | |
|---|------------------------|--|-----|------|-----|-------|--|
| | | Static | | | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \text{ uA}$ | 0.4 | | | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 8 V$ | | | ±10 | uA | |
| Zero Gate Voltage Drain Current | | $V_{DS} = 12.8V, V_{GS} = 0 V$ | | | 1 | uA | |
| Zero Gale Voltage Drain Guirent | IDSS | $V_{DS} = 12.8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$ | | | 10 | uл | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$ | 14 | | | А | |
| Drain-Source On-Resistance ^a | r | $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 4.8 \text{ A}$ | | | 11 | mΩ | |
| Drain-Source On-Resistance | r _{DS(on)} | $V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 4.4 \text{ A}$ | | | 13 | 11122 | |
| Forward Transconductance ^a | g _{fs} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 4.8 \text{ A}$ | | 54 | | S | |
| Diode Forward Voltage ^a | V_{SD} | $I_{S} = 1.1 \text{ A}, V_{GS} = 0 \text{ V}$ | | 0.65 | | V | |
| | Dynamic ^b | | | | | | |
| Total Gate Charge | Qg | $V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ | | 21 | | | |
| Gate-Source Charge | Q_gs | $V_{DS} = 10 V, V_{GS} = 4.3 V,$ $I_{D} = 4.8 A$ | | 1.7 | | nC | |
| Gate-Drain Charge | Q_{gd} | 10 - 4.0 / 1 | | 6.0 | | | |
| Turn-On Delay Time | t _{d(on)} | V _{DS} = 10 V, R _L = 2.1 Ω, | | 273 | | | |
| Rise Time | t _r | $V_{DS} = 10^{\circ} V, K_{L} = 2.1 \Omega_{2},$ $I_{D} = 4.8 \text{ A},$ | | 890 | | nc | |
| Turn-Off Delay Time | t _{d(off)} | $V_{GEN} = 4.5 \text{ V}, \text{ R}_{GEN} = 6 \Omega$ | | 4870 | | ns | |
| Fall Time | t _f | $v_{\text{GEN}} = 4.3 v, v_{\text{GEN}} = 0.22$ | | 3110 | | | |
| Input Capacitance | C _{iss} | | | 1257 | | | |
| Output Capacitance | C _{oss} | $V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$ | | 257 | | pF | |
| Reverse Transfer Capacitance | C _{rss} | | | 234 | | | |

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

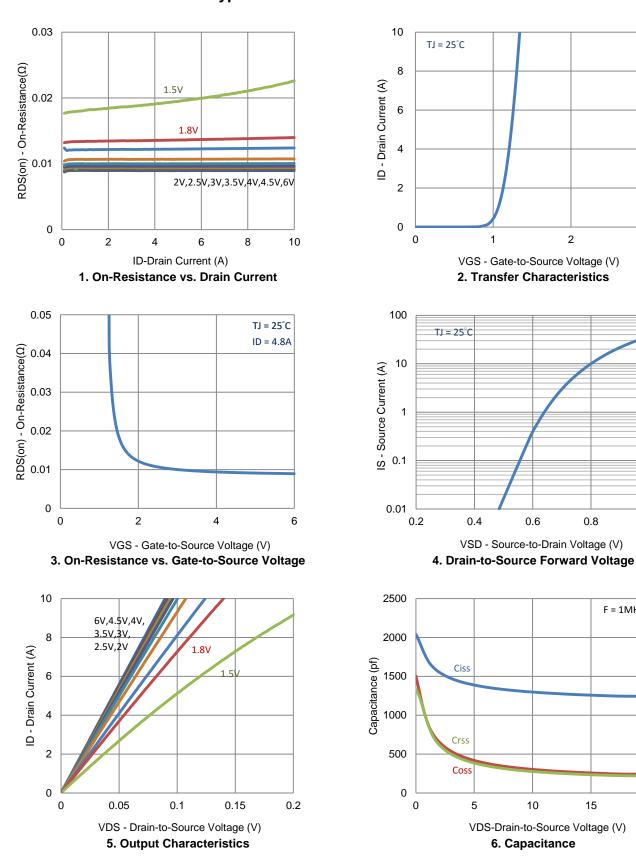
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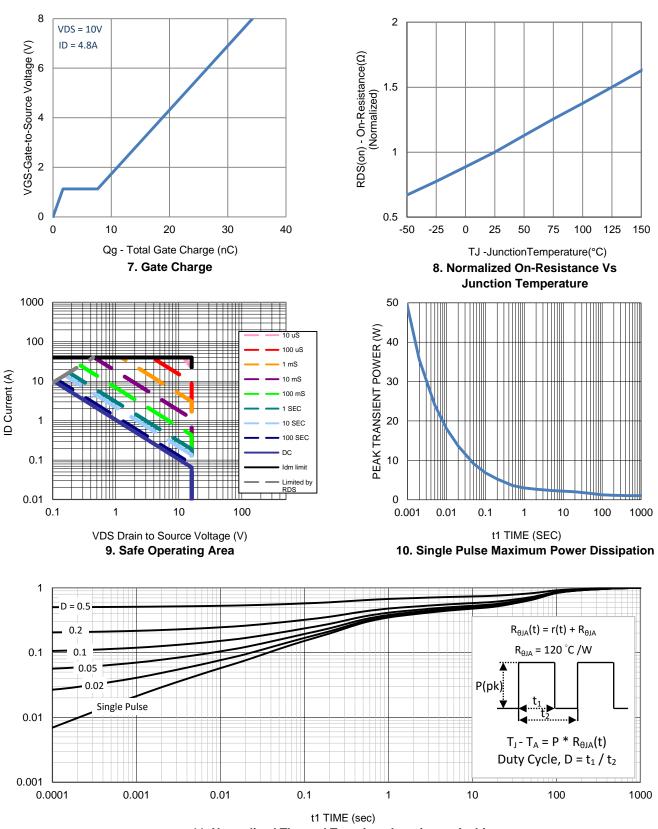
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F = 1MHz



Typical Electrical Characteristics

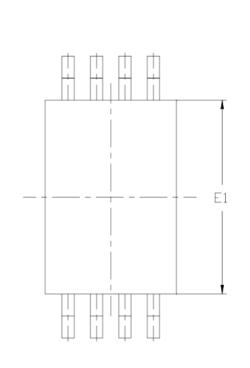
Publication Order Number: DS_AM6900NHE_1A



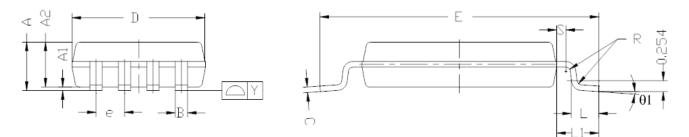
Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information



| T. T. J. | MILLIMETERS | | | | |
|----------|-------------|-------|------|--|--|
| DIM. | MIN. NOM. | | MAX. | | |
| A | 1.05 | 1.10 | 1.20 | | |
| A(1) | 0.05 | 0.10 | 0.15 | | |
| A(2) | 0.99 | 1.02 | 1.05 | | |
| В | 0.19 | 0.25 | 0.30 | | |
| С | | 0.127 | | | |
| D | 2.90 | 3.00 | 3.10 | | |
| E | 6.20 | 6.40 | 6.60 | | |
| E1 | 4.30 | 4.40 | 4.50 | | |
| e | 0.65BSC | | | | |
| L | 0.45 | 0.60 | 0.75 | | |
| L1 | 0.90 | 1.00 | 1.10 | | |
| Y | | | 0.10 | | |
| θ1 | 0° | 4° | 8° | | |
| R | 0.09 | | | | |
| S | 0.20 | | | | |



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.