N-Channel 500-V (D-S) MOSFET

Key Features:

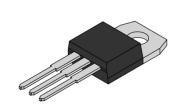
- Low r_{DS(on)} technology
- · Low thermal impedance
- · Fast switching speed

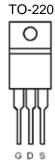
Typical	Application	s:
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- Electronic ballast
- · Electronic transformer
- Switch mode power supply

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)		
500	1.5 @ V _{GS} = 10V	4.5		







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)							
Parameter			Limit	Units			
Drain-Source Voltage			500	V			
Gate-Source Voltage	V_{GS}	±20	V				
Continuous Drain Current ^a T _C =25°C			4.5	Α			
Pulsed Drain Current ^b	I _{DM}	18	Α				
Continuous Source Current (Diode Conduction) a	I _S	4.5	Α				
Power Dissipation ^a	T _C =25°C	P_{D}	74	W			
Operating Junction and Storage Temperature Range			-55 to 175	°C			

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	62.5	°C/W			
Maximum Junction-to-Case	$R_{\theta JC}$	1.7	C/VV			

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Notes

- a. Package limited
- b. Pulse width limited by maximum junction temperature

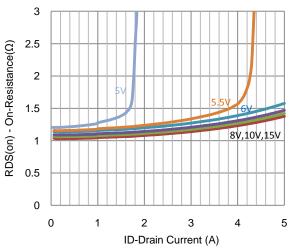
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $ID = 250 \text{ uA}$	2		4	V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	lass	$V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V}$			25	uA	
Zero Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			250		
On-State Drain Current	I _{D(on)}	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}$	5			Α	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 2.7 \text{ A}$			1.5	Ω	
Forward Transconductance	g _{fs}	$V_{DS} = 50 \text{ V}, I_{D} = 2.7 \text{ A}$		2.5		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 4.5 \text{ A}, V_{GS} = 0 \text{ V}$		1.6		V	
		Dynamic					
Total Gate Charge	Q_g			26			
Gate-Source Charge	Q_{gs}	$V_{DS} = 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3.1 \text{ A}$		4		nC	
Gate-Drain Charge	Q_{gd}			15			
Turn-On Delay Time	t _{d(on)}			12.8			
Rise Time	t _r	$V_{DD} = 250 \text{ V}, R_L = 79 \Omega, I_D = 3.1 \text{ A},$		7.4		nc	
Turn-Off Delay Time	$t_{d(off)}$	V_{GEN} = 10 V, R_{GEN} = 12 Ω		38.0		ns	
Fall Time	t _f			19.6			
Input Capacitance	C _{iss}			623			
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{MHz}$		112		pF	
Reverse Transfer Capacitance	C_{rss}			24			

Notes

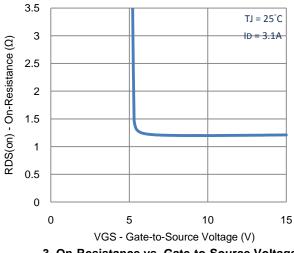
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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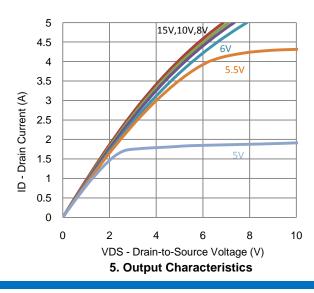
Typical Electrical Characteristics

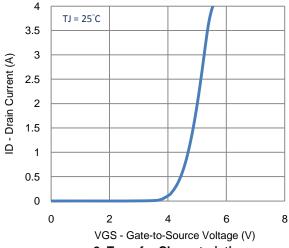


1. On-Resistance vs. Drain Current

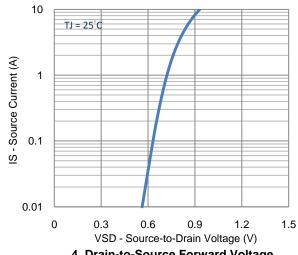


3. On-Resistance vs. Gate-to-Source Voltage

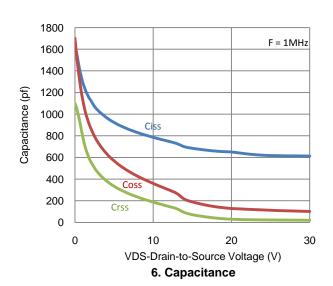




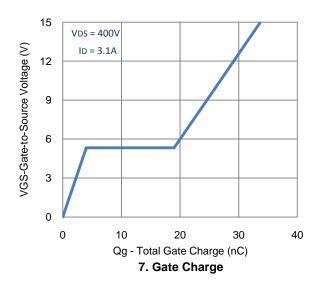
2. Transfer Characteristics

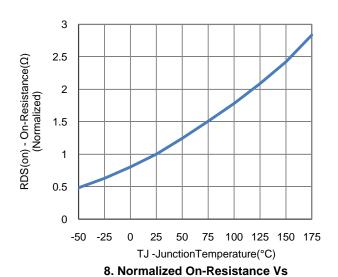


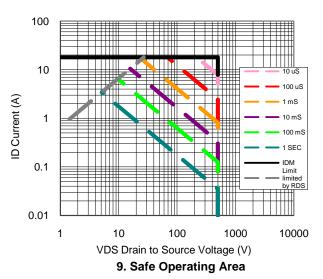
4. Drain-to-Source Forward Voltage

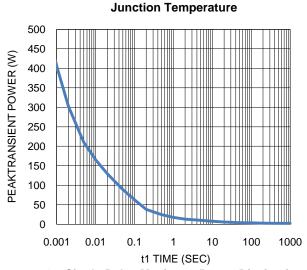


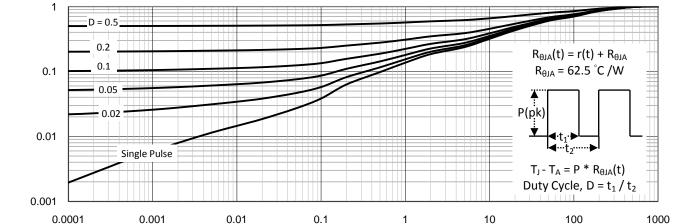
Typical Electrical Characteristics











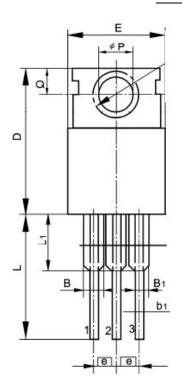
10. Single Pulse Maximum Power Dissipation

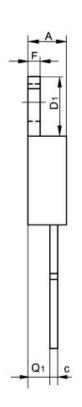
t1 TIME (sec)

11. Normalized Thermal Transient Junction to Ambient

Package Information

TO-220





Symbol	Min	Max	Typical	Symbol	Min	Max	Typical
Α	4	4.8		Е	9.9	10.7	1
В	1.2	1.4		е	1	1	1
B1	1	1.3		F	1.1	1.4	1
'b1	0.65	1		L	12.5	14.5	
С	0.4	0.55		L1	3	4	
D	15	16.5		Q	2.5	3	
D1	5.9	6.9		Q1	2	2.9	1
				Р			3.8