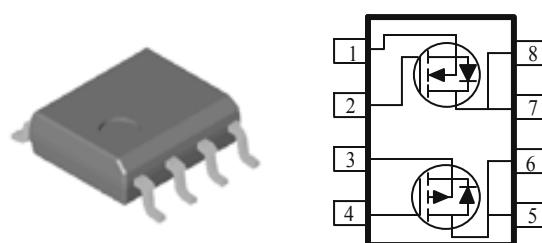


## P & N-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low  $r_{DS(on)}$  provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY		
V <sub>DS</sub> (V)	r <sub>DS(on)</sub> m(Ω)	I <sub>D</sub> (A)
30	31 @ V <sub>GS</sub> = 4.5V	6.9
-30	33 @ V <sub>GS</sub> = -4.5V	-6.8



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)				
Parameter	Symbol	N-Channel	P-Channel	Units
Drain-Source Voltage	V <sub>DS</sub>	30	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±12	±25	
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C	I <sub>D</sub>	6.9	A
	T <sub>A</sub> =70°C	I <sub>D</sub>	5.4	
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	20	-50	
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	1.3	-2.1	A
Power Dissipation <sup>a</sup>	T <sub>A</sub> =25°C	P <sub>D</sub>	2.1	W
	T <sub>A</sub> =70°C	P <sub>D</sub>	1.3	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150		°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Maximum	Units	
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	R <sub>θJA</sub>	62.5	°C/W
	Steady-State		110	°C/W

### Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

SPECIFICATIONS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Test Conditions		Limits			Unit
		Ch	Min	Typ	Max		
<b>Static</b>							
Gate-Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS} = V_{DS}, I_D = 250 \mu\text{A}$	N	0.6			V
		$V_{GS} = V_{DS}, I_D = -250 \mu\text{A}$	P	-1.0			
Gate-Body Leakage	$I_{GSS}$	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$	P		$\pm 100$		$\text{nA}$
		$V_{GS} = 12 \text{ V}, V_{DS} = 0 \text{ V}$	N		$\pm 100$		
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$	P		-1		$\text{uA}$
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	N		1		
On-State Drain Current <sup>A</sup>	$I_{D(\text{on})}$	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N	20			A
		$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P	-20			
Drain-Source On-Resistance <sup>A</sup>	$r_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}, I_D = 6.9 \text{ A}$	N		31		$\text{m}\Omega$
		$V_{GS} = -4.5 \text{ V}, I_D = -68 \text{ A}$	P		33		
Forward Tranconductance <sup>A</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}, I_D = 6.9 \text{ A}$	N		25		S
		$V_{DS} = -15 \text{ V}, I_D = -8.5 \text{ A}$	P		10		
Pulsed Source Current (Body Diode) <sup>A</sup>	$I_{SM}$					5	A
<b>Dynamic</b>							
Total Gate Charge	$Q_g$	N-Channel $V_{DS}=15\text{V}, V_{GS}=4.5\text{V}, I_D=6.9\text{A}$ P-Channel $V_{DS}=-15\text{V}, V_{GS}=-4.5\text{V}, I_D=-8.5\text{A}$	N	6.0			nC
Gate-Source Charge	$Q_{gs}$		P	13			
Gate-Drain Charge	$Q_{gd}$		N	1.0			
			P	5.8			
			N	1.5			
			P	12.0			
<b>Switching</b>							
Turn-On Delay Time	$t_{d(\text{on})}$	N-Chaneel $V_{DD}=15\text{V}, V_{GS}=4.5\text{V}, I_D=1\text{A}$ , $R_{GEN}=6\Omega$ , P-Channel $V_{DD}=-15\text{V}, V_{GS}=-4.5\text{V}, I_D=-1\text{A}$ $R_{GEN}=6\Omega$	N	7.4			nS
Rise Time	$t_r$		P	15			
Turn-Off Delay Time	$t_{d(\text{off})}$		N	4			
Fall-Time	$t_f$		P	16			
			N	22.2			
			P	62			
			N	3.6			
			P	46			

## Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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### Typical Electrical Characteristics (P-Channel)

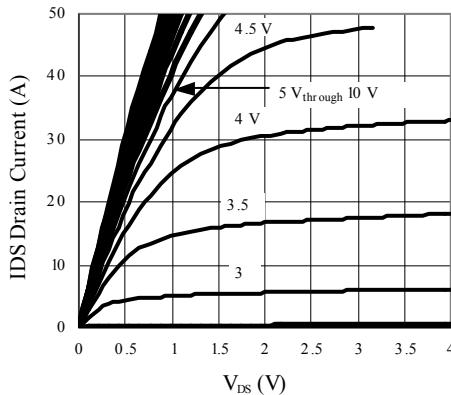


Figure 1. On-Region Characteristics

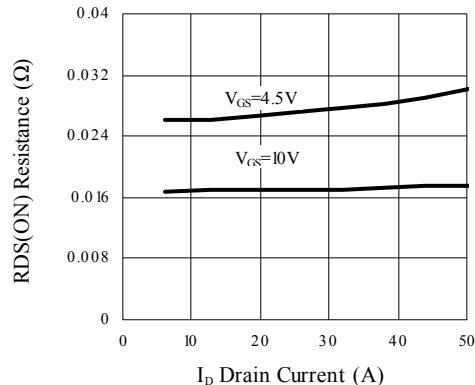


Figure 2. On-Resistance with Drain Current

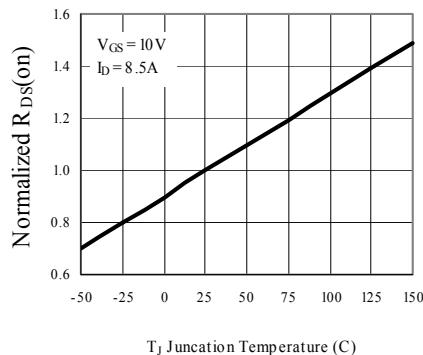


Figure 3. On-Resistance Variation with Temperature

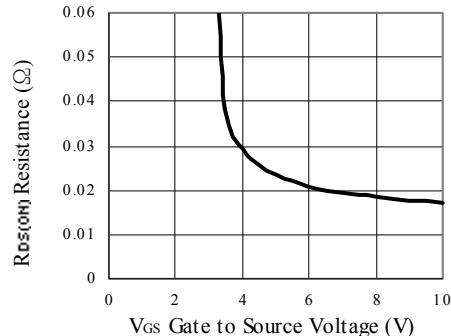


Figure 4. On-Resistance Variation with Gate to Source Voltage

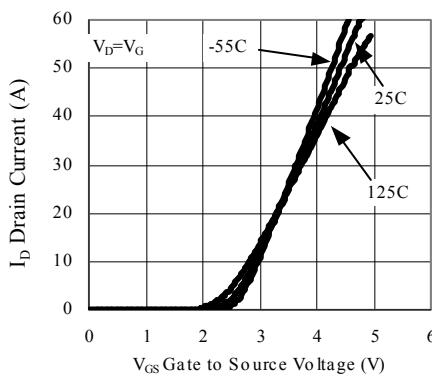


Figure 5. Transfer Characteristics

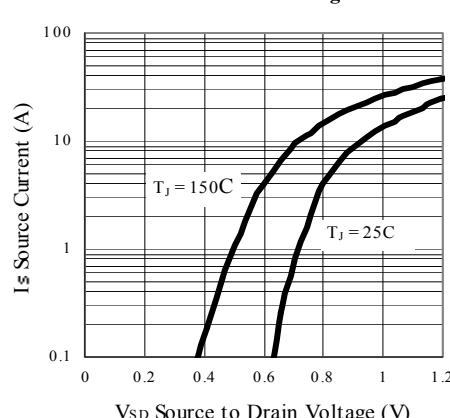


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

### Typical Electrical Characteristics (P-Channel)

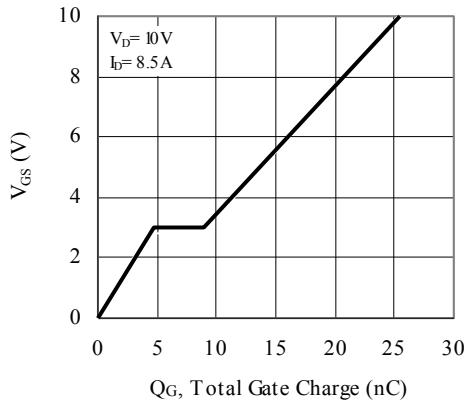


Figure 7. Gate Charge Characteristics

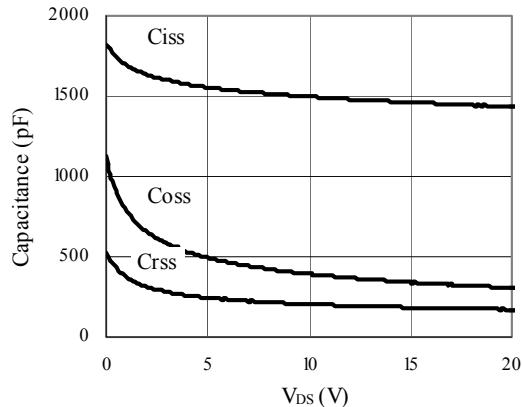


Figure 8. Capacitance Characteristics

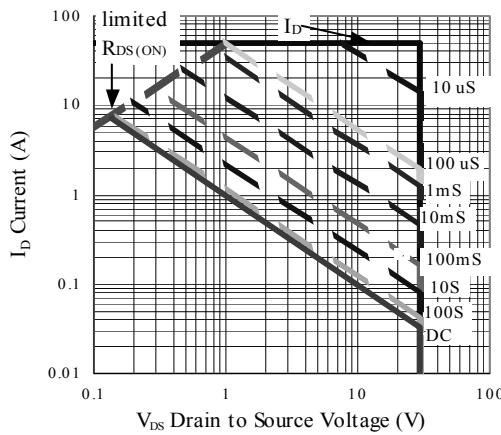


Figure 9. Maximum Safe Operating Area

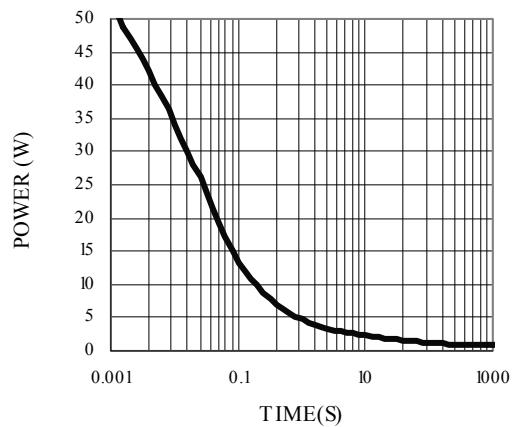


Figure 10. Single Pulse Maximum Power Dissipation

### Normalized Thermal Transient Junction to Ambient

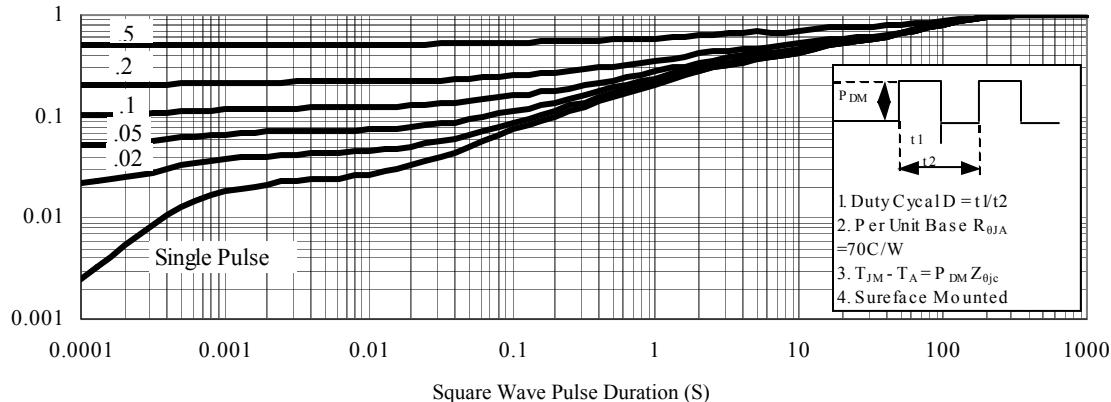
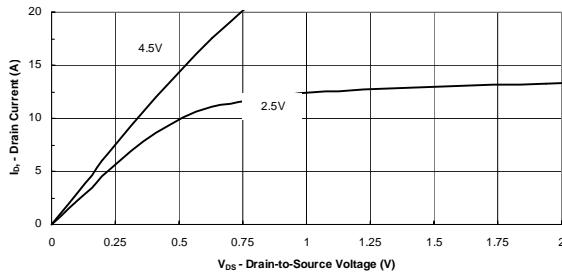
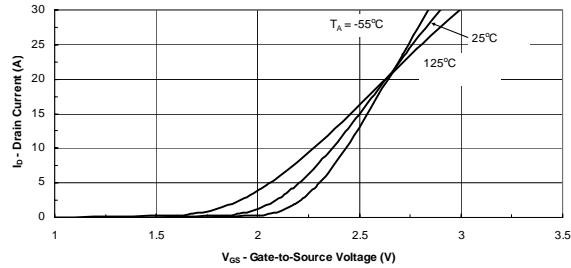


Figure 11. Transient Thermal Response Curve

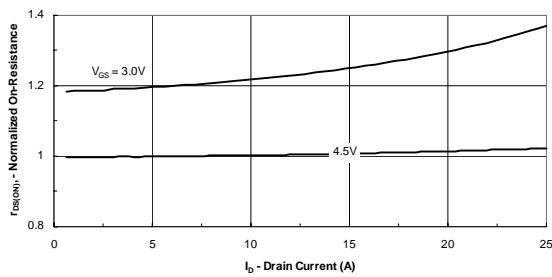
## Typical Electrical Characteristics (N-Channel)



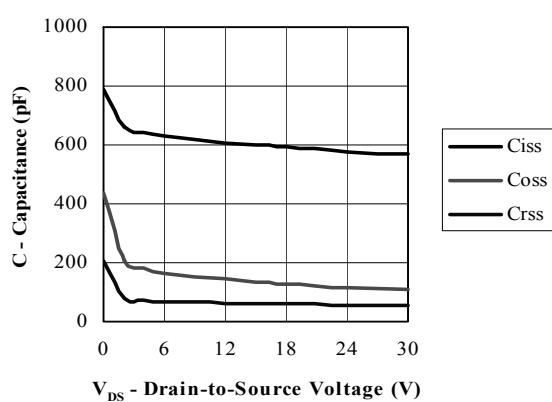
Output Characteristics



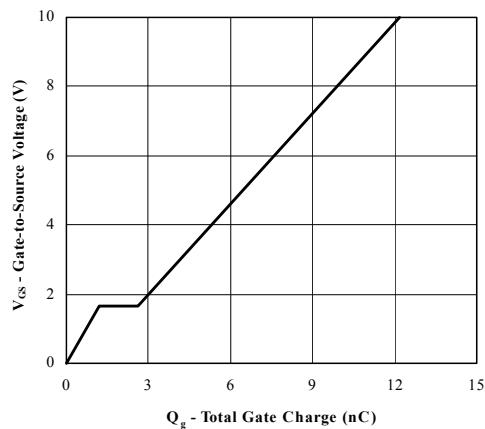
Transfer Characteristics



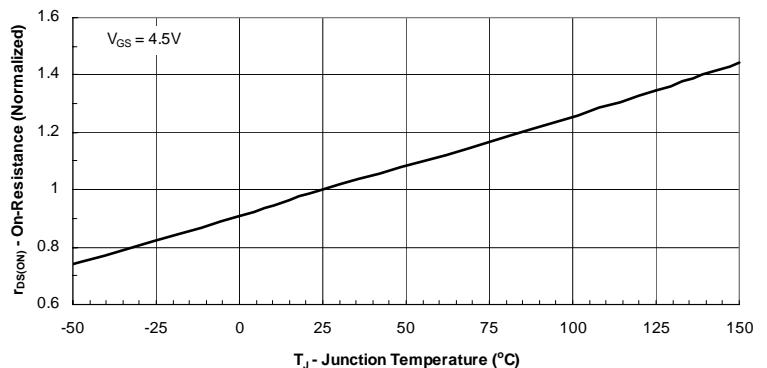
On-Resistance vs. Drain Current



Capacitance

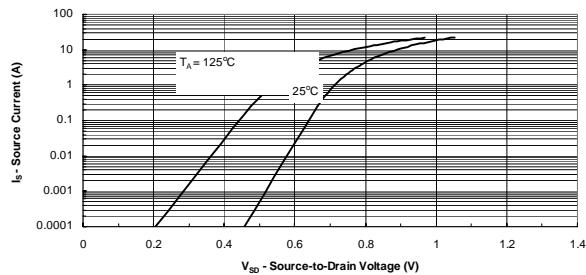


Gate Charge

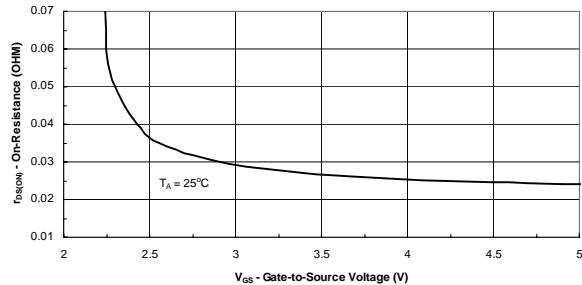


On-Resistance vs. Junction Temperature

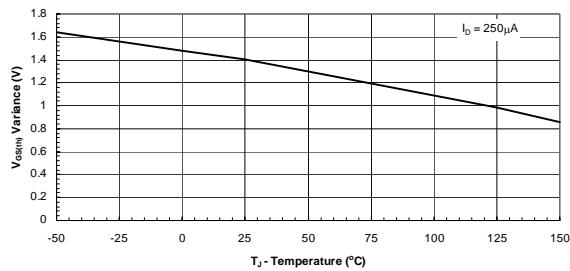
## Typical Electrical Characteristics (N-Channel)



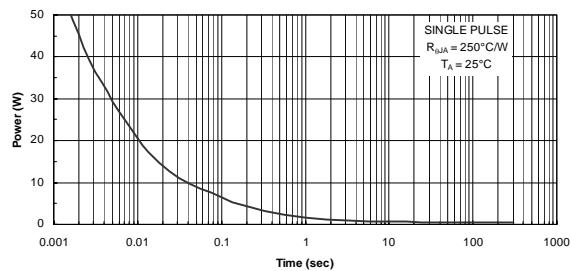
Source-Drain Diode Forward Voltage



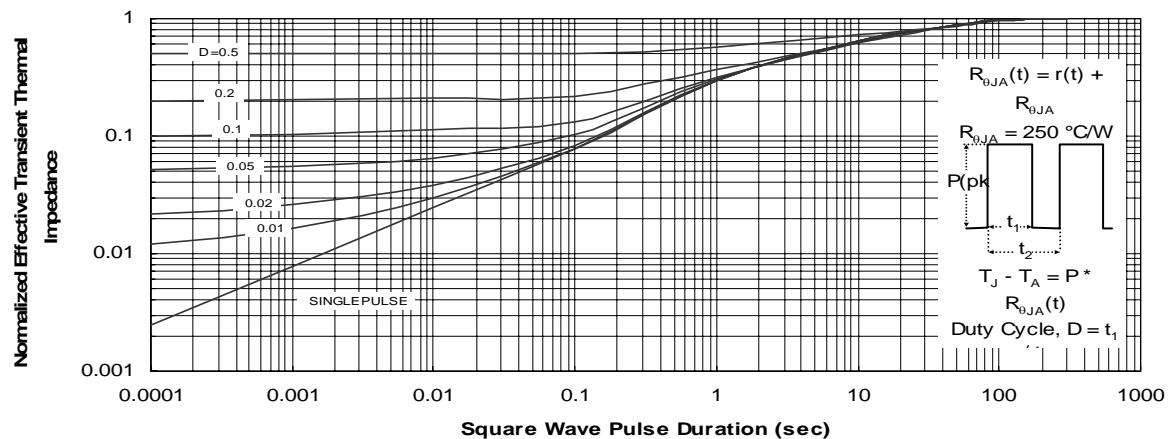
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



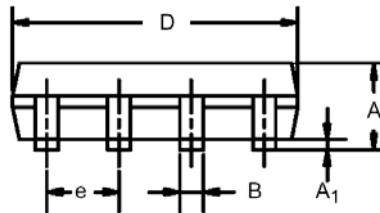
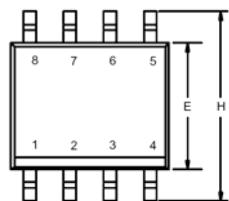
Single Pulse Power



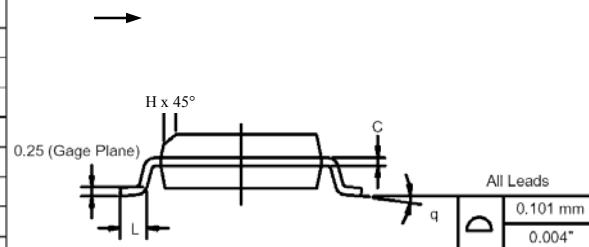
Normalized Thermal Transient Impedance, Junction-to-Ambient

## Package Information

## SO-8: 8LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
<b>A</b>	1.35	1.75	0.053	0.069
<b>A<sub>1</sub></b>	0.10	0.20	0.004	0.008
<b>B</b>	0.35	0.51	0.014	0.020
<b>C</b>	0.19	0.25	0.0075	0.010
<b>D</b>	4.80	5.00	0.189	0.196
<b>E</b>	3.80	4.00	0.150	0.157
<b>e</b>	1.27 BSC		0.050 BSC	
<b>H</b>	5.80	6.20	0.228	0.244
<b>h</b>	0.25	0.50	0.010	0.020
<b>L</b>	0.50	0.93	0.020	0.037
<b>q</b>	0°	8°	0°	8°



# Ordering information

- AM4502AC-T1-XX
  - A: Analog Power
  - M: MOSFET
  - 4502A: Part number
  - C: Complementary
  - T1: Tape & reel
  - XX: Blank: Standard  
PF: Leadfree