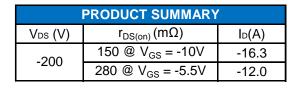
P-Channel 200-V (D-S) MOSFET

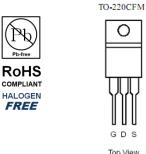
Key Features:

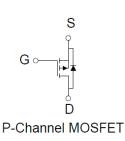
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits







Top View

GΡ S

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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)									
Parameter			Limit	Units					
Drain-Source Voltage				V					
Gate-Source Voltage				V					
Continuous Drain Current ^a	T _C =25°C	I _D	-16.3	٨					
Pulsed Drain Current ^b		I _{DM}	-60	A					
Continuous Source Current (Diode Conduction) ^a		ا _S	-59	А					
Power Dissipation ^a	T _C =25°C	PD	60	W					
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C					

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient ^a	R _{θJA}	62.5	°C/W			
Maximum Junction-to-Case	$R_{ extsf{ heta}JC}$	1	0/11			

Notes

- Surface Mounted on 1" x 1" FR4 Board. a.
- Pulse width limited by maximum junction temperature b.

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-1			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current		$V_{DS} = -160 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-1	uA		
	I _{DSS}	$V_{DS} = -160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-25			
On-State Drain Current	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	-30			Α		
Drain-Source On-Resistance	r	V _{GS} = -10 V, I _D = -13 A			150	mΩ		
	r _{DS(on)}	$V_{GS} = -5.5 \text{ V}, \text{ I}_{D} = -10.4 \text{ A}$			280			
Forward Transconductance	g _{fs}	V _{DS} = -15 V, I _D = -13 A		30		S		
Diode Forward Voltage	V_{SD}	I _S = -28.5 A, V _{GS} = 0 V		-0.86		V		
Dynamic								
Total Gate Charge	Qg	$V_{DS} = -100 \text{ V}, V_{GS} = -5.5 \text{ V},$ $I_{D} = -13 \text{ A}$		134		nC		
Gate-Source Charge	Q_gs			53				
Gate-Drain Charge	Q_{gd}			55				
Turn-On Delay Time	t _{d(on)}	V_{DS} = -100 V, R _L = 7.7 Ω, I _D = -13 A, V _{GEN} = -10 V, R _{GEN} = 6 Ω		26		ns		
Rise Time	t _r			52				
Turn-Off Delay Time	t _{d(off)}			286				
Fall Time	t _f			136				
Input Capacitance	C _{iss}	V _{DS} = -15 V, V _{GS} = 0 V, f = 1 MHz		10930		pF		
Output Capacitance	C _{oss}			496				
Reverse Transfer Capacitance	C _{rss}			348				

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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0.8

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6. Capacitance

1

1.2

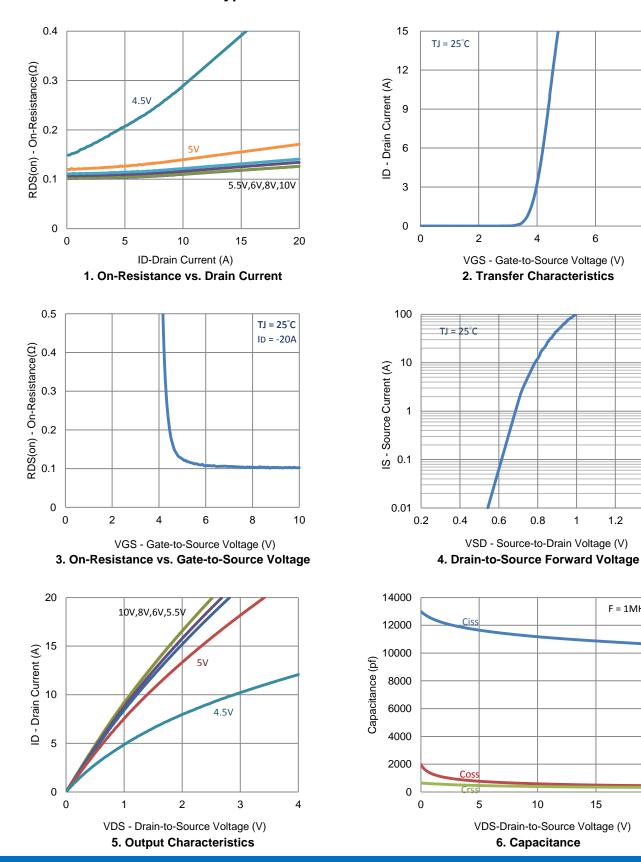
F = 1MHz

1.4

20

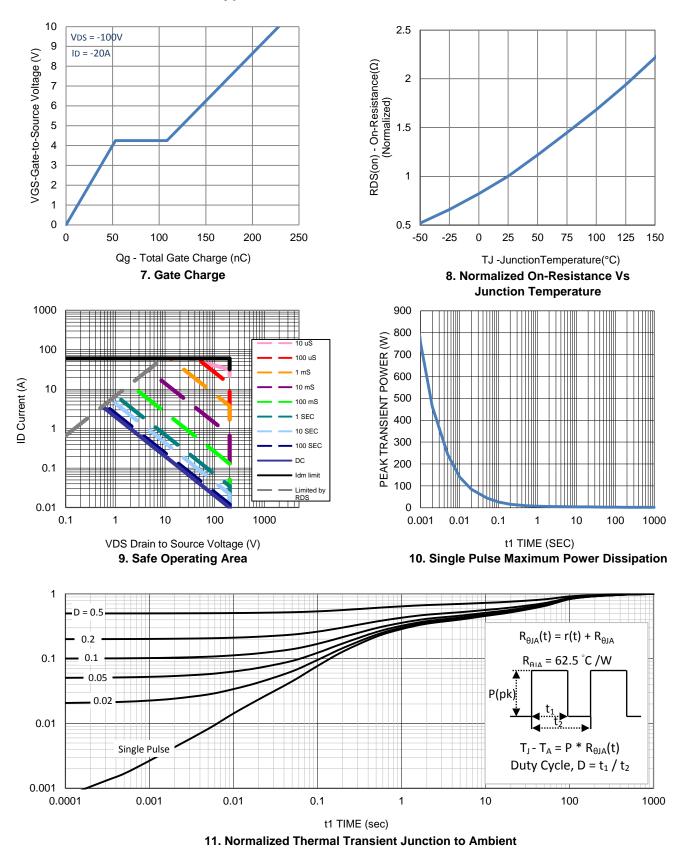
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Typical Electrical Characteristics

15



Typical Electrical Characteristics

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Package Information

