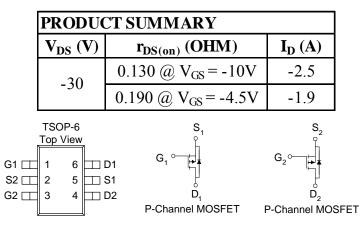
Analog Power

P-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are DC-DC converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} Provides Higher Efficiency and Extends Battery Life
- Miniature TSOP-6 Surface Mount Package
 Saves Board Space



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C UNLESS OTHERWISE NOTED)							
Parameter		Symbol	Maximum	Units			
Drain-Source Voltage		V_{DS}	-30	V			
Gate-Source Voltage		V _{GS}	±20				
Cartineer Drain Communi ^a	T _A =25°C	T_	-2.5				
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	ID	-1.9	А			
Pulsed Drain Current ^b		I _{DM}	-10				
Continuous Source Current (Diode Conduction) ^a			±1.6	А			
	T _A =25°C	D.,	1.15	W			
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	0.7	vv			
Operating Junction and Storage Temperature Range		TJ, Tstg	-55 to 150	°C			

THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Тур	Max				
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{thJA}	93	110	⁰ C/W			
	Steady State		130	150	°C/W			

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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SPECIFICATIONS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)							
Parameter	Granhal		Limits			T T •4	
	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}$, $I_D = -250 \text{ uA}$	-1.00				
Gate-Body Leakage	Igss	$V_{DS} = 0 V, V_{GS} = +/-20 V$			±100	nA	
Zero Gate Voltage Drain Current	In an	$V_{DS} = -24 V, V_{GS} = 0 V$			-1	uA	
	Idss	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			-10		
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 V$, $V_{GS} = -10 V$	-3			Α	
Drain-Source On-Resistance ^A		$V_{GS} = -10 \text{ V}, \text{ I}_D = -2.5 \text{ A}$			0.130		
	rDS(on)	$V_{GS} = -4.5 \text{ V}, \text{ ID} = -1.9 \text{ A}$			0.190	Ω	
Forward Tranconductance ^A	gís	$V_{DS} = -5 V$, $I_D = -2.5 A$		3		S	
Diode Forward Voltage	Vsd	$I_{S} = -1.6 A, V_{GS} = 0 V$		-0.70		V	
Dynamic ^b							
Total Gate Charge	Qg	$V_{DS} = -5 V, V_{GS} = -4.5 V,$ $I_D = -2.5 A$		6.0		nC	
Gate-Source Charge	Qgs			0.80			
Gate-Drain Charge	Qgd			1.30			
Input Capacitance	Ciss	P-Channel V _{DS} =-15V, V _{GS} =0V, f=1MHz		451		pF	
Output Capacitance	Coss			130			
Reverse Transfer Capacitance	Crss			33			
Turn-On Delay Time	td(on)			6.5			
Rise Time	tr	$V_{DD} = -5 V, RL = 5 OHM,$ $V_{GEN} = -4.5 V, R_G = 6 OHM$		20		ns	
Turn-Off Delay Time	td(off)			31			
Fall-Time	tf			21]	

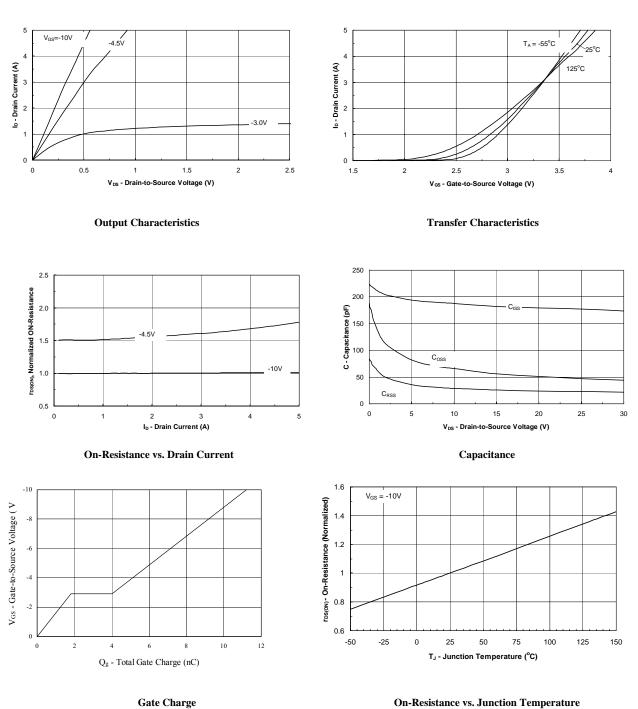
Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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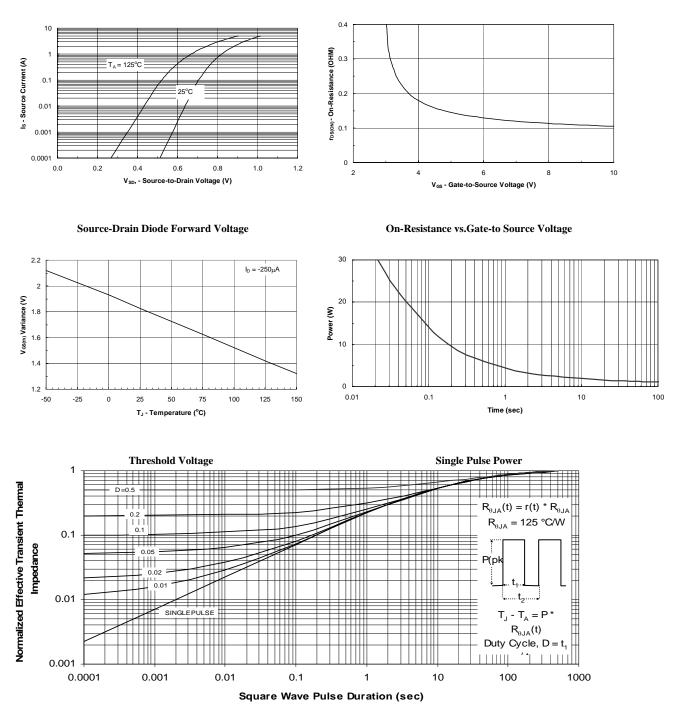
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Publication Order Number: DS-AM3993_B



Typical Electrical Characteristics (P-Channel)

On-Resistance vs. Junction Temperature



Typical Electrical Characteristics (P-Channel)

Normalized Thermal Transient Impedance, Junction-to-Ambient

© February, 2005 - Rev. A PRELIMINARY

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