Analog Power AM3448N

N-Channel 40-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

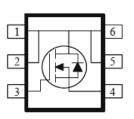
Typical Applications

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□ (A)	
40	16 @ V _{GS} = 10V	9.2	
40	18 @ $V_{GS} = 6V$	8.7	







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage			40	V	
Gate-Source Voltage	V_{GS}	±20	V		
Continuous Dusin Comment a	$T_A=25$ °C $T_A=70$ °C		9.2		
Continuous Drain Current ^a		l _D	7.4	Α	
Pulsed Drain Current ^b	I _{DM}	40	'		
Continuous Source Current (Diode Conduction) a		I _S	2.6	Α	
Down Dispination ⁹		P _D	2	W	
Power Dissipation ^a	$T_A=25$ °C $T_A=70$ °C	ı D	1.3	VV	
Operating Junction and Storage Temperature Range		T_J, T_{sta}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W	
Maximum Junction-to-Ambient	Steady State	IN _θ JΑ	110	C/VV	

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	1	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
Zelo Gate Voltage Brain Current	I _{DSS}	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	13.8			Α
Drain Cauras On Basistanas a	r	$V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$			16	mΩ
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 6 \text{ V}, I_{D} = 2 \text{ A}$			18	11122
Forward Transconductance a	g _{fs}	$V_{DS} = 20 \text{ V}, I_{D} = 3 \text{ A}$		12		S
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 1.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.78		V
		Dynamic ^b				
Total Gate Charge	Q_g	$V_{DS} = 20 \text{ V}, V_{GS} = 6 \text{ V},$		7		
Gate-Source Charge	Q_{gs}	$I_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V},$ $I_{D} = 2 \text{ A}$		3.3		nC
Gate-Drain Charge	Q_gd	10 - 2 A		1.5		
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 20 \text{ V}, R_L = 10 \Omega,$ $I_D = 2 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		7		
Rise Time	t _r			10		ne
Turn-Off Delay Time	$t_{d(off)}$			19		ns
Fall Time	t _f	V GEN = 10 V, 1 (GEN = 0.22		4		
Input Capacitance	C _{iss}	_		767		
Output Capacitance	C _{oss}	$V_{DS} = 20, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		120		pF
Reverse Transfer Capacitance	C_{rss}			13		

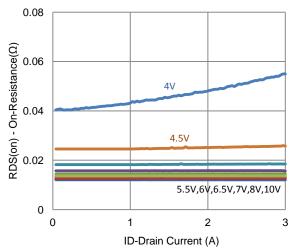
Notes

- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

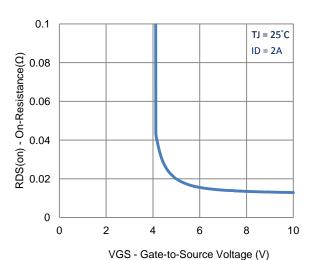
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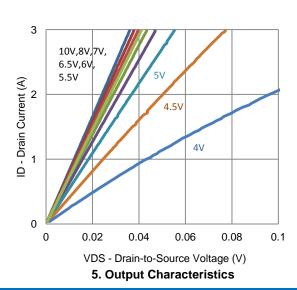
Typical Electrical Characteristics

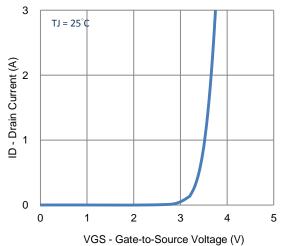


1. On-Resistance vs. Drain Current

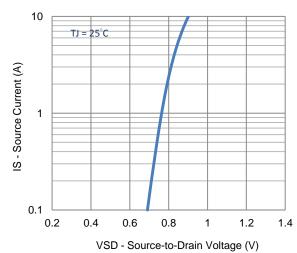


3. On-Resistance vs. Gate-to-Source Voltage

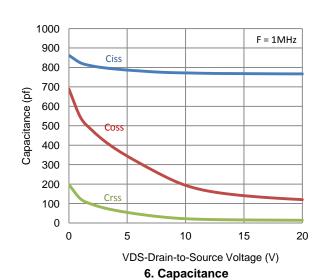




2. Transfer Characteristics



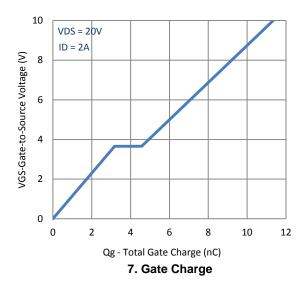
4. Drain-to-Source Forward Voltage

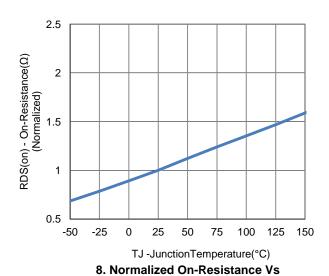


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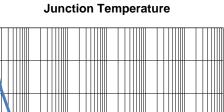
Typical Electrical Characteristics

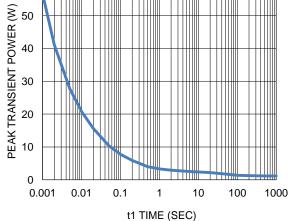
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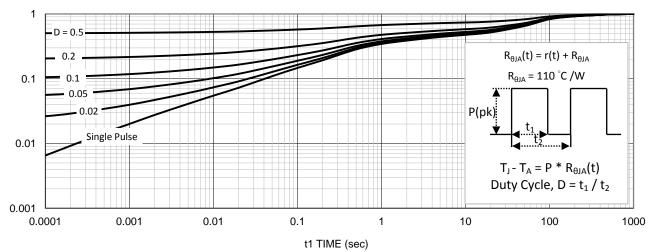


100 10 ID Current (A) 10 SEC 100 SEC 0.1 Limited by 0.01 0.1 10 100 1000 VDS Drain to Source Voltage (V) 9. Safe Operating Area



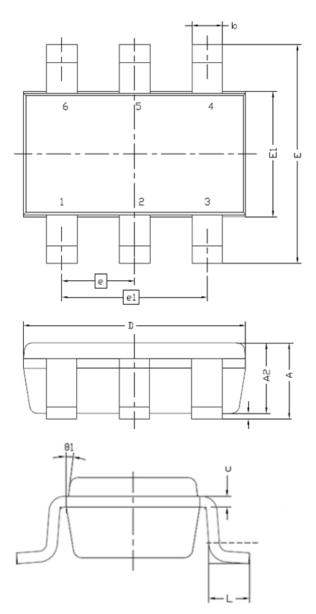


10. Single Pulse Maximum Power Dissipation



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Package Information



Symbol	MILLIMETERS		
Symbol	MIN	MAX	
Α	0.8	1.2	
A1	0	0.1	
A2	0.7	1.1	
b	0.3	0.5	
С	0.1	0.2	
D	2.8	3.1	
Е	2.6	3	
E1	1.4	1.7	
е	0.9	1	
e1	1.8	2	
Ĺ	0.3	0.6	
θ1	7° NOM		

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