P-Channel 20-V (D-S) MOSFET

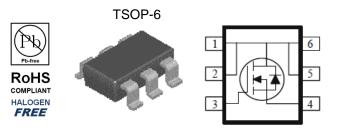
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- Load Switches
- DC/DC Conversion
- Motor Drives

PRODUCT SUMMARY			
Vds (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
-20	24 @ V _{GS} = -4.5V	-7.5	
	37 @ V _{GS} = -2.5V	-6.1	



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage	V _{GS}	±8	V		
Continuous Drain Current ^a	T _A =25°C		-7.5		
Continuous Drain Current	T _A =70°C	I _D	-6.1	А	
Pulsed Drain Current ^b	I _{DM}	-25			
Continuous Source Current (Diode Conduction) ^a		ا _s	-2.8	А	
Device Dispiration ^a	T _A =25°C	P _D	2	W	
Power Dissipation ^a	T _A =70°C	U 'D	1.3	٧V	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{eja}	62.5	°C/W	
	Steady State	ιν _θ ja	110		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

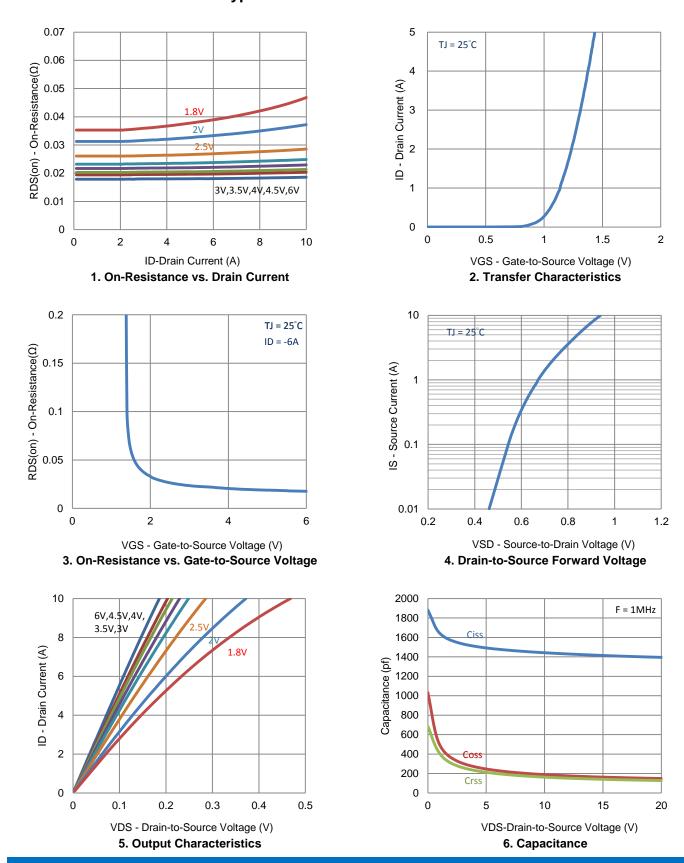
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-0.4			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -16 V, V_{GS} = 0 V$			-1	uA	
Zero Gale Voltage Dialit Current	DSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-10	uЛ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	-11			А	
Ducia Course On Desistance a	r	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -6 \text{ A}$			24	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -4.8 \text{ A}$			37		
Forward Transconductance ^a	g _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -6 \text{ A}$		6		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = -1.4 \text{ A}, V_{GS} = 0 \text{ V}$		-0.71		V	
	Dynamic ^b						
Total Gate Charge	Qg	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V},$		24			
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 V$, $V_{GS} = -4.3 V$, $I_{D} = -6 A$		3.4		nC	
Gate-Drain Charge	Q _{gd}	и <u>р</u> – -0 А		5.8		1	
Turn-On Delay Time	t _{d(on)}	V _{DS} = -10 V, R _L = 1.7 Ω,		9			
Rise Time	t _r	$V_{DS} = -10 V, K_{L} = 1.7 \Omega_{2},$ $I_{D} = -6 A,$		13		20	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		88		ns	
Fall Time	t _f	$V_{\text{GEN}} = 4.3 V$, $V_{\text{GEN}} = 0.32$		35			
Input Capacitance	C _{iss}			1414			
Output Capacitance	C _{oss}	V_{DS} = -15 V, V_{GS} = 0 V, f = 1 Mhz		163		pF	
Reverse Transfer Capacitance	C _{rss}			140			

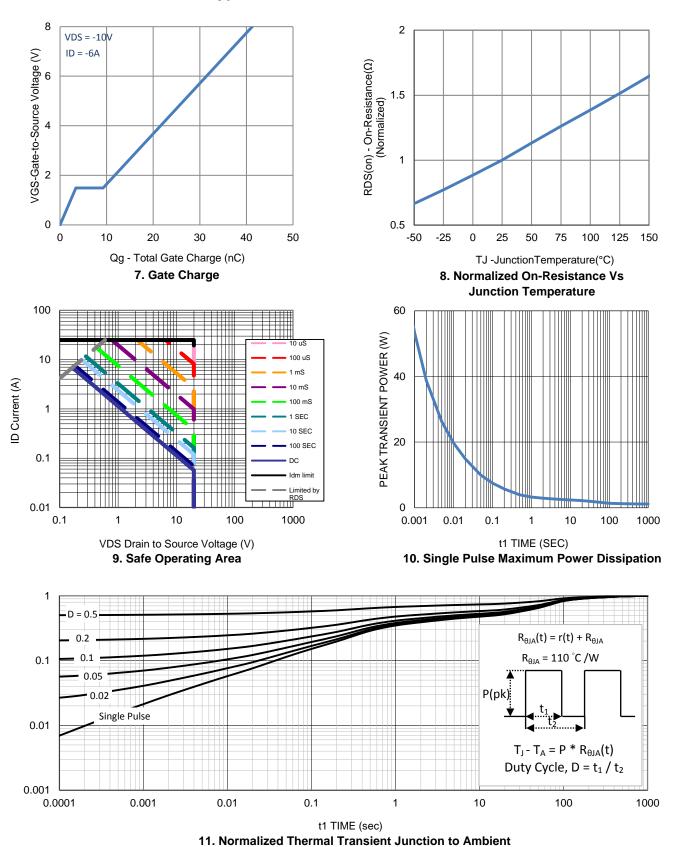
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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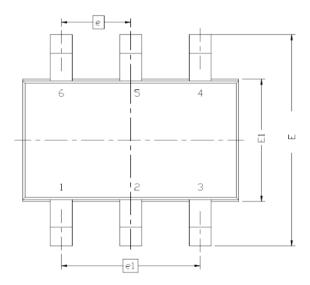


Typical Electrical Characteristics

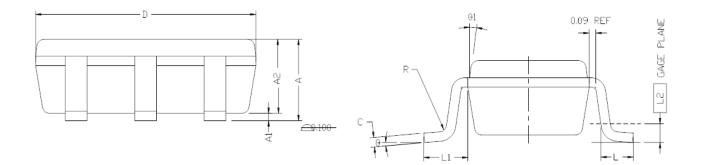


Typical Electrical Characteristics

Package Information



DIM.	MILLIMETERS				
DIM.	MIN	NDM	MAX		
Α	0.935		1.10		
A1	0.01		0.10		
A2	0.70		1.00		
b	0.25	0.32	0.40		
\subset	0.10	0.15	0.20		
D	2.95	3.05	3.10		
Ε	2.70	2.85	2.98		
E1	1.55	1.65	1.70		
е	0.95 BSC				
L	0.30		0.60		
L1	0.60REF				
L2	0.25BSC				
R	0.10				
θ	0?	4?	8?		
θ1	7? NOM				



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.