P-Channel 200-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

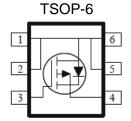
Typical	Applica	ations:
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- · White LED boost converters
- · Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□ (A)	
-200	2400 @ V _{GS} = -10V	-0.74	
	2550 @ V _{GS} = -4.5V	-0.72	







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V_{DS}	-200	V	
Gate-Source Voltage			±20	V	
Continuous Drain Current ^a	T _A =25°C	l_	-0.74		
Continuous Drain Current	T _A =70°C	I _D	-0.61	Α	
Pulsed Drain Current ^b			-5		
Continuous Source Current (Diode Conduction) a			-2	Α	
Dower Dissipation ^a	T _A =25°C	P_{D}	2	W	
Power Dissipation ^a	T _A =70°C	' D	1.3	V V	
Operating Junction and Storage Temperature Range		T_J,T_stg	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter			Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W	
Maximum Junction-to-Ambient	Steady State	IXOJA	110		

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zoro Cata Valtago Drain Current	1	$V_{DS} = -160 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-25		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-1			Α	
Drain Cauras On Basistanas a	r	$V_{GS} = -10 \text{ V}, I_{D} = -0.5 \text{ A}$			2400	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -0.4 \text{ A}$			2550	11122	
Forward Transconductance a	g _{fs}	$V_{DS} = -15 \text{ V}, I_{D} = -0.5 \text{ A}$		15		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = -1 \text{ A}, V_{GS} = 0 \text{ V}$		-0.84		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = -100 \text{ V}, V_{GS} = -4.5 \text{ V},$		2.0			
Gate-Source Charge	Q_{gs}	$I_{DS} = -100 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V},$ $I_{D} = -0.5 \text{ A}$		0.2		nC	
Gate-Drain Charge	Q_{gd}	1 _D = 0.5 A		1.3			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = -100 \text{ V}, R_{L} = 180 \Omega,$		5			
Rise Time	t _r	$V_{DS} = -100 \text{ V}, \text{ K}_{L} = -100 \Omega,$ $I_{D} = -0.5 \text{ A},$		5		no	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = -10 \text{ V}, R_{GEN} = 6 \Omega$		16		ns	
Fall Time	t _f	VGEN = 10 V, NGEN = 0 12		12			
Input Capacitance	C _{iss}			319			
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		45		pF	
Reverse Transfer Capacitance	C_{rss}			19			

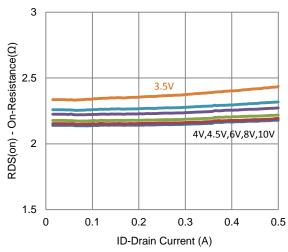
Notes

- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

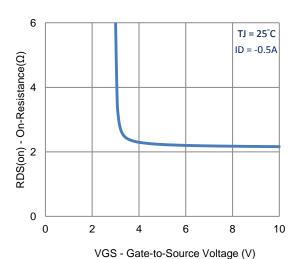
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Typical Electrical Characteristics

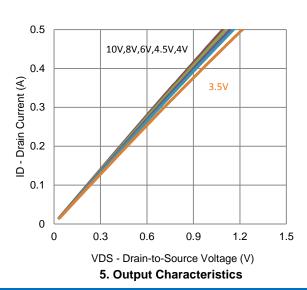
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1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage



0.5

TJ = 25°C

0.4

(4)

using 0.3

using 0.2

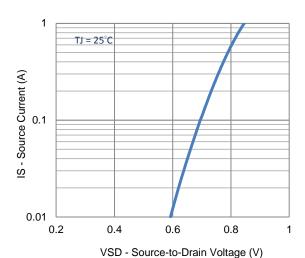
0.1

0.0

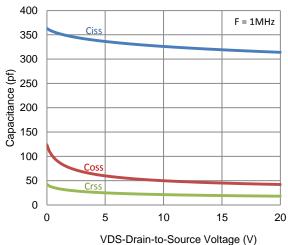
0 1 2 3 4 5

VGS - Gate-to-Source Voltage (V)

2. Transfer Characteristics

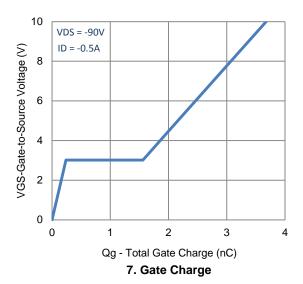


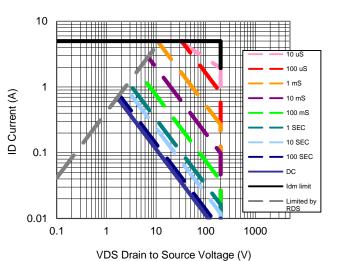
4. Drain-to-Source Forward Voltage



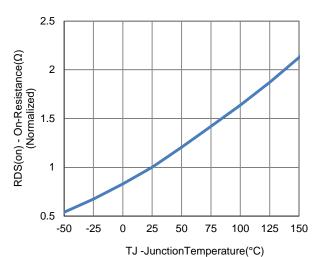
6. Capacitance

Typical Electrical Characteristics

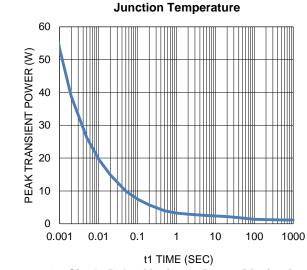




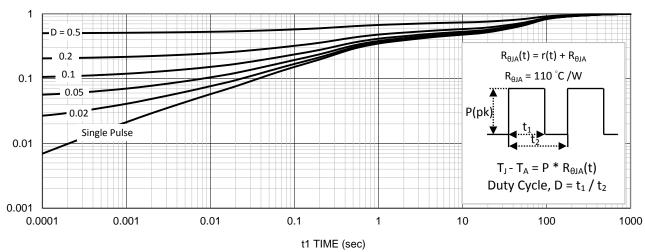
9. Safe Operating Area



8. Normalized On-Resistance Vs

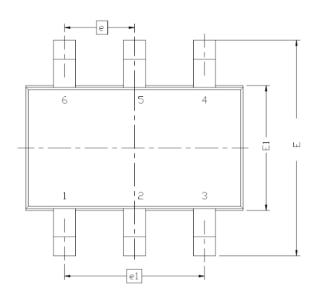


10. Single Pulse Maximum Power Dissipation

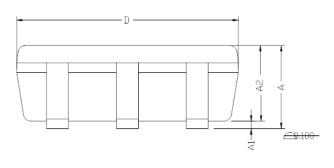


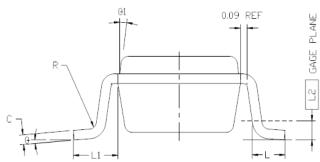
11. Normalized Thermal Transient Junction to Ambient

Package Information



DIM.	MILLIMETERS				
DIM.	MIN	NDM	MAX		
Α	0.935		1.10		
A1	0.01		0.10		
A2	0.70		1.00		
b	0.25	0.32	0.40		
C	0.10	0.15	0.20		
D	2.95	3.05	3.10		
Ε	2.70	2.85	2.98		
E1	1.55	1.65	1.70		
6	0.95 BSC				
L	0.30		0.60		
L1	0.60REF				
L2	0.25BSC				
R	0.10				
θ	0?	4?	8?		
θ1	7? N□M				





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.