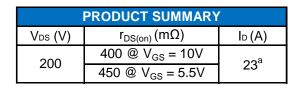
N-Channel 200-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

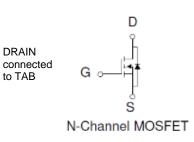




TO-220AB

О

G D S Top View



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)									
Parameter			Limit	Units					
Drain-Source Voltage	V _{DS}	200	V						
Gate-Source Voltage			±20	v					
Continuous Drain Current ^a	T _C =25°C	I _D	23	А					
Pulsed Drain Current ^b	I _{DM}	100	A						
ntinuous Source Current (Diode Conduction) ^a T _C =25°		ا _s	23	А					
Power Dissipation ^a	T _C =25°C	PD	300	W					
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C					

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient °	R _{θJA}	62.5	°C/W				
Maximum Junction-to-Case	$R_{ extsf{ heta}JC}$	0.5	C/W				

Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

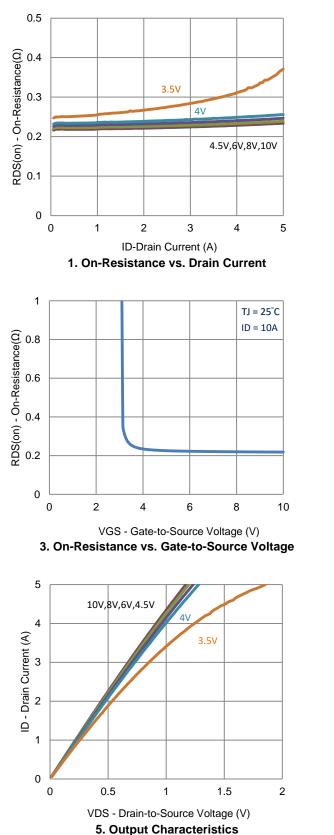
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Unit		
Static								
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current		$V_{DS} = 160 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	uA		
	DSS	$V_{DS} = 160 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	40			А		
Drain-Source On-Resistance ^a	r _{no} ,	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$			400	mΩ		
	r _{DS(on)}	$V_{GS} = 5.5 \text{ V}, \text{ I}_{D} = 8 \text{ A}$			450			
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		19		S		
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 12 \text{ A}, V_{GS} = 0 \text{ V}$		0.86		V		
Dynamic ^b								
Total Gate Charge	Qg	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 5.5 \text{ V},$ $I_{D} = 10 \text{ A}$		8		nC		
Gate-Source Charge	Q _{gs}			2.8				
Gate-Drain Charge	Q_{gd}			4.1				
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 100 \text{ V}, \text{ R}_{L} = 10 \Omega,$ $I_{D} = 10 \text{ A},$ $V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		7		ns		
Rise Time	t _r			7				
Turn-Off Delay Time	t _{d(off)}			30				
Fall Time	t _f			11				
Input Capacitance	C _{iss}	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ Mhz}$		819		pF		
Output Capacitance	C _{oss}			77				
Reverse Transfer Capacitance	C _{rss}			39				

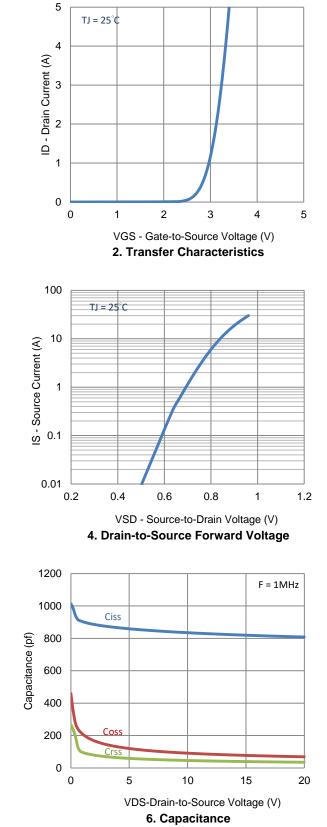
Notes

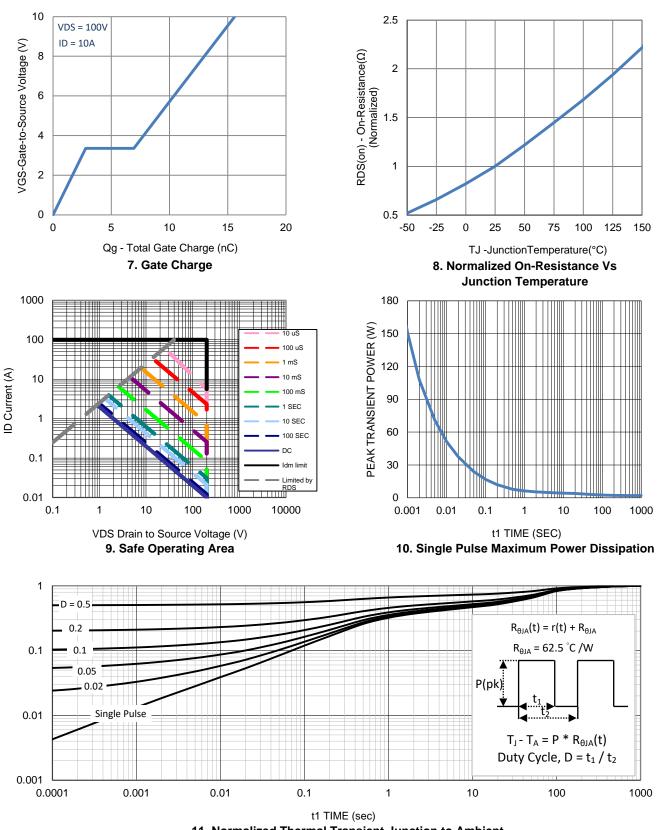
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics

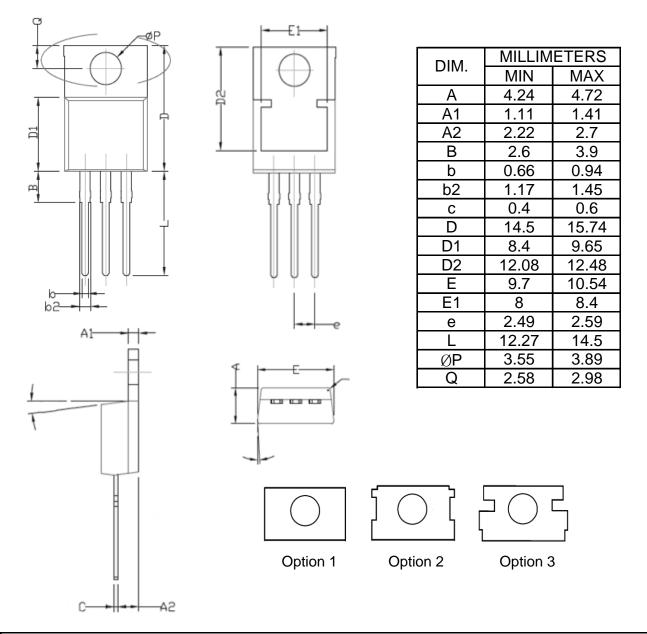




Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information



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