AM2325P

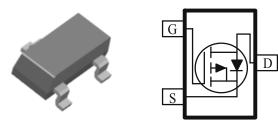
Analog Power

P-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY			
V _{DS} (V)	r _{DS(on)} (OHM)	I _D (A)	
	0.055 @ V _{GS} = -4.5V	-3.6	
-20	$0.089 @ V_{GS} = -2.5V$	-2.8	
	$0.20 @ V_{GS} = -1.8V$	-1.8	



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Ratings	Units	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage		V _{GS}	±12	v	
Continuous Durin Connect ^a	T _A =25°C	T	-3.6		
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	Ъ	-2.9	А	
Pulsed Drain Current ^b		I _{DM}	-10		
Continuous Source Current (Diode Conduction) ^a		I _S	±0.46	А	
Derver Dissinction ⁸	T _A =25°C	p	1.25	W	
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	I D	0.8	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Marine Investion to Ameliant ^a	t <= 5 sec	P	100	°C/W	
Maximum Junction-to-Ambient ^a	Steady-State	K _{THJA}	166		

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

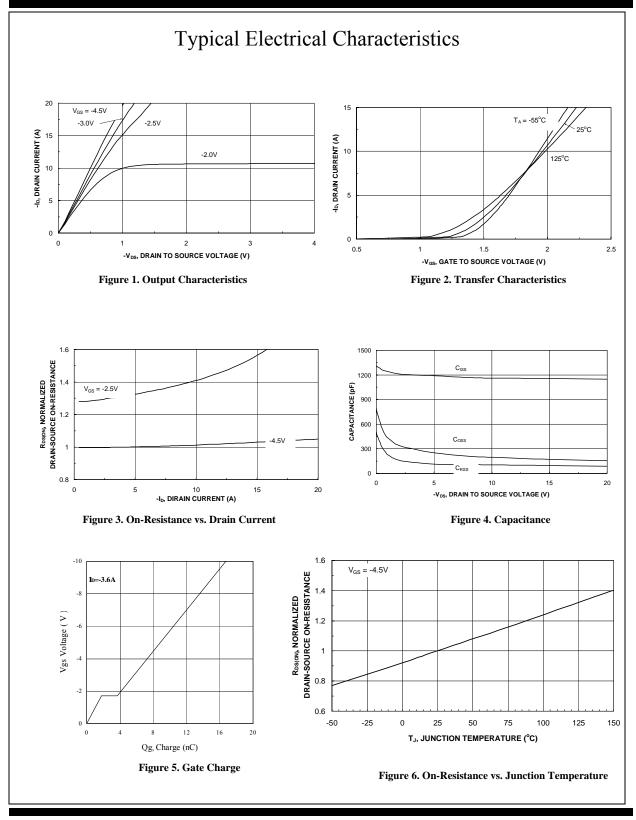
Deverseden	a l l	Test Conditions	Limits			TT *4
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	-0.7			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -16 V, V_{GS} = 0 V$			-1	uA
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			-10	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	-10			Α
		$V_{GS} = -4.5 \text{ V}, I_D = -3.6 \text{ A}$			55	mΩ
Drain-Source On-Resistance ^A	r _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -2.8 \text{ A}$			89	
		$V_{GS} = -1.8 \text{ V}, I_D = -1.8 \text{ A}$			200	
Forward Tranconductance ^A	g _{fs}	$V_{DS} = -5 V, I_D = -3.6 A$		12		S
Diode Forward Voltage	V _{SD}	$I_{\rm S} = -0.46$ A, $V_{\rm GS} = 0$ V		-0.60		V
Dynamic ^b						
Total Gate Charge	Qg	$V_{DS} = -5 V, V_{GS} = -4.5 V,$		16.7		nC
Gate-Source Charge	Q _{gs}	$v_{\rm DS} = -3 v, v_{\rm GS} = -4.5 v,$ $I_{\rm D} = -3.6 \text{ A}$		1.8		
Gate-Drain Charge	Q _{gd}	$I_{\rm D} = -5.0 ~{\rm A}$		1.9		
Turn-On Delay Time	t _{d(on)}			9		ns
Rise Time	t _r	$V_{DD} = -10 \text{ V}, \text{ I}_{L} = -1 \text{ A},$		4		
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, R_G = 6 \Omega$		25		
Fall-Time	t _f			20		

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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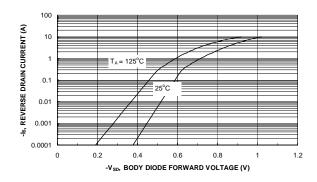


Figure 7. Source-Drain Diode Forward Voltage

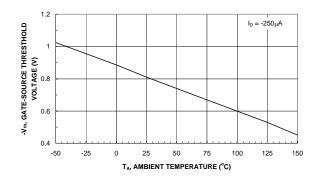


Figure 9. Vth Gate to Source Voltage Vs Temperature

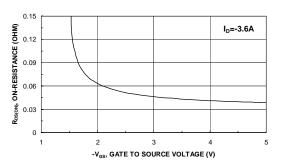
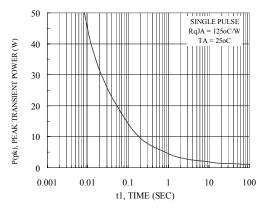
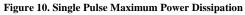
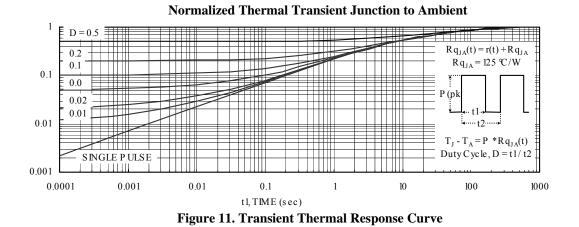


Figure 8. On-Resistance with Gate to Source Voltage







Ordering information

- AM2325P-T1-XX
 - A: Analog Power
 - M: MOSFET
 - 2325: Part number
 - P: P-Channel
 - T1: Tape & reel
 - XX: Blank: StandardPF: Lead-free

Package Information

