N-Channel 20-V (D-S) MOSFET

Key Features:

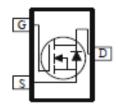
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
20	32 @ V _{GS} = 4.5V	5.3	
	44 @ V _{GS} = 2.5V	4.5	







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		V _{DS}	20	V		
Gate-Source Voltage		V _{GS}	±12	V		
Continuous Drain Current ^a	T _A =25°C T _A =70°C	1	5.3	A		
	T _A =70°C	I _D	4.1			
Pulsed Drain Current ^b		I _{DM}	20			
Continuous Source Current (Diode Conduction) ^a			1.8	А		
Power Dissipation ^a	T _A =25°C	P _D	1.3	W		
	T _A =70°C	۰D	0.8	vv		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{θJA}	100	°C/W	
	Steady State		166		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

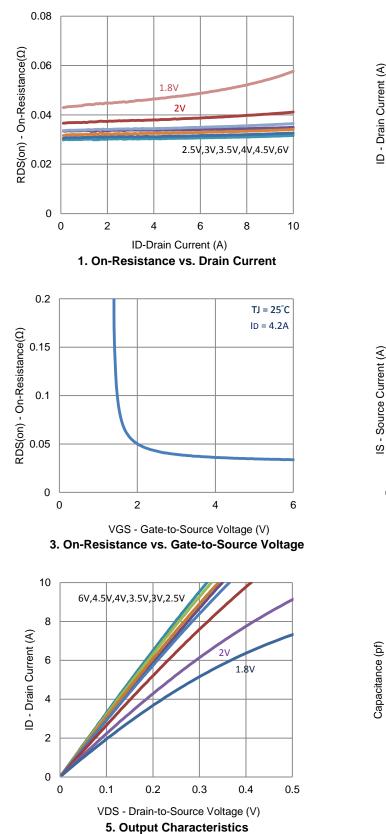
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	0.4			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			±10	uA
Zero Gate Voltage Drain Current	1	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
	IDSS	$V_{DS} = 16 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	10			Α
Drain-Source On-Resistance ^a	r	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 4.2 \text{ A}$			32	mΩ
	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.8 \text{ A}$			44	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 4.2 \text{ A}$		11		S
Diode Forward Voltage ^a	V_{SD}	$I_{\rm S} = 0.9$ A, $V_{\rm GS} = 0$ V		0.7		V
Dynamic ^b						
Total Gate Charge	Qg			6.2		nC
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_{D} = 4.2 \text{ A}$		1		
Gate-Drain Charge	Q_gd	ID = 4.2 A		2		
Turn-On Delay Time	t _{d(on)}	V 10V B - 240		6		ns
Rise Time	t _r	$V_{DS} = 10 \text{ V}, \text{ R}_{L} = 2.4 \Omega,$		19		
Turn-Off Delay Time	t _{d(off)}	I _D = 4.2 A, V _{GEN} = 4.5 V, R _{GEN} = 6 Ω		47		
Fall Time	t _f	$V_{\text{GEN}} = 4.3 \text{ V}, $		25		
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		413		pF
Output Capacitance	C _{oss}			76		
Reverse Transfer Capacitance	C _{rss}			67		

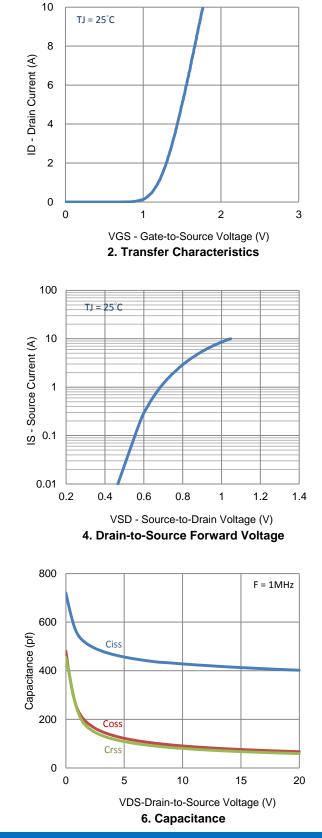
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

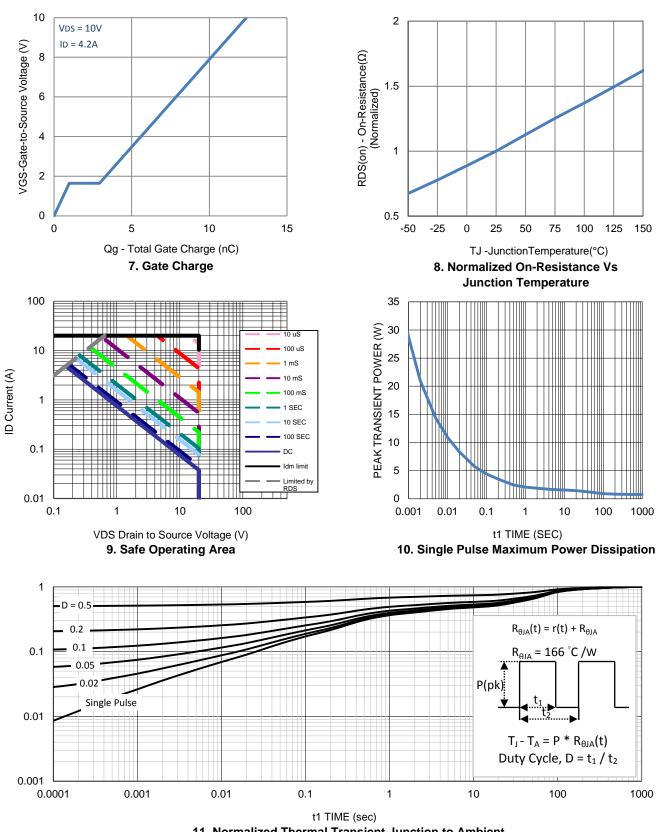
Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.



Typical Electrical Characteristics



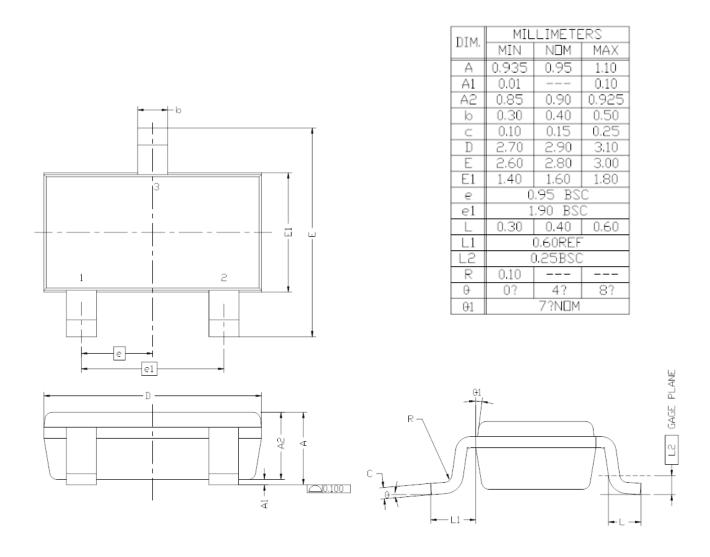
© Preliminary



Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.