N-Channel 30-V (D-S) MOSFET

Key Features:

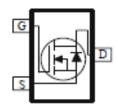
- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

Typical Applications:

- Power Routing
- Li Ion Battery Packs
- Level Shifting and Driver Circuits

PRODUCT SUMMARY			
Vds (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
30	60 @ V _{GS} = 4.5V	3.5	
	82 @ V _{GS} = 2.5V	3.0	







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage			30	V	
Gate-Source Voltage		V _{GS}	±12	V	
Continuous Drain Current ^a	T _A =25°C T _A =70°C	1_	3.5		
	T _A =70°C	I _D	2.8	А	
Pulsed Drain Current ^b		I _{DM}	15		
Continuous Source Current (Diode Conduction) ^a		I _S	1.9	А	
Power Dissinction ^a	T _A =25°C	P _D	1.3	W	
Power Dissipation ^a	T _A =70°C	'D	0.8	۷V	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	- R _{θJA}	100	°C/W	
	Steady State		166		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

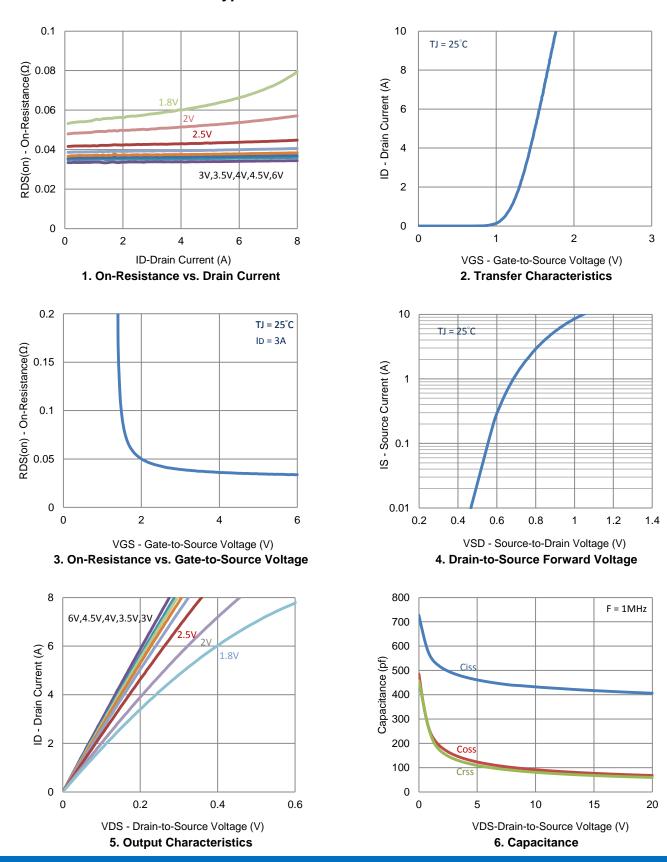
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	0.4			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			±10	uA
Zero Gate Voltage Drain Current	1	$V_{DS} = 16 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	uA
	IDSS	$V_{DS} = 16 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55^{\circ}\text{C}$			25	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 4.5 V$	5			A
Drain-Source On-Resistance ^a	r.	$V_{GS} = 4.5 \text{ V}, I_{D} = 3 \text{ A}$			60	mΩ
	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 2.4 \text{ A}$			82	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 2.8 \text{ A}$		12		S
Diode Forward Voltage ^a	V_{SD}	$I_{\rm S} = 1 {\rm A}, {\rm V}_{\rm GS} = 0 {\rm V}$		0.69		V
Dynamic ^b						
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 3 \text{ A}$		6		nC
Gate-Source Charge	Q _{gs}			1.0		
Gate-Drain Charge	Q_gd			2.0		
Turn-On Delay Time	t _{d(on)}	$V_{\text{DS}} = 15 \text{ V}, \text{ R}_{\text{L}} = 3.6 \Omega, \text{ I}_{\text{D}} = 3 \text{ A},$ $V_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		8		
Rise Time	t _r			21		ns
Turn-Off Delay Time	t _{d(off)}			48		
Fall Time	t _f			26		
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		417		pF
Output Capacitance	C _{oss}			77		
Reverse Transfer Capacitance	C _{rss}			68		

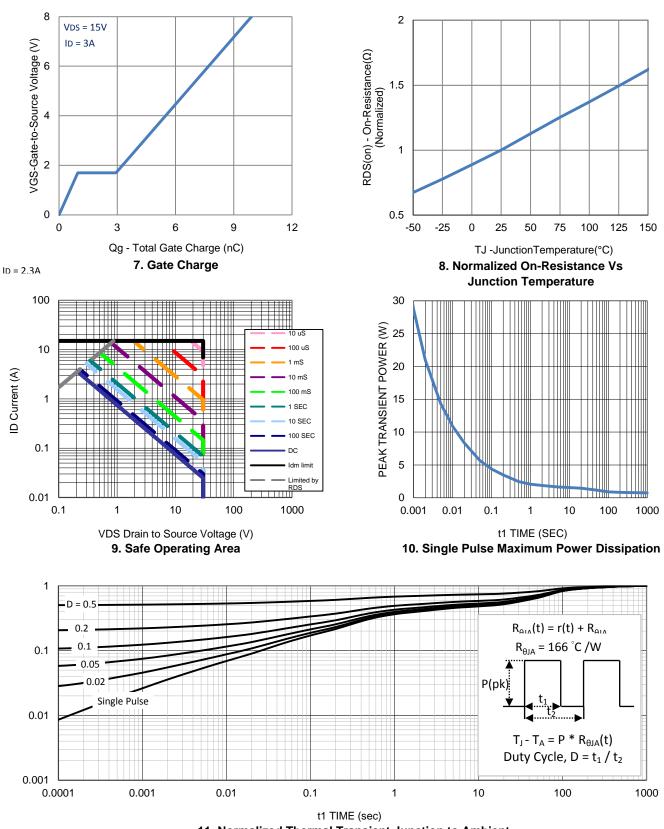
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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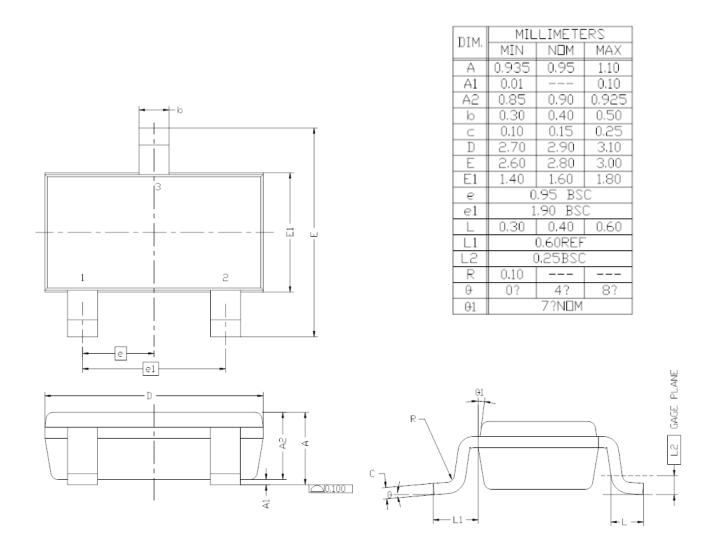
Typical Electrical Characteristics



Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.