N-Channel 150-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

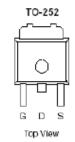
Typical Applications:

- PoE Power Sourcing Equipment
- PoE Powered Devices
- Telecom DC/DC converters
- · White LED boost converters

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
150	255 @ V _{GS} = 10V	12		
	290 @ V _{GS} = 4.5V	11		







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter			Limit	Units		
Drain-Source Voltage			150	V		
Gate-Source Voltage		V_{GS}	±20	V		
Continuous Drain Current	T _C =25°C	I _D	10 A			
Pulsed Drain Current ^b	I _{DM}	50	^			
Continuous Source Current (Diode Conduction)			45	Α		
Power Dissipation	T _C =25°C	P_{D}	50	W		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	40	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	3	C/VV		

Notes

a. Surface Mounted on 1" x 1" FR4 Board, drain pad using 2 oz copper, value dependent on PC board thermal characteristics

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b. Pulse width limited by maximum junction temperature

Electrical Characteristics

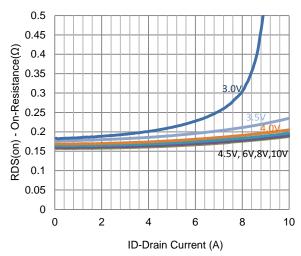
Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$				V	
Gate-Body Leakage	I _{GSS}	.,,			±10	uA	
Zero Gate Voltage Drain Current	lana	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Brain Gurrent	I _{DSS}	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25		
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	34			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_{D} = 6 \text{ A}$			255	mΩ	
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$			290	11122	
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 6 \text{ A}$		20		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 25 \text{ A}, V_{GS} = 0 \text{ V}$		0.95		V	
		Dynamic					
Total Gate Charge	Q_g			16.7			
Gate-Source Charge	Q_{gs}	$V_{DS} = 75 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 6 \text{ A}$		3.5		nC	
Gate-Drain Charge	Q_gd			9.3			
Turn-On Delay Time	t _{d(on)}			11			
Rise Time	t _r	$V_{DD} = 75 \text{ V}, \text{ R}_{L} = 12.5 \Omega, \text{ I}_{D} = 6 \text{ A},$		34		no	
Turn-Off Delay Time	$t_{d(off)}$	V_{GEN} = 10 V, R_{GEN} = 6 Ω		46		ns	
Fall Time	t _f			77			
Input Capacitance	C _{iss}			965			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{Mhz}$		86		pF	
Reverse Transfer Capacitance	C_{rss}			55			

Notes

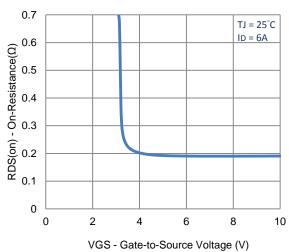
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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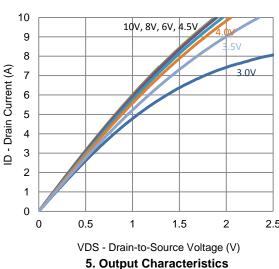
Typical Electrical Characteristics

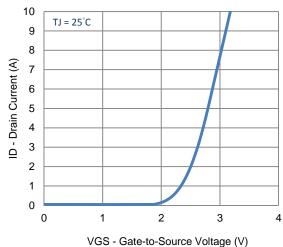


1. On-Resistance vs. Drain Current

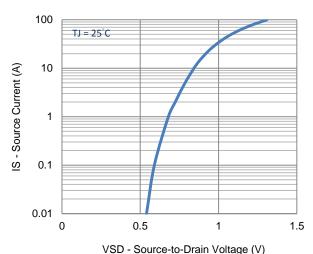


3. On-Resistance vs. Gate-to-Source Voltage

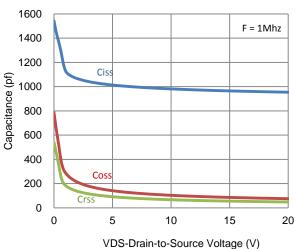




2. Transfer Characteristics

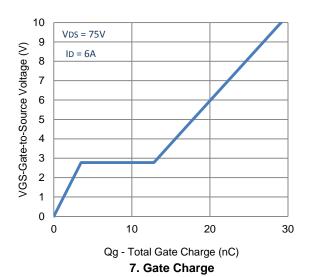


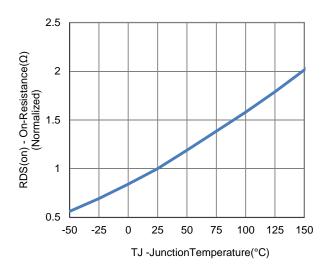
4. Drain-to-Source Forward Voltage

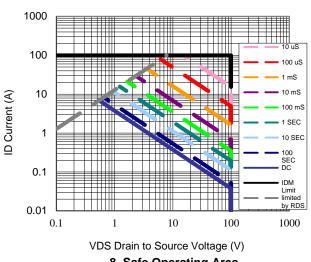


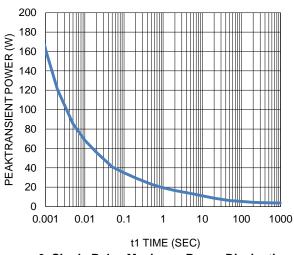
6. Capacitance

Typical Electrical Characteristics



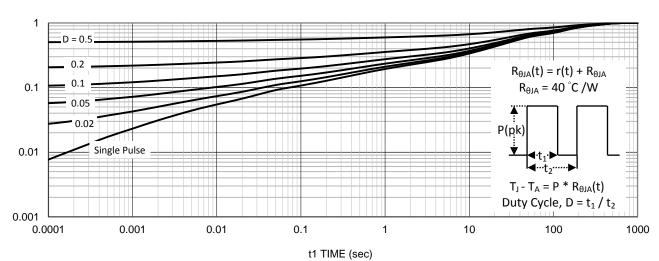






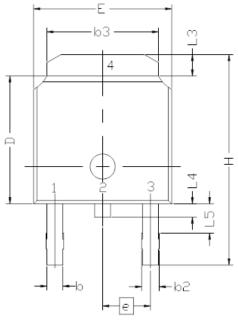


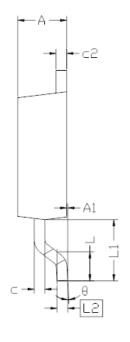
9. Single Pulse Maximum Power Dissipation



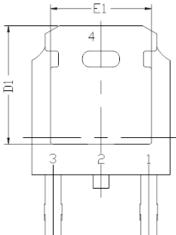
10. Normalized Thermal Transient Junction to Ambient

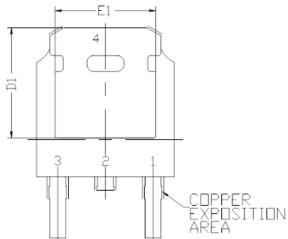
Package Information





CVADO	DIMENS:		REQMTS			
SYMBOL	MIN	NDM	MAX			
E	6.40	6.60	6.731			
L	1.40	1.52	1.77			
_L1		2.743 REF				
L2	0.	.508 BS	C C			
L3	0.89		1.27			
L4	0.64		1.01			
L5						
D	6.00	6.10	6,223			
Н	9.40	10.00	10.40			
b	0.64	0.76	0,88			
b2	0.77	0.84	1.14			
b3	5.21	5.34	5.46			
е		286 BS				
А	2,20	2.30	2,38			
A1	0		0.127			
	0.45	0.50	0.60			
c2	0.45	0.50	0.58			
D1	5,30					
E1	4.40					
θ	0°		10°			





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.

Ordering Information

AM20N15-250D-T1-XX

A: Analog Power

- M: MOSFET

- 20N15-250: Part number, N-Channel

– D: TO-252

– T1: Tape & reel

XX: Blank: Standard

PF: Leadfree