# N-Channel 80-V (D-S) MOSFET

### **Key Features:**

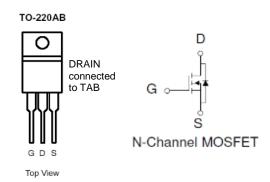
- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

# **Typical Applications:**

- Automotive Systems
- DC/DC Conversion Circuits
- · Battery Powered Power Tools

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>□</sub> (A)	
80	8.5 @ V <sub>GS</sub> = 10V	150	
	12.5 @ V <sub>GS</sub> = 4.5V	125	





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)							
Parameter		Symbol	Limit	Units			
Drain-Source Voltage		$V_{DS}$	80	V			
Gate-Source Voltage		$V_{GS}$	±20	·			
Continuous Drain Current a	T <sub>C</sub> =25°C	I <sub>D</sub>	150	۸			
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	600	A			
Continuous Source Current (Diode Conduction) a T <sub>C</sub> =25°C		I <sub>S</sub>	150	Α			
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_D$	300	W			
Operating Junction and Storage Temperature Range		$T_J$ , $T_{stg}$	-55 to 175	°C			

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV

#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

#### **Electrical Characteristics**

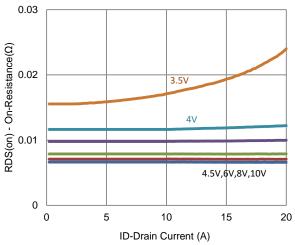
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current	1	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA		
	I <sub>DSS</sub>	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA		
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α		
Drain-Source On-Resistance <sup>a</sup>	r	$V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$			8.5	mΩ		
	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 16 \text{ A}$			12.5			
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		52		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{S} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.86		V		
	Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$	$V_{DS} = 40 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 20 \text{ A}$		21		nC		
Gate-Source Charge	$Q_{gs}$			7.4				
Gate-Drain Charge	$Q_{gd}$			10				
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 40 \text{ V}, R_{L} = 2 \Omega,$ $I_{D} = 20 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		12		ns		
Rise Time	t <sub>r</sub>			24				
Turn-Off Delay Time	$t_{d(off)}$			58				
Fall Time	t <sub>f</sub>			32				
Input Capacitance	$C_{iss}$	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, f = 1 Mhz		1551		pF		
Output Capacitance	$C_{oss}$			315				
Reverse Transfer Capacitance	$C_{rss}$			96				

#### Notes

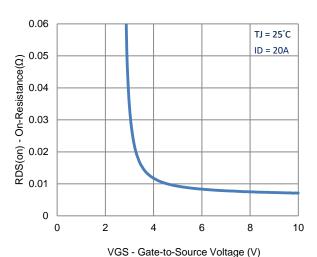
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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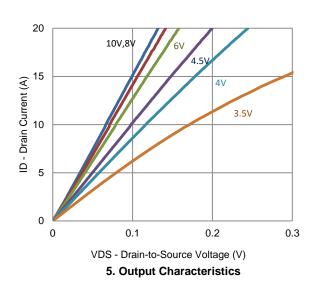
## **Typical Electrical Characteristics**

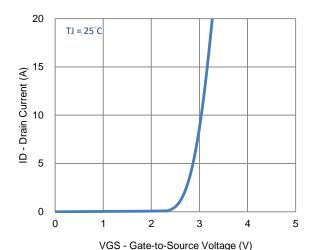


#### 1. On-Resistance vs. Drain Current

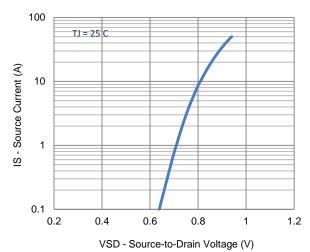


3. On-Resistance vs. Gate-to-Source Voltage

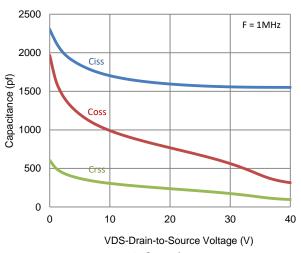




2. Transfer Characteristics

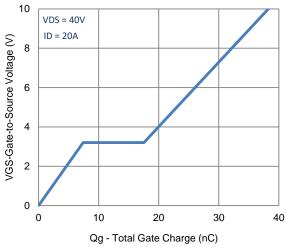


4. Drain-to-Source Forward Voltage

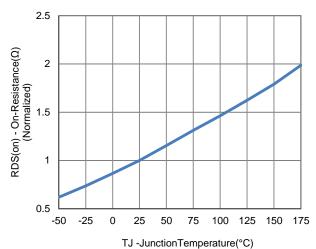


6. Capacitance

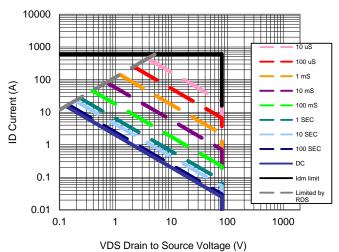
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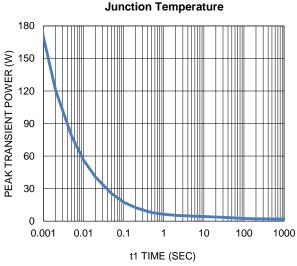
7. Gate Charge



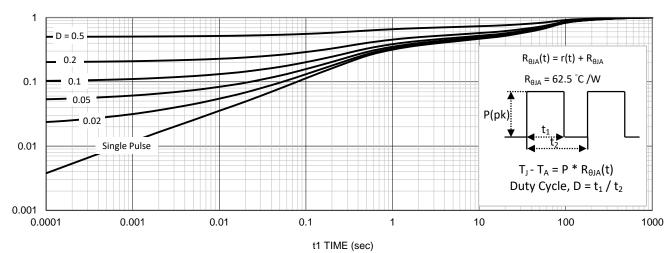
8. Normalized On-Resistance Vs



9. Safe Operating Area

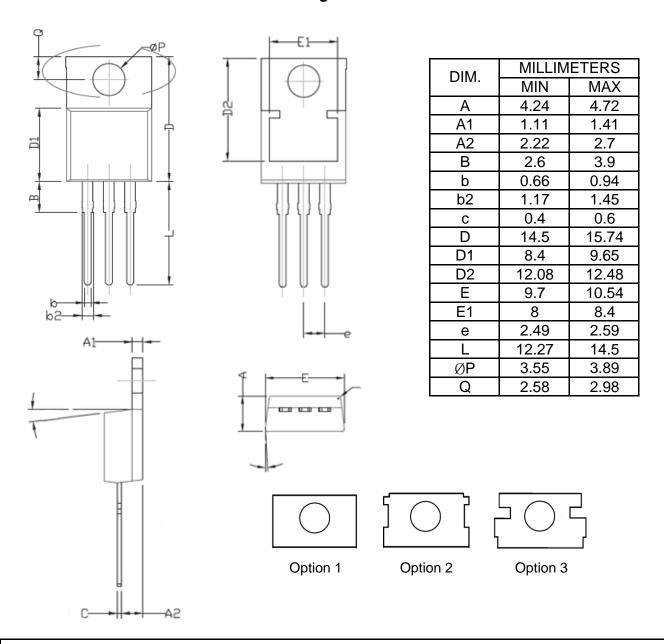


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

### **Package Information**



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