N-Channel 250-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

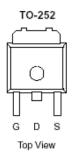
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
250	280 @ V _{GS} = 10V	10.9		
230	340 @ V _{GS} = 5.5V	9.9		

in





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		V _{DS}	250	V		
Gate-Source Voltage		V _{GS}	±20	v		
Continuous Drain Current ^a	T _C =25°C	I _D	10.9	А		
Pulsed Drain Current ^b		I _{DM}	40	~		
Continuous Source Current (Diode Conduction) ^a		I _S	46	А		
Power Dissipation ^a	T _C =25°C	PD	50	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	R_{\thetaJA}	40	°C/W		
Maximum Junction-to-Case	R _{eJC}	3	0/11		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

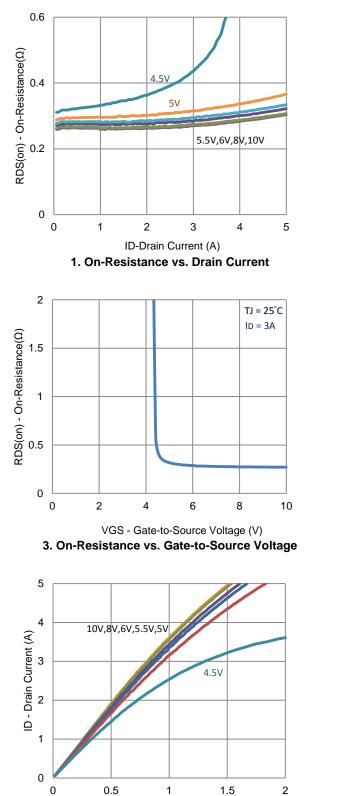
Electrical Characteristics

Parameter	Symbol	ymbol Test Conditions		Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$			±100	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1 uA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	14			А	
Drain Sauras On Desistance a	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$			280	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 5.5 \text{ V}, \text{ I}_{D} = 2.4 \text{ A}$			340	11122	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		4		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 23.1 A, V _{GS} = 0 V		0.95		V	
		Dynamic ^b					
Total Gate Charge	Qg	V _{DS} = 120 V, V _{GS} = 5.5 V,		16		nC	
Gate-Source Charge	Q_gs	$V_{DS} = 120 V, V_{GS} = 3.3 V,$ $I_{D} = 3 A$		7.1			
Gate-Drain Charge	Q_{gd}	1 <u>0</u> = 0 / (6.8			
Turn-On Delay Time	t _{d(on)}	V _{DS} = 120 V, R _I = 40 Ω,		22			
Rise Time	t _r	$V_{\rm DS} = 120$ V, $K_{\rm L} = 40$ Ω, $I_{\rm D} = 3$ A,		25		ne	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		55		ns	
Fall Time	t _f	$v_{\text{GEN}} = 10 v$, $N_{\text{GEN}} = 0.02$		43			
Input Capacitance	C _{iss}			1691			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		117		pF	
Reverse Transfer Capacitance	C _{rss}			65			

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

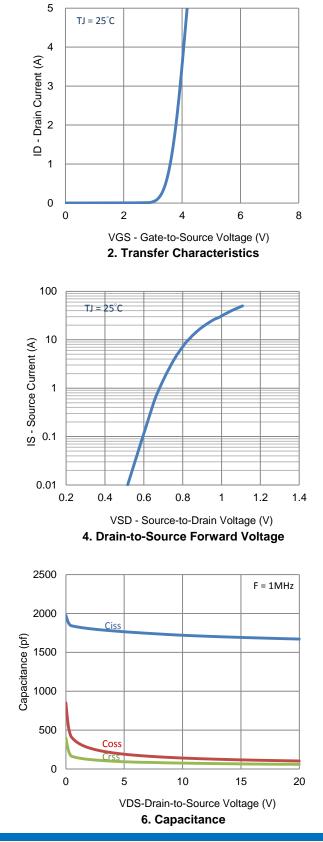
Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.



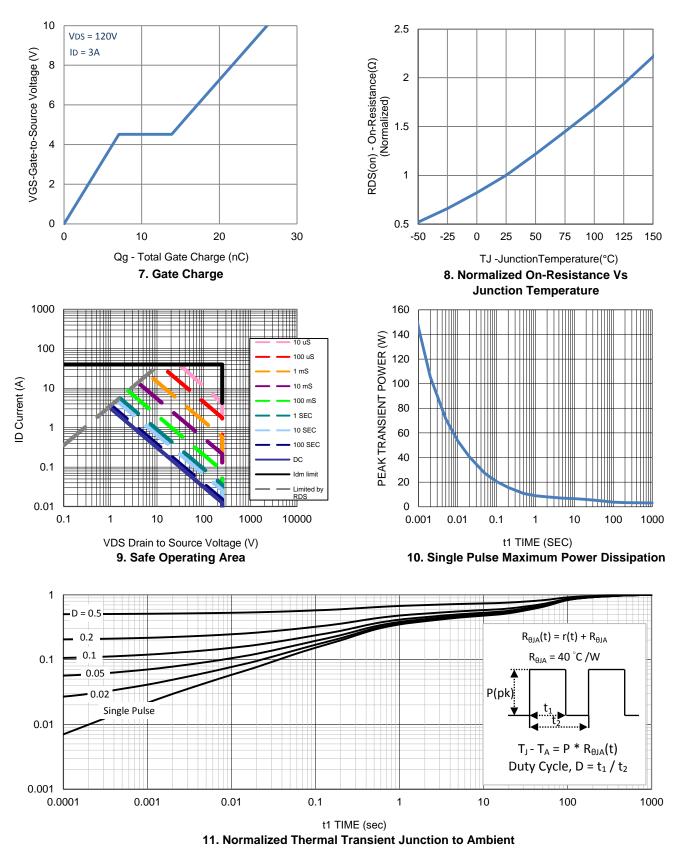
VDS - Drain-to-Source Voltage (V)

5. Output Characteristics

Typical Electrical Characteristics

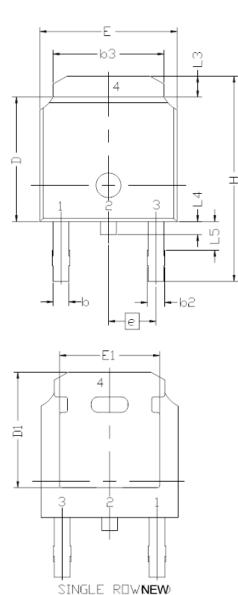


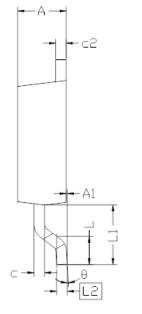
© Preliminary



Typical Electrical Characteristics

Package Information





	DIMENS:	IONAL F	REQMTS
SYMBOL	MIN	NDM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1		.743 RI	
L2		508 BS	
L3	0.89		1.27
L4	0.64		1.01
L5			
D	6.00	6.10	6.223
Н	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e A		286 BS	
A	2.20	2.30	2.38
A1	0		0.127
C _	0,45	0.50	0.60
L C2	0.45	0.50	0,58
D1	5.30		
E1	4.40		
θ	0°		10°

Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.