

N-Channel 700-V (D-S) MOSFET

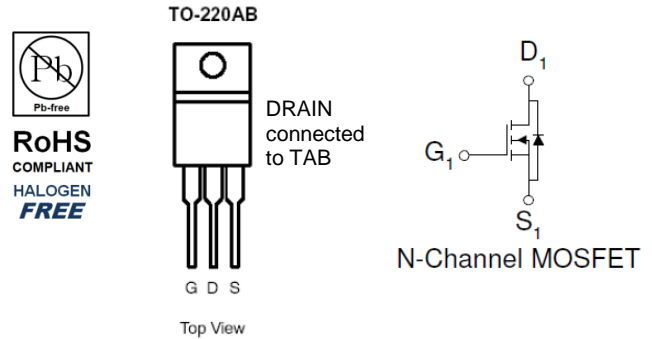
Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- Power Supplies
- Motor Drives
- Consumer Electronics

| PRODUCT SUMMARY | | |
|-----------------|---------------------------|-----------------|
| V_{DS} (V) | $r_{DS(on)}$ (Ω) | I_D (A) |
| 700 | 1 @ $V_{GS} = 10V$ | 12 ^a |



| ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED) | | | | |
|---|--------------------------|----------------|------------|------------------|
| Parameter | | Symbol | Limit | Units |
| Drain-Source Voltage | | V_{DS} | 700 | V |
| Gate-Source Voltage | | V_{GS} | ± 20 | |
| Continuous Drain Current ^a | $T_C = 25^\circ\text{C}$ | I_D | 12 | A |
| Pulsed Drain Current ^b | | I_{DM} | 50 | |
| Continuous Source Current (Diode Conduction) ^a | $T_C = 25^\circ\text{C}$ | I_S | 12 | A |
| Power Dissipation ^a | $T_C = 25^\circ\text{C}$ | P_D | 300 | W |
| Operating Junction and Storage Temperature Range | | T_J, T_{stg} | -55 to 175 | $^\circ\text{C}$ |

| THERMAL RESISTANCE RATINGS | | | |
|--|-----------------|---------|--------------------|
| Parameter | Symbol | Maximum | Units |
| Maximum Junction-to-Ambient ^c | $R_{\theta JA}$ | 62.5 | $^\circ\text{C/W}$ |
| Maximum Junction-to-Case | $R_{\theta JC}$ | 0.5 | |

Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

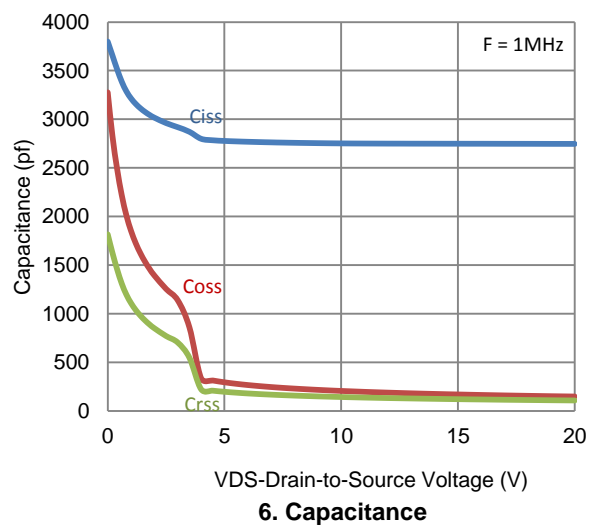
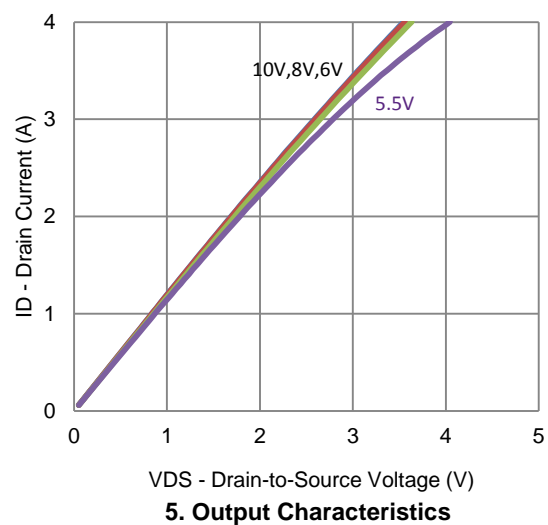
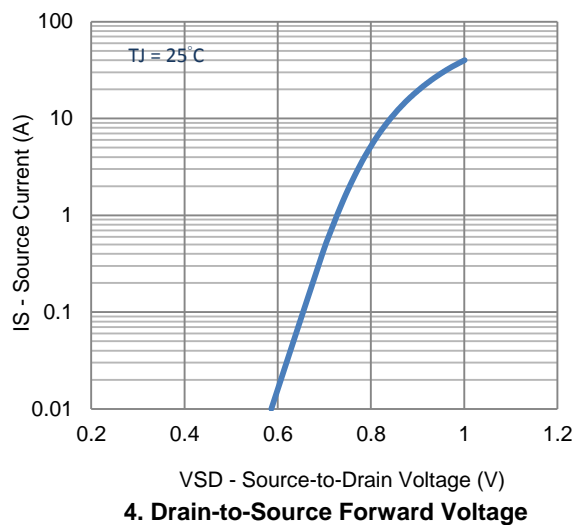
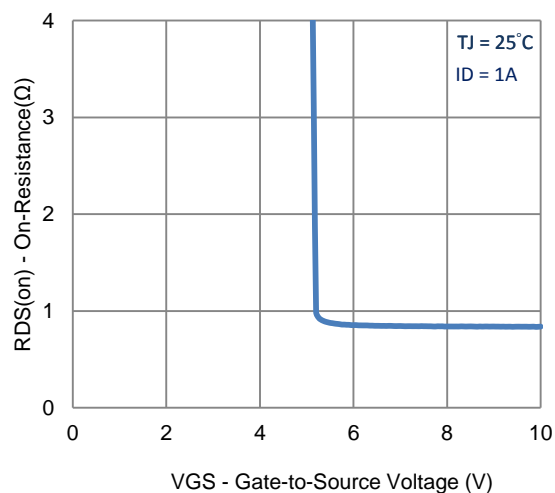
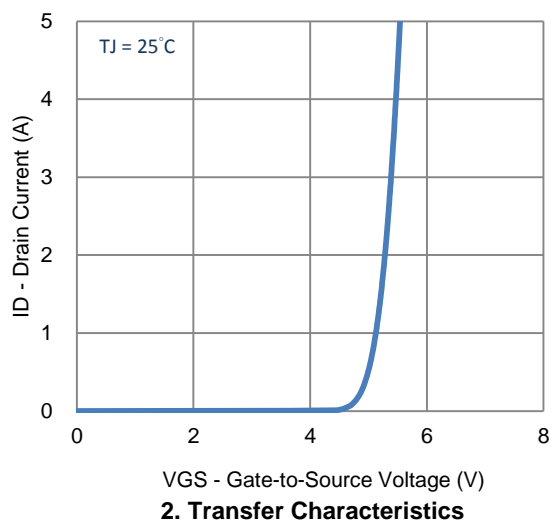
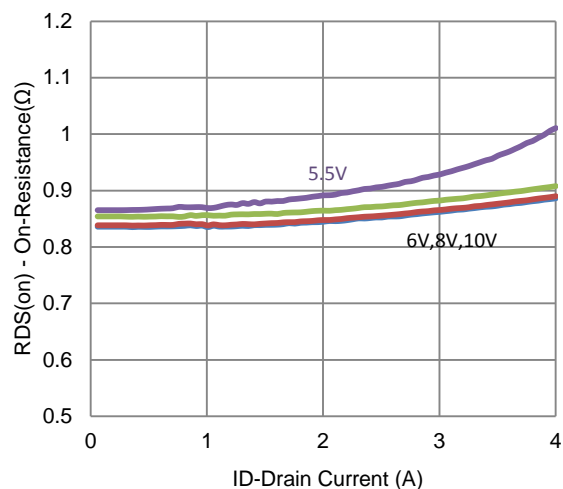
| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|---|--------------|---|-----|------|-----------|----------|
| Static | | | | | | |
| Gate-Source Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 2 | | | V |
| Gate-Body Leakage | I_{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 20 V$ | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = 560 V, V_{GS} = 0 V$ | | | 1 | μA |
| | | $V_{DS} = 560 V, V_{GS} = 0 V, T_J = 55^\circ C$ | | | 25 | |
| On-State Drain Current ^a | $I_{D(on)}$ | $V_{DS} = 5 V, V_{GS} = 10 V$ | 15 | | | A |
| Drain-Source On-Resistance ^a | $r_{DS(on)}$ | $V_{GS} = 10 V, I_D = 1 A$ | | | 1 | Ω |
| Forward Transconductance ^a | g_{fs} | $V_{DS} = 15 V, I_D = 1 A$ | | 5 | | S |
| Diode Forward Voltage ^a | V_{SD} | $I_S = 6 A, V_{GS} = 0 V$ | | 0.8 | | V |
| Dynamic ^b | | | | | | |
| Total Gate Charge | Q_g | $V_{DS} = 100 V, V_{GS} = 6 V,$ $I_D = 1 A$ | | 23 | | nC |
| Gate-Source Charge | Q_{gs} | | | 10 | | |
| Gate-Drain Charge | Q_{gd} | | | 9.6 | | |
| Turn-On Delay Time | $t_{d(on)}$ | $V_{DS} = 100 V, R_L = 100 \Omega,$ $I_D = 1 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 \Omega$ | | 25 | | ns |
| Rise Time | t_r | | | 7 | | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 61 | | |
| Fall Time | t_f | | | 20 | | |
| Input Capacitance | C_{iss} | $V_{DS} = 15 V, V_{GS} = 0 V, f = 1 Mhz$ | | 2748 | | pF |
| Output Capacitance | C_{oss} | | | 169 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 120 | | |

Notes

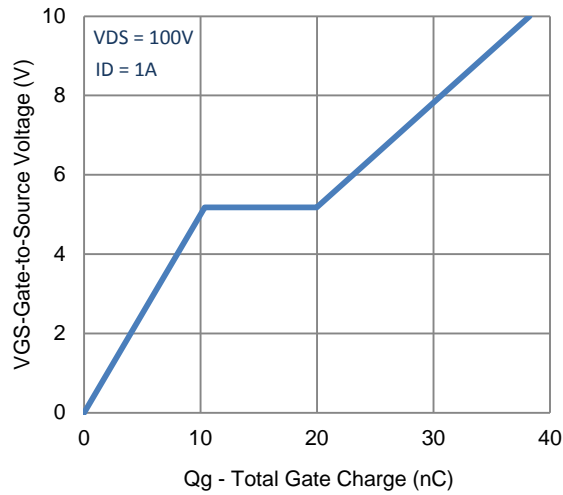
- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

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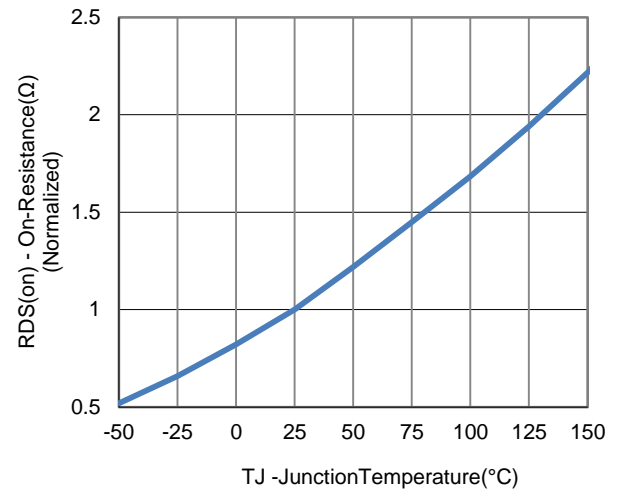
Typical Electrical Characteristics



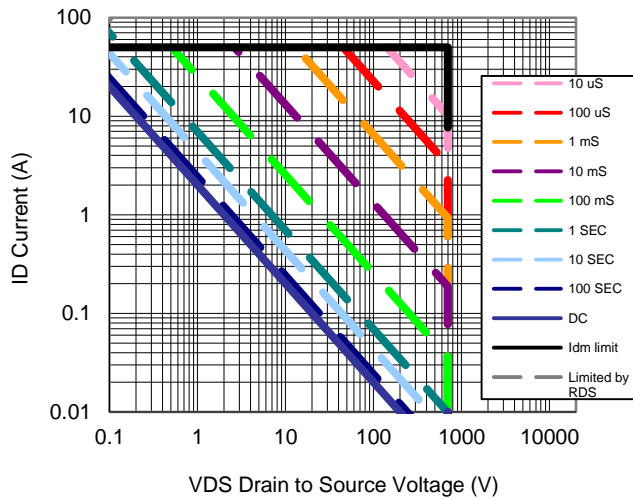
Typical Electrical Characteristics



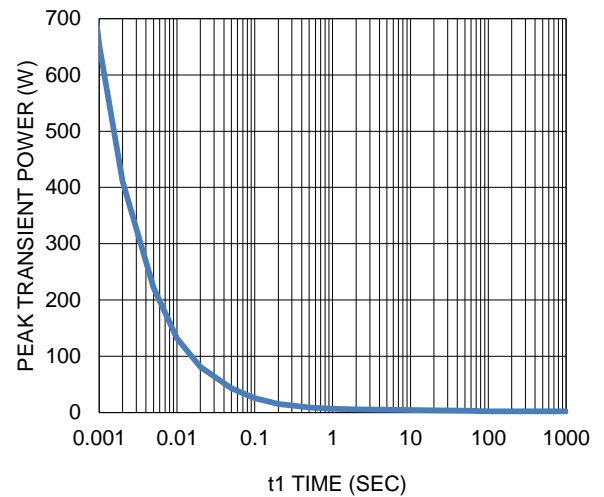
7. Gate Charge



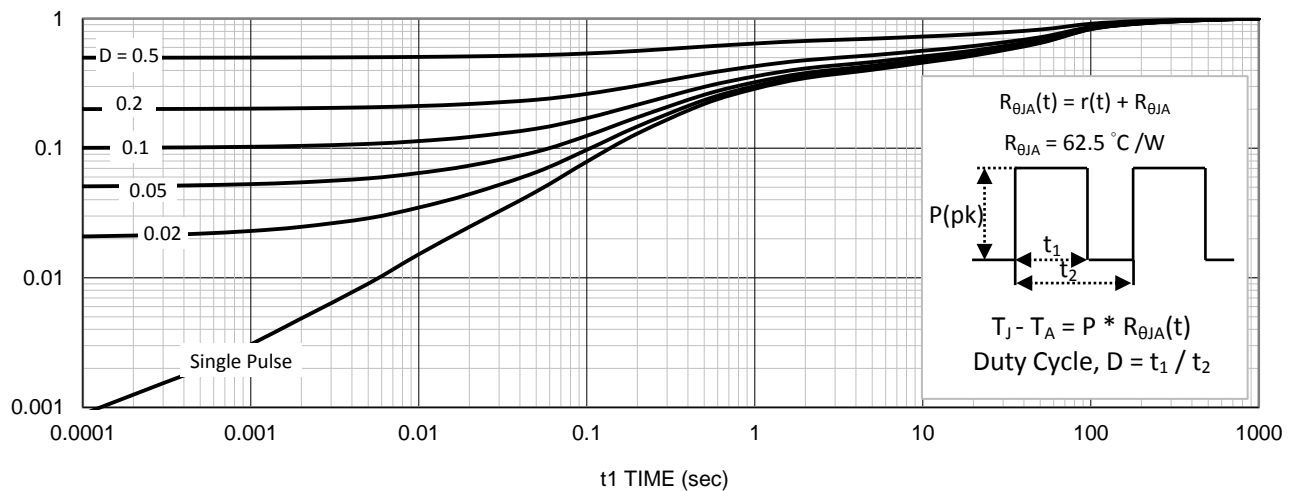
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

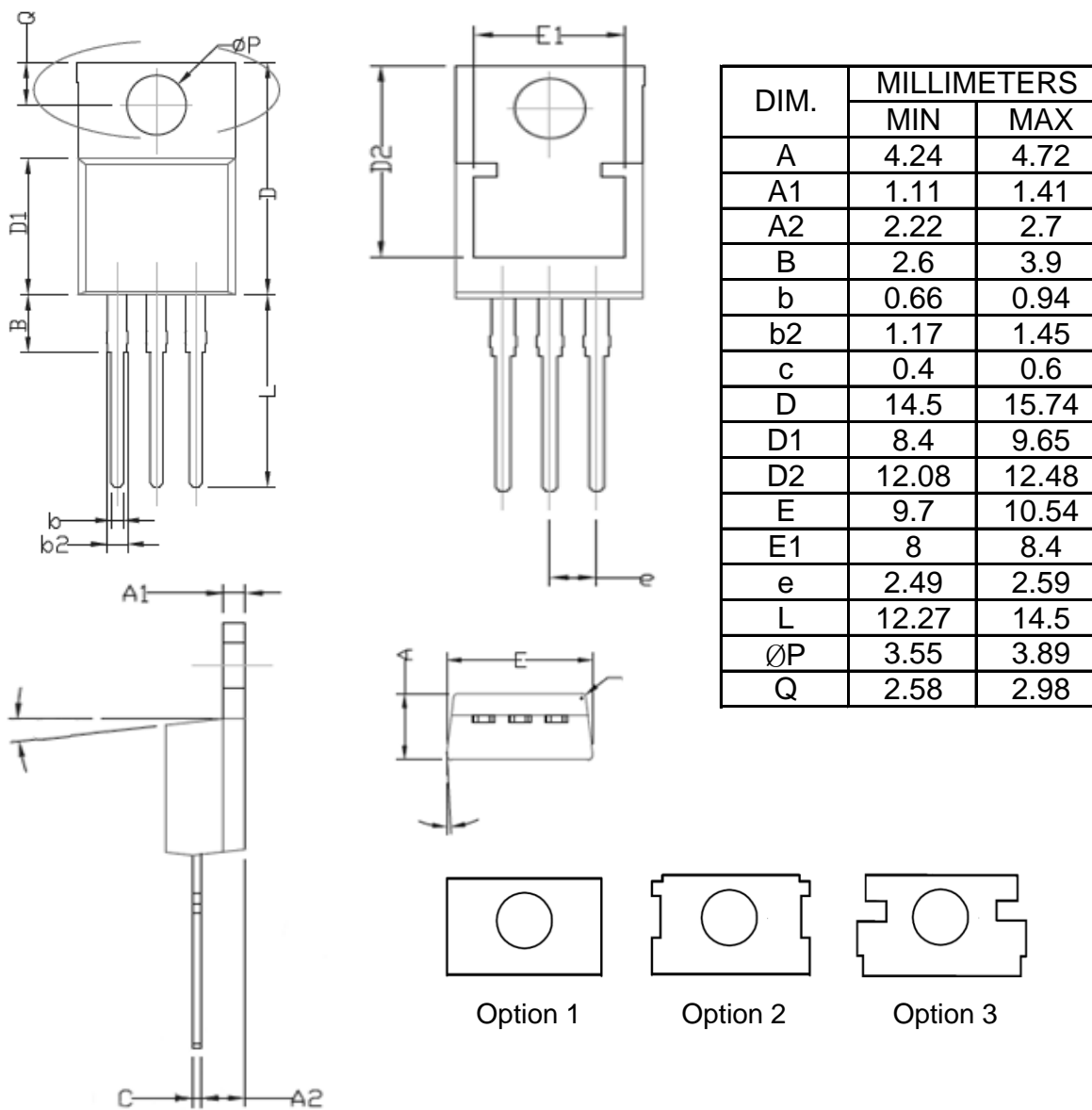


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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