P-Channel 250-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

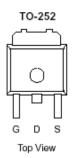
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
-250	1000 @ V _{GS} = -10V	-5.8	
-230	1040 @ V _{GS} = -6V	-5.7	

in





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V _{DS}	-250	V	
Gate-Source Voltage		V _{GS}	±20	v	
Continuous Drain Current ^a	T _C =25°C	I _D	-5.8	А	
Pulsed Drain Current ^b		I _{DM}	-40	~	
Continuous Source Current (Diode Conduction) ^a		۱ _s	-5	А	
Power Dissipation ^a	T _C =25°C	PD	50	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	R_{\thetaJA}	40	°C/W		
Maximum Junction-to-Case	R _{eJC}	3	0/11		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

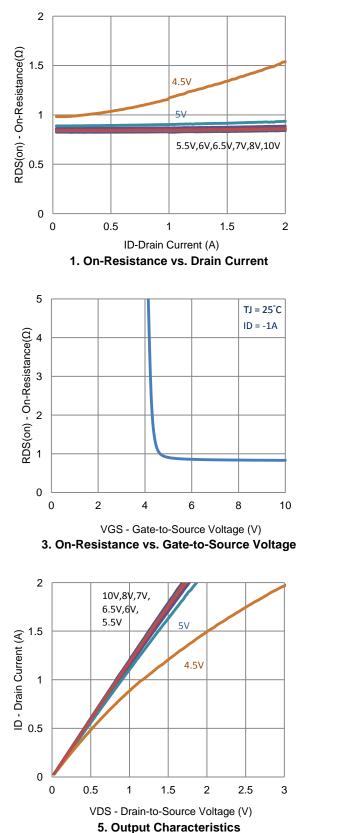
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-1	uA	
Zero Gale Voltage Drain Current	DSS	$V_{DS} = -200 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	-8.7			А	
Drain-Source On-Resistance ^a	r	V_{GS} = -10 V, I_{D} = -5.8 A			1000	mΩ	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = -6 \text{ V}, \text{ I}_{D} = -4.7 \text{ A}$			1040	11122	
Forward Transconductance ^a	g _{fs}	V _{DS} = -50 V, I _D = -5.8 A		6		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = -2.5 \text{ A}, V_{GS} = 0 \text{ V}$		-0.87		V	
		Dynamic ^b					
Total Gate Charge	Qg	V _{DS} = -100 V, V _{GS} = -6 V,		18			
Gate-Source Charge	Q _{gs}	$V_{DS} = -100 \text{ V}, \text{ V}_{GS} = -0 \text{ V},$ $I_{D} = -1 \text{ A}$		6.3		nC	
Gate-Drain Charge	Q_{gd}	1 <u>0</u> – 177		7.6			
Turn-On Delay Time	t _{d(on)}	V _{DS} = -100 V, R ₁ = 100 Ω,		10			
Rise Time	t _r	$V_{DS} = -100 V, K_L = 100 \Omega_2,$ $I_D = -1 A,$		5		200	
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = -10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		22		ns	
Fall Time	t _f	$V_{\text{GEN}} = -10$ V, $R_{\text{GEN}} = 0.02$		52			
Input Capacitance	C _{iss}	1		757			
Output Capacitance	C _{oss}	V_{DS} = -50 V, V_{GS} = 0 V, f = 1 Mhz		27		pF	
Reverse Transfer Capacitance	C _{rss}]		23			

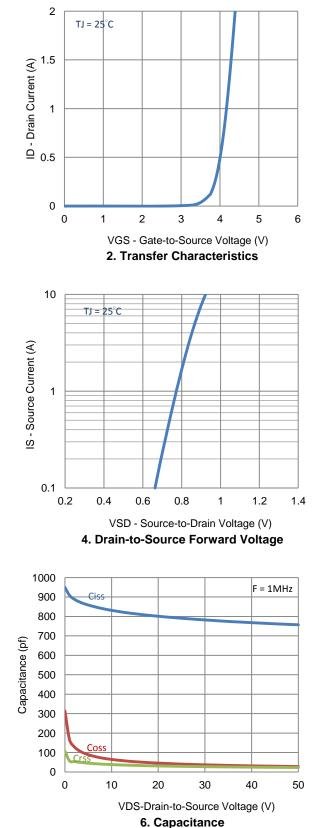
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

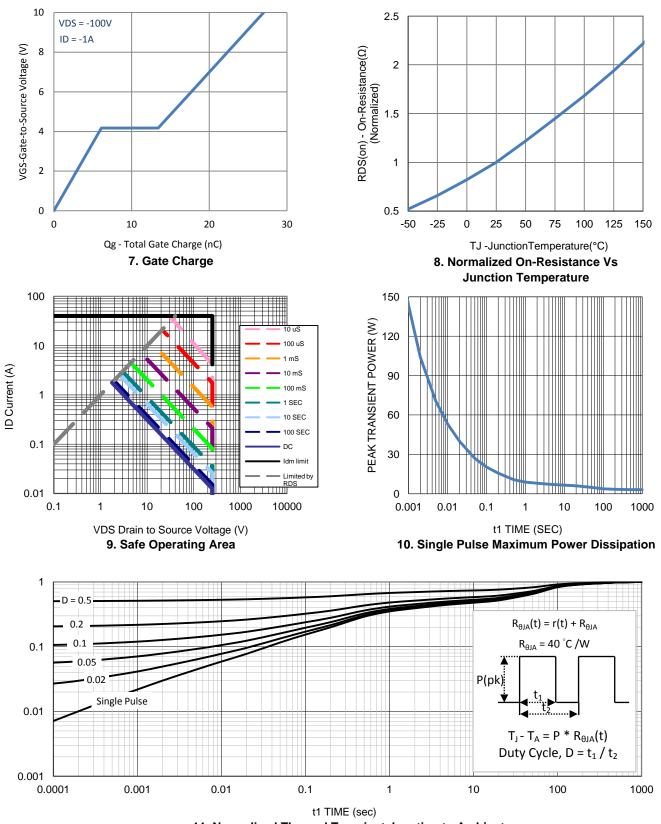
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Typical Electrical Characteristics



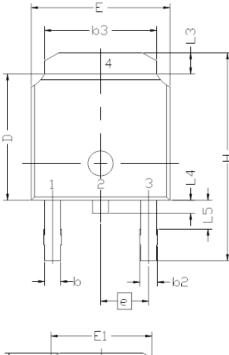
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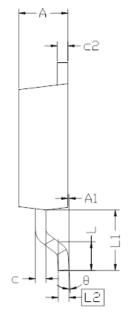


Typical Electrical Characteristics

11. Normalized Thermal Transient Junction to Ambient

Package Information





SINGLE ROW NEW	

SYMBOL	DIMENS: MIN	IONAL F Nom	REQMTS MAX
F	6.40	6.60	6.731
L	1.40	1.52	1.77
L1	2	.743 R	
L2	0.	.508 BS	C
L3	0.89		1.27
L4	0.64		1.01
L5			
D	6.00	6.10	6,223
Н	9.40	10.00	10.40
b	0.64	0.76	0,88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e	2.	286 BS	
A	2.20	2.30	2.38
A1	0		0.127
C C2	0.45	0.50	0.60
	0.45	0.50	0.58
D1	5.30		
E1	4.40		
θ	0°		10°

Note:

1. All Dimension Are In mm.

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- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.