1200-V Fast Recovery Diode

Key Features:

- Ultra-fast recovery behavior
- Easy paralleling
- Positive temperature coefficient
- Small switching losses

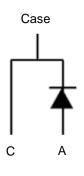
Typical Applications:

- · Soft switching topologies
- · Secondary side rectification

PRODUCT SUMMARY				
V _{BR} (V)	$V_F(V)$	I _{F(AV)} (A)		
1200	2.9	40		







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Cathode-Anode Voltage		V_{BR}	1200	V	
Diode Forward Current a	T _C =25°C	I _{F(AV)}	40	Α	
Single Pulse Forward Current ^b	T _C =25°C	I _{FSM}	105	Α	
Joule Integral		i ² t	80	A²·s	
Power Dissipation ^a	T _C =25°C	P_D	125	W	
Storage Temperature Range		T _{stg}	-55 to 150	°C	
Operating Junction Temperature		T_J	-40 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Maximum	Units	
Maximum Junction-to-Ambient °	$R_{\theta JA}$	40	°C/W	
Maximum Junction-to-Case	$R_{ heta JC}$	1	C/VV	

1

Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

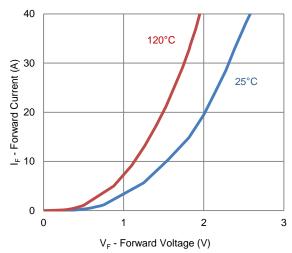
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
	V _F	I _F = 40 A		2.6		V
Forward Voltage ^a	v _F	$I_F = 40 \text{ A}, T_J = 120^{\circ}\text{C}$		2		
Repetitive Peak Reverse Voltage	V_{RRM}	V_{RRM} $T_J = -40$ °C to 120°C				V
Junction Capacitance	CJ	$V_R = 200 \text{ V}, V_{\text{sine}} = 0.6 \text{ V}_{\text{eff}},$ f = 100 kHz		59		pF
		V _R = 1200 V			100	uA
Reverse Leakage Current	I _R	V _R = 1200 V, T _J = 120°C			600	uA
Dynamic ^b						
Reverse Recovery Time	T _{rr}	I _F = 40 A, dl/dt = 100 A/us,		318		ns
Reverse Recovery Charge	Q_{rr}	$T_{\rm I} = 40 \text{ A}$, divid = 100 A/ds, $T_{\rm I} = 25^{\circ}\text{C}$		994		nC
Peak Recovery Current	I _{RRM}	1j = 25 O		4.7		Α
Reverse Recovery Time	T_{rr}	$I_F = 40 \text{ A}, dI/dt = 100 \text{ A/us},$		553		ns
Reverse Recovery Charge	Q_{rr}	$T_{ij} = 40 \text{ A}$, divid = 100 A/ds, $T_{ij} = 120 ^{\circ}\text{C}$		2405		nC
Peak Recovery Current	I _{RRM}	17 = 120 0		6.4		Α
Reverse Recovery Time	T_{rr}	$I_F = 40 \text{ A}, \text{ dI/dt} = 500 \text{ A/us},$		178		ns
Reverse Recovery Charge	Q_{rr}	$T_{\rm I} = 40 \text{ A}$, $\text{divid} = 500 \text{ A/ds}$, $T_{\rm I} = 25^{\circ}\text{C}$		2250		nC
Peak Recovery Current	I _{RRM}	1 J = 20 O		19.5		Α
Reverse Recovery Time	T _{rr}	L = 40 A dl/dt = 500 A/vs		303		ns
Reverse Recovery Charge	Q _{rr}	$I_F = 40 \text{ A}, \text{ dI/dt} = 500 \text{ A/us},$ $T_{.I} = 120^{\circ}\text{C}$	_	5310		nC
Peak Recovery Current	I _{RRM}	1, = 120 0		26.2		Α

Notes

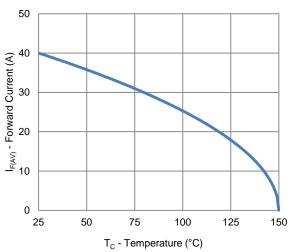
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

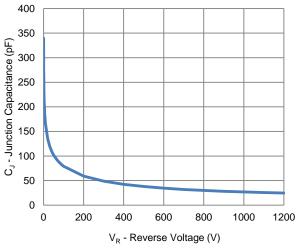
Typical Electrical Characteristics



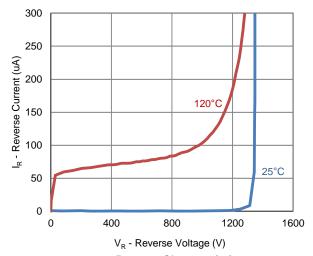
1. Forward Characteristics



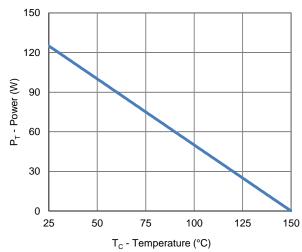
3. Current Derating



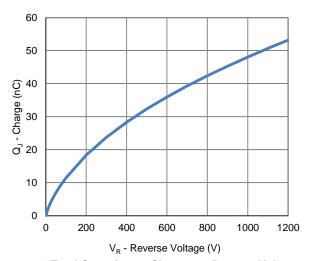
5. Junction Capacitance vs. Reverse Voltage



2. Reverse Characteristics

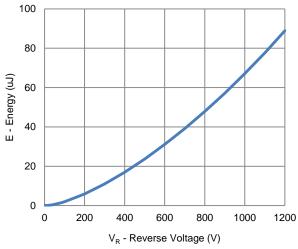


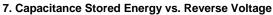
4. Power Derating

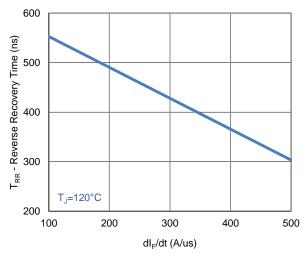


6. Total Capacitance Charge vs. Reverse Voltage

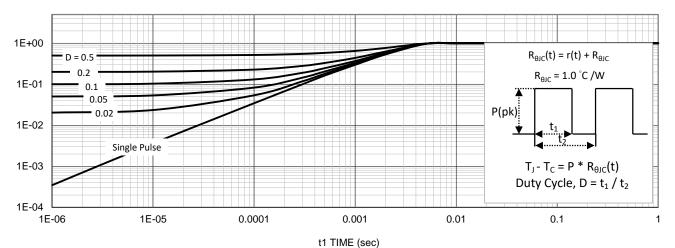
Typical Electrical Characteristics





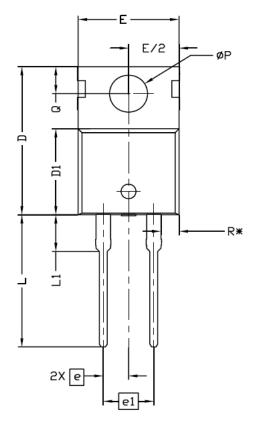


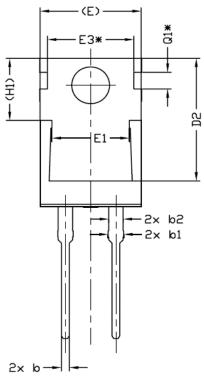
8. Reverse Recovery Time vs. dl_F/dt

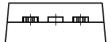


9. Thermal Transient Junction to Ambient

Package Information







SYMBOL	DIMENSIONS				
STIVIBOL	MIN.	NOM.	MAX.		
Α	4.24	4,44	4.64		
A1	1.15	1.27	1.40		
A2	2.30	2.48	2.70		
р	0.70	0.80	0.90		
b1	1.20	1.55	1.75		
b2	1.20	1.45	1.70		
С	0.40	0.50	0.60		
D	14.70	15.37	16.00		
D1	8.82	8.92	9.02		
D2	12.43	12.73	12.83		
Е	9.96	10.16	10.36		
E1	6.86	7.77	8,89		
E3*	8.70REF.				
е	2,54BSC				
e1	5.08BSC				
H1	6.30	6.45	6.60		
L	13.47	13.72	13.97		
L1	3.60	3.80	4.00		
ØP	3.75	3.84	3.93		
Ø	2.60	2.80	3,00		
Q1*	1,73REF.				
R*	1.82REF.				